



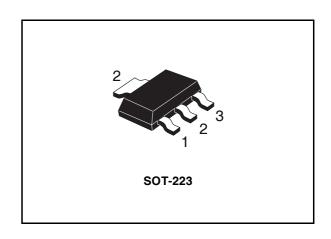
Low power NPN Transistor

General features

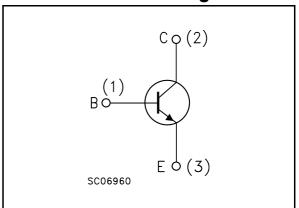
- Silicon epitaxial planar NPN medium voltage transistor
- SOT-223 plastic package for surface mounting circuits
- Available in tape & reel packing
- In compliance with the 2002/93/EC European Directive
- The PNP complementary type is BCP53-16

Applications

- Medium voltage load switch transistor
- Output stage for audio amplifiers circuits
- Automotive post-voltage regulation



Internal schematic diagram



Order codes

Part Number	Marking	Package	Packing
BCP56-16	BCP5616	SOT-223	Tape & reel

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BCP56-16 Electrical ratings

1 Electrical ratings

Table 1. Absolute maximum rating

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-base voltage (I _E = 0)	100	V
V _{CEO}	Collector-emitter voltage (I _B = 0)	80	V
V _{EBO}	Emitter-base voltage (I _C = 0)	5	V
I _C	Collector current	1	Α
I _{CM}	Collector peak current (t _P < 5ms)	1.5	Α
I _B	Base current	0.1	Α
I _{BM}	Base peak current (t _P < 5ms)	0.2	Α
P _{tot}	Total dissipation at T _{amb} = 25°C	1.6	W
T _{stg}	Storage temperature	-65 to 150	°C
TJ	Max. operating junction temperature	150	°C

Table 2. Thermal data

Symbol	Parameter	Value	Unit
R _{thj-amb}	Thermal resistance junction-ambient (1) max	78	°C/W

^{1.} Device mounted on PCB area of 1 cm².

Electrical characteristics BCP56-16

2 Electrical characteristics

 $(T_{case} = 25^{\circ}C \text{ unless otherwise specified})$

Table 3. Electrical characteristics

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
I _{CBO}	Collector cut-off current (I _E =0)	$V_{CB} = 30V$ $V_{CB} = 30V$; $T_j = 125^{\circ}0$	C		100 10	nA μA
V _{(BR)CEO} (2)	Collector-emitter breakdown voltage (I _B =0)	I _C = 20mA	80			V
V _{(BR)CBO}	Collector-base breakdown voltage (I _E =0)	I _C = 100μA	100			V
V _{(BR)EBO}	Emitter-base breakdown voltage (I _C =0)	I _E = 10μA	5			V
V _{CE(sat)} (2)	Collector-emitter saturation voltage	$I_C = 500 \text{mA}$ $I_B = 50 \text{m}$	nA		0.5	V
V _{BE(on)} (2)	Base-emitter on voltage	$I_C = 500 \text{mA}$ $V_{CE} = 2$	/		1	V
h _{FE} ⁽²⁾	DC current gain	$I_C = 5mA$ $V_{CE} = 2V_{CE}$ $I_C = 150mA$ $V_{CE} = 2V_{CE}$ $I_C = 500mA$ $V_{CE} = 2V_{CE}$	/ 100		250	

Note (2) Pulsed duration = 300 μ s, duty cycle \leq 1.5%

2.1 Electrical characteristics (curves)

Figure 1. DC current gain

T_J = 125 °C

T_J = -40 °C

T_J = 25 °C

V_{CE} = 2V

Figure 2. Collector-emitter saturation voltage

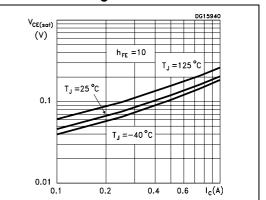
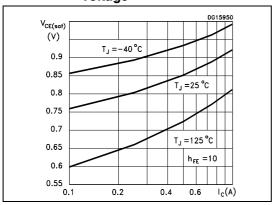


Figure 3. Base-emitter saturation voltage



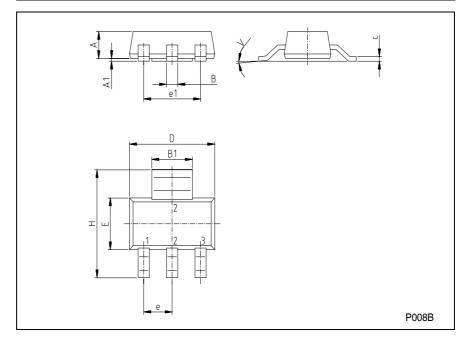
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3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com

SOT-223 MECHANICAL DATA

DIM.	mm			inch		
Dim.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
Α			1.80			0.071
В	0.60	0.70	0.80	0.024	0.027	0.031
B1	2.90	3.00	3.10	0.114	0.118	0.122
С	0.24	0.26	0.32	0.009	0.010	0.013
D	6.30	6.50	6.70	0.248	0.256	0.264
е		2.30			0.090	
e1		4.60			0.181	
E	3.30	3.50	3.70	0.130	0.138	0.146
Н	6.70	7.00	7.30	0.264	0.276	0.287
V			10°			10°
A1		0.02				



Revision history BCP56-16

4 Revision history

Table 4. Revision history

Date	Revision	Changes	
02-Sep-2004	1	Initial release.	
26-May-2006	2	New template	
14-Jun-2006	3	Three curves has been added on page 5.	

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