



## PRODUCT BULLETIN

Generic Copy

**ISSUE DATE:** 28-Jan-2014  
**NOTIFICATION:** 15997  
**TITLE:** I.MX6DQ Rev 1.3 Production Release  
**EFFECTIVE DATE:** 29-Jan-2014

### DEVICE(S)

MPN
MCIMX6D4AVT08A
MCIMX6D4AVT08AC
MCIMX6D4AVT08ACR
MCIMX6D4AVT08AD
MCIMX6D4AVT08ADR
MCIMX6D4AVT10A
MCIMX6D4AVT10AC
MCIMX6D4AVT10ACR
MCIMX6D4AVT10AD
MCIMX6D4AVT10ADR
MCIMX6D5EYM10A
MCIMX6D5EYM10AC
MCIMX6D5EYM10ACR
MCIMX6D5EYM10AD
MCIMX6D5EYM10ADR
MCIMX6D5EYM10C
MCIMX6D5EYM12A
MCIMX6D5EYM12AC
MCIMX6D5EYM12AD
MCIMX6D5EYM12C
MCIMX6D6AVT08A
MCIMX6D6AVT08AC
MCIMX6D6AVT08ACR
MCIMX6D6AVT08AD
MCIMX6D6AVT08ADR
MCIMX6D6AVT10A
MCIMX6D6AVT10AC
MCIMX6D6AVT10ACR
MCIMX6D6AVT10AD

MCIMX6D6AVT10ADR
MCIMX6D7CVT08A
MCIMX6D7CVT08AC
MCIMX6D7CVT08AD
MCIMX6Q4AVT08A
MCIMX6Q4AVT08AC
MCIMX6Q4AVT08ACR
MCIMX6Q4AVT08AD
MCIMX6Q4AVT08ADR
MCIMX6Q4AVT10A
MCIMX6Q4AVT10AC
MCIMX6Q4AVT10ACR
MCIMX6Q4AVT10AD
MCIMX6Q4AVT10ADR
MCIMX6Q5EYM10A
MCIMX6Q5EYM10AC
MCIMX6Q5EYM10ACR
MCIMX6Q5EYM10AD
MCIMX6Q5EYM10ADR
MCIMX6Q5EYM10C
MCIMX6Q5EYM12A
MCIMX6Q5EYM12AC
MCIMX6Q5EYM12AD
MCIMX6Q5EYM12C
MCIMX6Q6AVT08A
MCIMX6Q6AVT08AC
MCIMX6Q6AVT08ACR
MCIMX6Q6AVT08AD
MCIMX6Q6AVT08ADR
MCIMX6Q6AVT10A
MCIMX6Q6AVT10AC
MCIMX6Q6AVT10ACR
MCIMX6Q6AVT10AD
MCIMX6Q6AVT10ADR
MCIMX6Q7CVT08A
MCIMX6Q7CVT08AC
MCIMX6Q7CVT08AD
SC6Q888YM10AC

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## **AFFECTED CHANGE CATEGORIES**

- DESIGN CHANGE

### **DESCRIPTION OF CHANGE**

Freescale is releasing a new revision of i.MX6Q/D silicon to fix specific errata that are listed below.

ERR006282 - ROM code uses nonreset PFDs to generate clocks.

ERR007117 - When booting from NAND flash, enfc\_clk\_root clock is not gated off.

ERR007220 - NAND boot may fail due to incorrect Hamming checking implementation.

The new errata document references these changes and can be found online at:

<http://cache.freescale.com/files/32bit/doc/errata/IMX6DQCE.pdf>

### **REASON FOR CHANGE**

This new revision fixes errata that had limited or no workarounds available.

### **ANTICIPATED IMPACT OF PRODUCT CHANGE(FORM, FIT, FUNCTION, OR RELIABILITY)**

The change allows for added functionality of the device. The new revision meets the same high Freescale reliability standards met by the previous revision and will be fully backwards compatible.

The revision has been fully validated. All of the updates are working as intended and no new issues have been introduced.

Both the previous revision and new revision are available for purchase. The previous revision is denoted by a "C" at the end of the ordering part number. The new revision is denoted by a "D" at the end of the ordering part number.

The marking on the new revision contains one major change; the addition of the maskset number. An additional line has been added that will have the "5N55D" maskset number.

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#### **NOTE:**

THE CHANGE(S) SPECIFIED IN THIS NOTIFICATION WILL BE IMPLEMENTED ON THE EFFECTIVE DATE LISTED ABOVE. To request further data or inquire about the notification, please enter a [Service Request](#)

For sample inquiries - please go to [www.freescale.com](http://www.freescale.com)

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**QUAL DATA AVAILABILITY DATE:** 19-Dec-2013

**QUALIFICATION STATUS:** COMPLETED

**QUALIFICATION PLAN:**

N/A

**RELIABILITY DATA SUMMARY:**

N/A

**ELECTRICAL CHARACTERISTIC SUMMARY:**

N/A

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**CHANGED PART IDENTIFICATION:**

N/A

**SAMPLE AVAILABILITY DATE:** 18-Dec-2013

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**ATTACHMENT(S):**

External attachment(s) FOR this notification can be viewed AT:  
[15997\\_IMX6DQCE.pdf](https://www.freescale.com/files/shared/doc/pcn/PCN15997.htm)

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