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NTE74LS153 Integrated Circuit TTL – Dual 4–Line–to–1–Line Data Selector/Multiplexer

Description:

The NTE74LS153 is a dual 4–line–to–1–line data selector/multiplexer in a 16–Lead plastic DIP type package that contains inverters and drivers to supply fully complementary, on–chip, binary decoding data selection to the AND–OR gates. Separate strobe inputs are provided for each of the two four–line sections.

Features:

- Permits Multiplexing from N Lines to One Line
- Performs Parallel–to–Serial Conversion
- Strobe (Enable) Line Provided fo cascading (N Lines to n Lines)
- High–Fan–Out, Low–Impedance, Totem–Pole Outputs
- Compatible with most TTL Circuits

Absolute Maximum Ratings: (Note 1)

Supply Voltage, V_{CC}	7V
DC Input Voltage, V_{IN}	7V
Power Dissipation, P_D	31mW
Operating Temperature Range, T_A	0°C to +70°C
Storage Temperature Range, T_{stg}	–65°C to +150°C

Note 1. Unless otherwise specified, all voltages are referenced to GND.

Recommended Operating Conditions:

Parameter	Symbol	Min	Typ	Max	Unit
Supply Voltage	V_{CC}	4.75	5.0	5.25	V
High–Level Input Voltage	V_{IH}	2	–	–	V
Low–Level Input Voltage	V_{IL}	–	–	0.8	V
High–Level Output Current	I_{OH}	–	–	–0.4	mA
Low–Level Output Current	I_{OL}	–	–	8	mA
Operating Temperature Range	T_A	0	–	+70	°C

Electrical Characteristics: (Note 2, Note 3)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit	
Input Clamp Voltage	V_{IK}	$V_{CC} = \text{MIN}, I_I = -18\text{mA}$	-	-	-1.5	V	
High Level Output Voltage	V_{OH}	$V_{CC} = \text{MIN}, V_{IH} = 2\text{V}, V_{IL} = \text{MAX}, I_{OH} = -0.4\text{mA}$	2.7	3.4		V	
Low Level Output Voltage	V_{OL}	$V_{CC} = \text{MIN}, V_{IH} = 2\text{V}, V_{IL} = \text{MAX}$	$I_{OL} = 4\text{mA}$	-	0.25	0.4	V
			$I_{OL} = 8\text{mA}$	-	0.35	0.5	V
Input Current	I_I	$V_{CC} = \text{MAX}, V_I = 7\text{V}$	-	-	0.1	mA	
High Level Input Current	I_{IH}	$V_{CC} = \text{MAX}, V_I = 2.7\text{V}$	-	-	20	μA	
Low Level Input Current 1G, 2G	I_{IL}	$V_{CC} = \text{MAX}, V_I = 0.4\text{V}$	-	-	-0.2	mA	
All Other			-	-	-0.4	mA	
Short-Circuit Output Current	I_{OS}	$V_{CC} = \text{MAX}, \text{Note 4}$	-20	-	-100	mA	
Supply Current	I_{CCL}	$V_{CC} = \text{MAX}, \text{Outputs Open}, \text{Note 5}$	-	6.2	10	mA	

Note 2. For conditions shown as MIN or MAX, use the appropriate value specified under "Recommended Operation Conditions".

Note 3. All typical values are at $V_{CC} = 5\text{V}, T_A = +25^\circ\text{C}$.

Note 4. Not more than one output should be shorted at a time.

Note 5. I_{CCL} is measured with the outputs open and all inputs grounded.

Switching Characteristics: ($V_{CC} = 5\text{V}, T_A = +25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Propagation Delay Time (From Data Input to Y Output)	t_{PLH}	$R_L = 2\text{k}\Omega, C_L = 15\text{pF}$	-	10	15	ns
	t_{PHL}		-	17	26	ns
Propagation Delay Time (From Select Input to Y Output)	t_{PLH}		-	19	29	ns
	t_{PHL}		-	25	38	ns
Propagation Delay Time (From Strobe \bar{G} Input to Y Output)	t_{PLH}		-	16	24	ns
	t_{PHL}		-	21	32	ns

Function Table:

Inputs						Strobe \bar{G}	Output Y
Select		Data					
B	A	C0	C1	C2	C3		
X	X	X	X	X	X	H	L
L	L	L	X	X	X	L	L
L	L	H	X	X	X	L	H
L	H	X	L	X	X	L	L
L	H	X	H	X	X	L	H
H	L	X	X	L	X	L	L
H	L	X	X	H	X	L	H
H	H	X	X	X	L	L	L
H	H	X	X	X	H	L	H

Select inputs A and B are common to both sections.

H = HIGH Level

L = LOW Level

X = Don't Care

Pin Connection Diagram

