#### TOSHIBA Photocoupler GaAs IRED & Photo-Triac

# **TLP161J**

Triac Drive
Programmable Controllers
AC-Output Module
Solid State Relay

The TOSHIBA mini flat coupler TLP161J is a small outline coupler, suitable for surface mount assembly.

The TLP161J consists of a photo triac, optically coupled to a gallium arsenide infrared emitting diode.

• Zero-voltage crossing turn-on

Peak off-state voltage: 600 V (min)

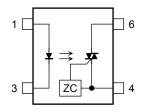
• Trigger LED current: 10 mA (max)

• On-state current: 70 mA (max)

• Isolation voltage: 2500 Vrms (min)

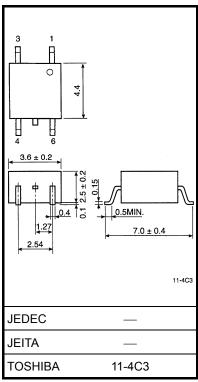
• UL recognized: UL1577, file No. E67349

#### **Pin Configurations**



- 1: Anode
- 3: Cathode
- 4: Terminal 1
- 6: Terminal 2

Unit: mm



Weight: 0.09 g (typ.)

#### **Trigger LED Current**

Classification (*)	Trigger LED			
	$V_T = 6 V$ ,	Marking of Classification		
	Min	Max		
(IFT7)	_	7	T7	
Standard	_	10	T7, Blank	

\*: Ex. (IFT7): TLP161J (IFT7)

Note: Application type name for certification test, please use standard product type name, i.e. TLP161J (IFT7): TLP161J

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics			Symbol	Rating	Unit	
LED	Forward current		lF	50	mA	
	Forward current de (Ta ≥ 53°C)	rating	ΔI <sub>F</sub> /°C	-0.7	mA/°C	
	Peak forward curre (100 μs pulse, 100		I <sub>FP</sub>	1	А	
	Reverse voltage		V <sub>R</sub>	5	V	
	Junction temperatu	ire	Tj	125	°C	
	Off-state output ter	minal voltage	$V_{DRM}$	600	V	
	On-state RMS current	Ta = 25°C	l=	70	mA	
		Ta = 70°C	l <sub>T(RMS)</sub>	40	IIIA	
Detector	On-state current de (Ta ≥ 25°C)	erating	ΔI <sub>T</sub> /°C	-0.67	mA/°C	
	Peak on-state curre (100 μs pulse, 120		I <sub>TP</sub>	2	А	
	Peak nonrepetitive (PW = 10 ms, DC =		I <sub>TSM</sub>	I <sub>TSM</sub> 1.2		
	Junction temperatu	ire	Tj	115	°C	
Storage temperature range			T <sub>stg</sub> –55 to 125		°C	
Operating temperature range			T <sub>opr</sub>	-40 to 100	°C	
Lead soldering temperature (10 s)			T <sub>sol</sub>	260	°C	
Isolation voltage (AC, 1 min., R.H. ≤ 60%) (Note)			BVS	2500	Vrms	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note: Device considered a two terminal device: Pins 1 and 3 shorted together and pins 4 and 6 shorted together.

### **Recommended Operating Conditions**

Characteristics	Symbol	Min	Тур.	Max	Unit
Supply voltage	$V_{AC}$	_	_	240	Vac
Forward current	lF	15	20	25	mA
Peak on-state current	I <sub>TP</sub>	_	_	1	Α
Operating temperature	T <sub>opr</sub>	-25	_	85	°C

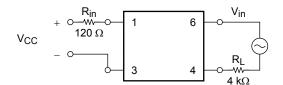
Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

## **Individual Electrical Characteristics (Ta = 25°C)**

	Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
LED	Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 10 mA	1.0	1.15	1.3	V
	Reverse current	I <sub>R</sub>	V <sub>R</sub> = 5 V	_	_	10	μА
	Capacitance	C <sub>T</sub>	V = 0, f = 1 MHz	_	30	_	pF
Detector	Peak off-state current	I <sub>DRM</sub>	V <sub>DRM</sub> = 600 V	_	10	1000	nA
	Peak on-state voltage	$V_{TM}$	I <sub>TM</sub> = 70 mA	_	1.7	2.8	٧
	Holding current	lΗ	_	_	0.6	_	mA
	Critical rate of rise of off-state voltage	dv/dt	V <sub>in</sub> = 240 Vrms, Ta = 85°C (Figure 1)	200	500	_	V/μs
	Critical rate of rise of commutating voltage	dv/dt(c)	V <sub>in</sub> = 60 Vrms, I <sub>T</sub> = 15 mA (Figure 1)	_	0.2	_	V/μs

## **Coupled Electrical Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Trigger LED current	I <sub>FT</sub>	V <sub>T</sub> = 6 V	_	5	10	mA
Inhibit voltage	V <sub>IH</sub>	I <sub>F</sub> = Rated I <sub>FT</sub>	_	_	50	V
Leakage in inhibited state	I <sub>IH</sub>	I <sub>F</sub> = Rated I <sub>FT</sub> , V <sub>T</sub> = Rated V <sub>DRM</sub>	_	200	600	μА
Capacitance (input to output)	Cs	V <sub>S</sub> = 0, f = 1 MHz	_	0.8	_	pF
Isolation resistance	R <sub>S</sub>	V <sub>S</sub> = 500 V, R.H. ≤ 60%	1 × 10 <sup>12</sup>	10 <sup>14</sup>	_	Ω
	BVS	AC, 1 minute	2500	_	_	Vrms
Isolation voltage		AC, 1 s, in oil	_	5000	_	
		DC, 1 minute, in oil	_	5000	_	Vdc



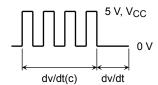
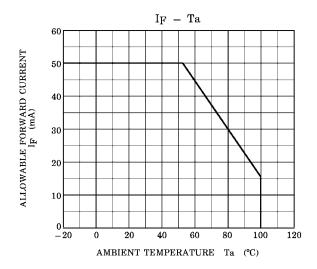
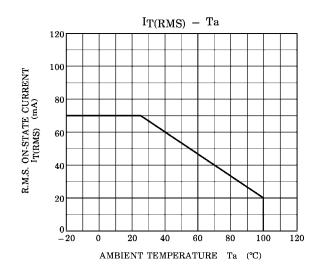
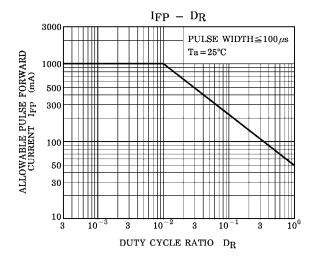
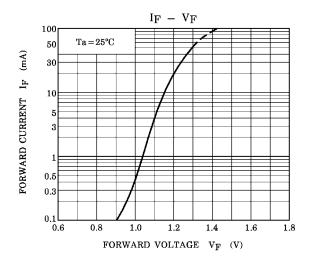


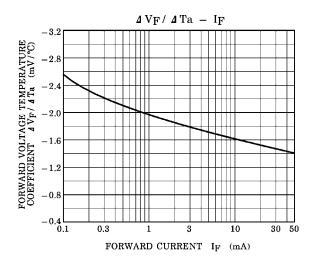
Figure 1 dv/dt Test Circuit

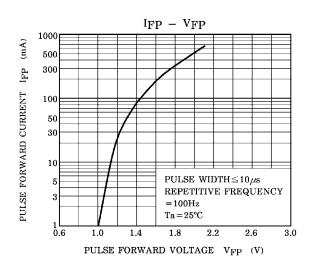


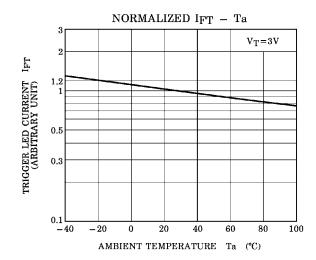


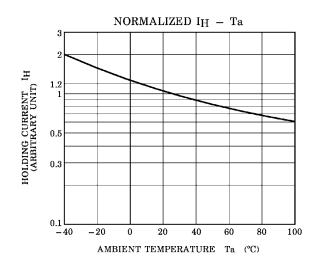


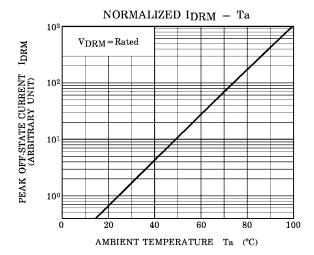


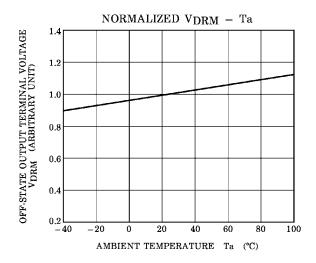


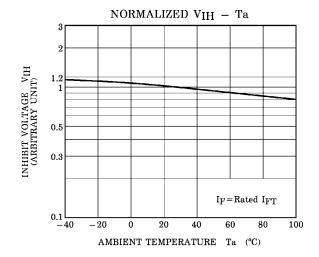


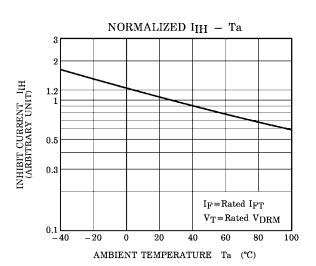












5

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  - In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc.
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