



DMN3016LFDF

Product Summary

BV _{DSS}	Rds(on) Max	I _D Max T _A = +25°C
	12mΩ @ V _{GS} = 10V	10A
30V	16mΩ @ V _{GS} = 4.5V	8.5A

Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

U-DFN2020-6 (Type F)

- Battery Management Application
- Power Management Functions
- DC-DC Converters

N-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

- 0.6mm Profile Ideal for Low Profile Applications
- PCB Footprint of 4mm²
- Low Gate Threshold Voltage
- Low On-Resistance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

https://www.diodes.com/products/automotive/automotiveproducts/.

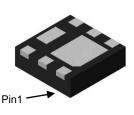
 This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability. https://www.diodes.com/quality/product-definitions/

Mechanical Data

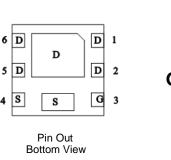
- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @4
- Weight: 0.0065 grams (Approximate)

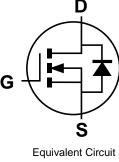


Top View



Bottom View





Ordering Information (Note 4)

	Part Number	Case	Packaging				
	DMN3016LFDF-7	U-DFN2020-6 (Type F)	3,000/Tape & Reel				
	DMN3016LFDF-13	U-DFN2020-6 (Type F)	10,000/Tape & Reel				
Notes:	Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.						

No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Haloger- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information

Site 1



NZ = Product Type Marking Code YM = Date Code Marking Y = Year (ex: G = 2019) M = Month (ex: 9 = September)

Date Code Key

Year	2017	20	18	2019	2020	20	21	2022	2023	20)24	2025
Code	E	F	=	G	Н		I	J	K		L	М
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

Site 2



NZ = Product Type Marking Code YWX = Date Code Marking

Y = Year (ex: 9 = 2019)

W = Week (ex: a = Week 27; z Represents Week 52 and 53) X = Internal Code (ex: U = Monday)

Date Code Key								
Year	2019	2020	2021	2022	2023	2024	2025	2026
Code	9	0	1	2	3	4	5	6

Week	1-26	27-52	53
Code	A-Z	a-z	Z

Internal Code	Sun	Mon	Tue	Wed	Thu	Fri	Sat
Code	Т	U	V	W	Х	Y	Z



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			VDSS	30	V
Gate-Source Voltage			Vgss	±20	V
	Steady	T _A = +25°C	- lo -	10	^
Continuous Drain Current (Note 6) V _{GS} = 10V	State	T _A = +70°C		8	A
	1.10-	T _A = +25°C	- I _D	12	•
	t<10s	T _A = +70°C		9	A
Maximum Continuous Body Diode Forward Curre	nt (Note 6)		ls	2.5	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1	%)		I _{DM}	50	A
Avalanche Current (Note 7) L = 0.1mH			lar	22	A
Avalanche Energy (Note 7) L = 0.1mH			Ear	24	mJ

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

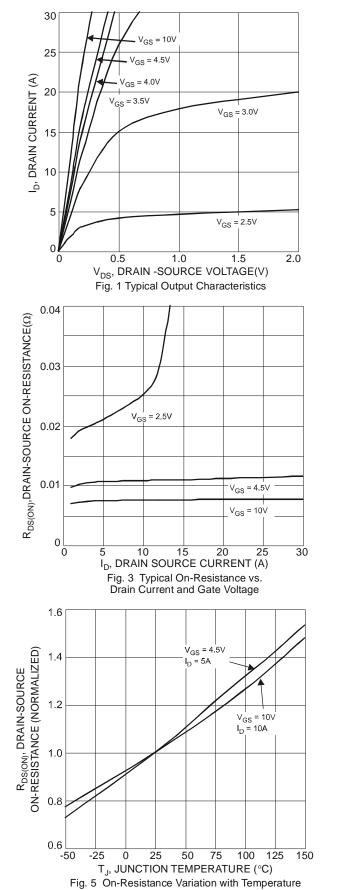
Characteristic		Symbol	Value	Unit	
Total Dawar Dissinction (Nata 5)	T _A = +25°C	D -	0.73	10/	
Total Power Dissipation (Note 5)	$T_A = +70^{\circ}C$	PD	0.47	W	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Reja	174	°C/W	
Thermal Resistance, Junction to Ambient (Note 5)	t<10s	κθΊΑ	121		
Total Power Dissipation (Note 6)	T _A = +25°C	PD	2.02	W	
Total Power Dissipation (Note 6)	$T_A = +70^{\circ}C$	PD	1.30	vv	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	D	66		
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	$R_{ heta JA}$	42	°C/W	
Thermal Resistance, Junction to Case (Note 6)	Steady State	R _{θJC}	11.6		
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C	

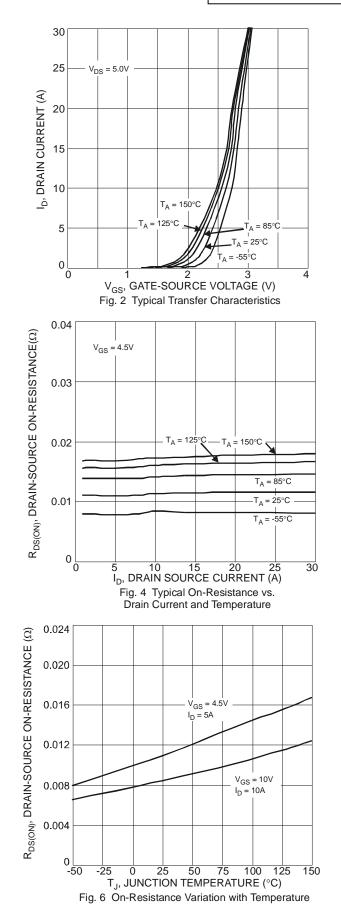
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	30	—	—	V	$V_{GS} = 0V, I_D = 250 \mu A$	
Zero Gate Voltage Drain Current	IDSS	—	—	1	μA	$V_{DS} = 30V, V_{GS} = 0V$	
Gate-Source Leakage	lgss	—	—	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	VGS(TH)	1.4	—	2.0	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	
Static Drain-Source On-Resistance	Provent	_	8	12	mΩ	$V_{GS} = 10V, I_{D} = 11A$	
Static Dialit-Source Off-Resistance	RDS(ON)	—	12	16	1115.2	VGS = 4.5V, ID = 9A	
Diode Forward Voltage	V _{SD}	—	0.70	1.0	V	$V_{GS} = 0V, I_S = 1A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	Ciss	_	1415	-			
Output Capacitance	Coss	—	119	—	pF	$V_{DS} = 15V, V_{GS} = 0V,$ f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	82	-		1 = 1.00012	
Gate Resistance	Rg		2.6	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1.0MHz	
Total Gate Charge (V _{GS} = 4.5V)	Qg		11.3	—			
Total Gate Charge (V _{GS} = 10V)	Qg	_	25.1	—	nC		
Gate-Source Charge	Qgs		3.5	—	nc	$V_{DS} = 15V, I_D = 12A$	
Gate-Drain Charge	Q _{gd}	—	3.6	—			
Turn-On Delay Time	t _{D(ON)}	—	4.8	—			
Turn-On Rise Time	tR	—	16.5	—		V _{DD} = 15V, V _{GS} = 10V,	
Turn-Off Delay Time	tD(OFF)	—	26.1	—	ns	$R_L = 1.25\Omega, R_g = 3\Omega$	
Turn-Off Fall Time	tF	—	5.6	—			
Reverse Recovery Time	trr	—	12.3	—	ns		
Reverse Recovery Charge	Q _{RR}	—	10.4	—	nC	IF = 12A, di/dt = 500A/µs	

5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
7. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.
8. Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design. Not subject to product testing. Notes:

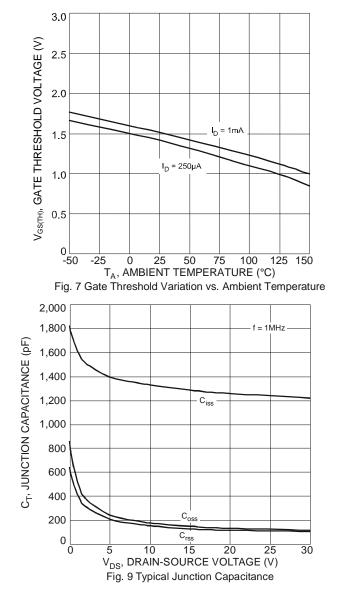


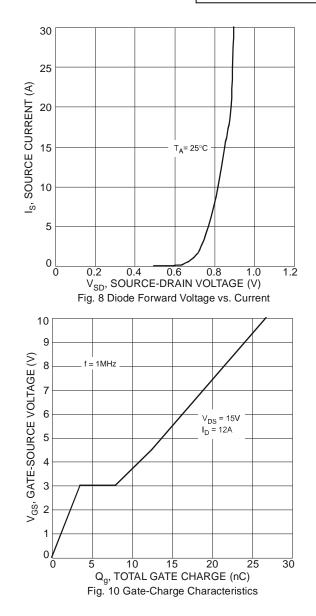




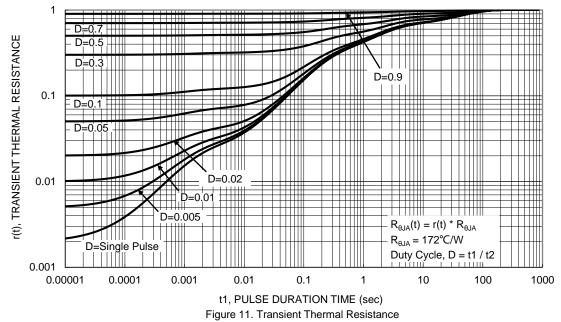










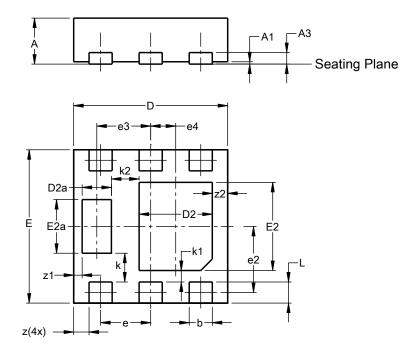




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)

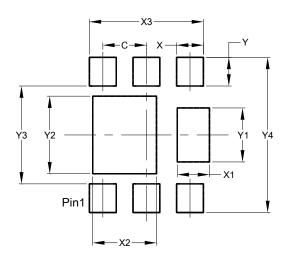


	U-DFN2020-6						
-		be F)					
Dim		Min Max Typ					
Α	0.57	0.63	0.60				
A1	0.00	0.05	0.03				
A3	-	-	0.15				
b	0.25	0.35	0.30				
D	1.95	2.05	2.00				
D2	0.85	1.05	0.95				
D2a	0.33	0.43	0.38				
E	1.95	2.05	2.00				
E2	1.05	1.25	1.15				
E2a	0.65	0.75	0.70				
е	Ť	0.65 BS	С				
e2	C).863 BS	SC				
e3	ł	0.70 BS	С				
e4	C).325 BS	SC				
k	-	0.37 BS	С				
k1	-	0.15 BS	С				
k2		0.36 BS	С				
L	0.225	0.325	0.275				
z		0.20 BS	С				
z1	C).110 BS	SC				
z2	1	0.20 BS	С				
All C	Dimens	ions in	mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)



Dimensions	Value
Dimensions	(in mm)
С	0.650
Х	0.400
X1	0.480
X2	0.950
X3	1.700
Y	0.425
Y1	0.800
Y2	1.150
Y3	1.450
Y4	2.300



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