## **MOSFET** - Power, N-Channel, SUPERFET<sup>®</sup> III, FAST

650 V, 95 mΩ, 30 A

# NTP095N65S3H

### Description

SUPERFET III MOSFET is ON Semiconductor's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This advanced technology is tailored to minimize conduction loss, provides superior switching performance, and withstand extreme dv/dt rate.

Consequently, SUPERFET III FAST MOSFET series helps minimize various power systems and improve system efficiency.

### Features

- 700 V @  $T_J = 150^{\circ}C$
- Typ.  $R_{DS(on)} = 77 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ.  $Q_g = 58 \text{ nC}$ )
- Low Effective Output Capacitance (Typ. C<sub>oss(eff.)</sub> = 522 pF)
- 100% Avalanche Tested
- These Devices are Pb-Free and are RoHS Compliant

## Applications

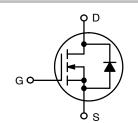
- Telecom / Server Power Supplies
- Industrial Power Supplies
- UPS / Solar



## **ON Semiconductor®**

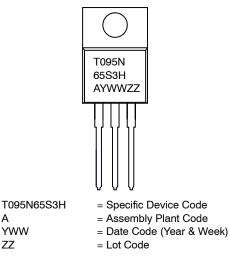
#### www.onsemi.com

V <sub>DSS</sub>	R <sub>DS(ON)</sub> MAX	I <sub>D</sub> MAX
650 V	95 mΩ @ 10 V	30 A





## MARKING DIAGRAM



### **ORDERING INFORMATION**

See detailed ordering and shipping information on page 2 of this data sheet.

А

Symbol	Paran	Value	Unit	
V <sub>DSS</sub>	Drain to Source Voltage		650	V
V <sub>GSS</sub>	Gate to Source Voltage	DC	±30	V
		AC (f > 1 Hz)	±30	V
I <sub>D</sub>	Drain Current	Continuous (T <sub>C</sub> = 25°C)	30	А
		Continuous (T <sub>C</sub> = 100°C)	18	
I <sub>DM</sub>	Drain Current Pulsed (Note 1)		84	А
E <sub>AS</sub>	Single Pulsed Avalanche Energy (Note 2)		284	mJ
I <sub>AS</sub>	Avalanche Current (Note 2)		5.5	А
E <sub>AR</sub>	Repetitive Avalanche Energy (Note 1)		2.08	mJ
dv/dt	MOSFET dv/dt		120	V/ns
	Peak Diode Recovery dv/dt (Note 3)		20	
P <sub>D</sub>	Power Dissipation	(T <sub>C</sub> = 25°C)	208	W
		Derate Above 25°C	1.67	W/°C
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		-55 to +150	°C
ΤL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 s		260	°C

#### ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub> = 25°C, Unless otherwise noted)

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. 1. Repetitive rating: pulse-width limited by maximum junction temperature. 2.  $I_{AS} = 5.5 \text{ A}$ ,  $R_G = 25 \Omega$ , starting  $T_J = 25^{\circ}\text{C}$ . 3.  $I_{SD} \le 15 \text{ A}$ , di/dt  $\le 200 \text{ A/}\mu\text{s}$ ,  $V_{DD} \le 400 \text{ V}$ , starting  $T_J = 25^{\circ}\text{C}$ .

#### **THERMAL CHARACTERISTICS**

Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	0.60	°C/W
$R_{ ext{ heta}JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	

#### PACKAGE MARKING AND ORDERING INFORMATION

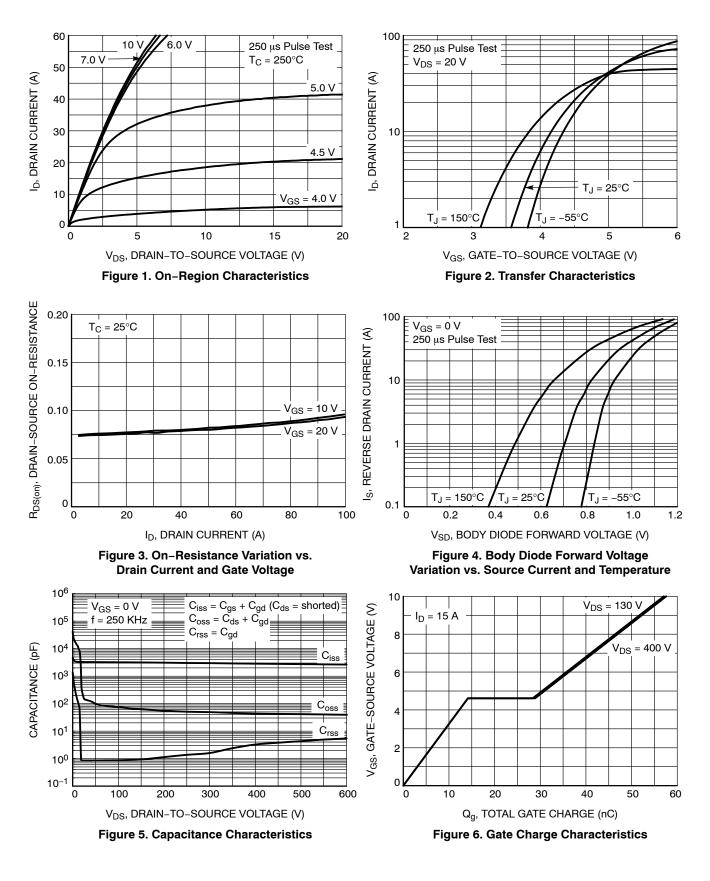
Part Number	Top Marking	Package	Shipping
NTP095N65S3H	T095N65S3H	TO-220-3LD (Pb-Free / Halogen Free)	50 Units / Tube

## ELECTRICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise noted)

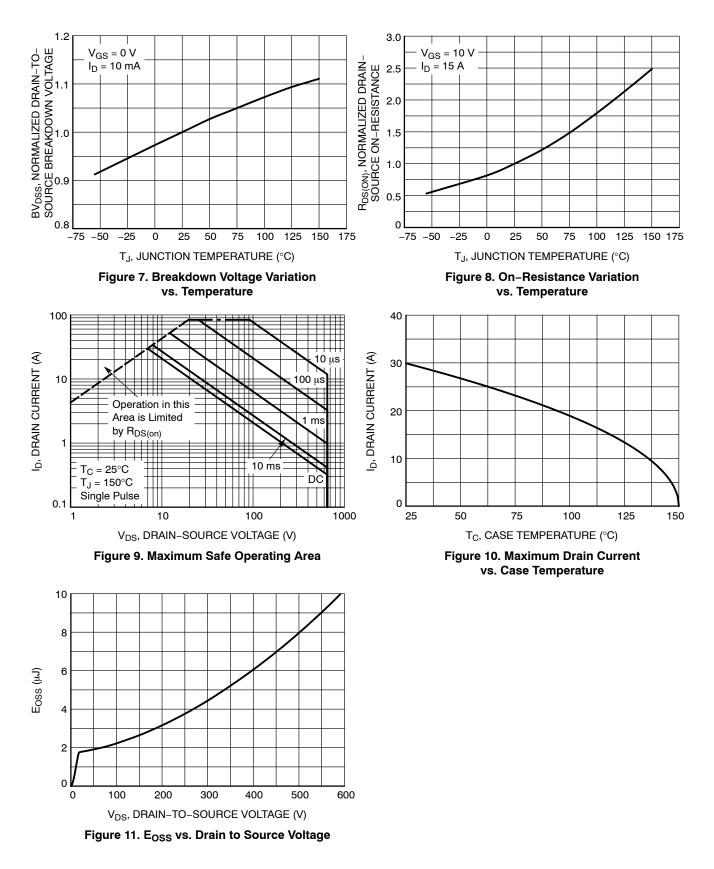
Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
OFF CHARACT	ERISTICS					
BV <sub>DSS</sub>	Drain to Source Breakdown Voltage	$V_{GS}$ = 0 V, $I_D$ = 1 mA, $T_J$ = 25°C	650			V
		$V_{GS}$ = 0 V, I <sub>D</sub> = 1 mA, T <sub>J</sub> = 150°C	700			V
$\Delta \text{BV}_{\text{DSS}} / \Delta \text{T}_{\text{J}}$	Breakdown Voltage Temperature Coefficient	$I_D$ = 10 mA, Referenced to 25°C		0.63		V/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS}$ = 650 V, $V_{GS}$ = 0 V			1	μA
		$V_{DS}$ = 520 V, $T_C$ = 125°C		1.8		
I <sub>GSS</sub>	Gate to Body Leakage Current	$V_{GS}$ = $\pm 30$ V, $V_{DS}$ = 0 V			±100	nA
ON CHARACTE	ERISTICS					
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS}$ = $V_{DS}$ , $I_D$ = 2.8 mA	2.4		4.0	V
R <sub>DS(on)</sub>	Static Drain to Source On Resistance	$V_{GS}$ = 10 V, I <sub>D</sub> = 15 A		77	95	mΩ
9 <sub>FS</sub>	Forward Transconductance	$V_{DS} = 20 \text{ V}, \text{ I}_{D} = 15 \text{ A}$		30		S
YNAMIC CHA	RACTERISTICS		-			-
C <sub>iss</sub>	Input Capacitance	$V_{DS}$ = 400 V, $V_{GS}$ = 0 V, f = 250 kHz		2833		pF
Coss	Output Capacitance			43		pF
C <sub>oss(eff.)</sub>	Effective Output Capacitance	$V_{DS}$ = 0 V to 400 V, $V_{GS}$ = 0 V		522		pF
C <sub>oss(er.)</sub>	Energy Related Output Capacitance	$V_{DS}$ = 0 V to 400 V, $V_{GS}$ = 0 V		75		pF
Q <sub>g(tot)</sub>	Total Gate Charge at 10 V			58		nC
Q <sub>gs</sub>	Gate to Source Gate Charge	V <sub>DS</sub> = 400 V, I <sub>D</sub> = 15 A, V <sub>GS</sub> = 10 V (Note 4)		14		nC
Q <sub>gd</sub>	Gate to Drain "Miller" Charge			15		nC
ESR	Equivalent Series Resistance	f = 1 MHz		1.2		Ω
WITCHING CH	IARACTERISTICS				•	
t <sub>d(on)</sub>	Turn-On Delay Time			24		ns
t <sub>r</sub>	Turn-On Rise Time	$V_{DD} = 400 \text{ V}, \text{ I}_{D} = 15 \text{ A},$		7.4		ns
t <sub>d(off)</sub>	Turn-Off Delay Time	- V <sub>GS</sub> = 10 V, R <sub>g</sub> = 4.7 Ω (Note 4)		69		ns
t <sub>f</sub>	Turn-Off Fall Time			2.4		ns
OURCE-DRAI	N DIODE CHARACTERISTICS		1	•		
۱ <sub>S</sub>	Maximum Continuous Source to Drain Diode Forward Current				30	Α
I <sub>SM</sub>	Maximum Pulsed Source to Drain Diode Forward Current				84	Α
V <sub>SD</sub>	Source to Drain Diode Forward Voltage	$V_{GS}$ = 0 V, $I_{SD}$ = 15 A			1.2	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>DD</sub> = 400 V, I <sub>SD</sub> = 15 A,		367		ns
Q <sub>rr</sub>	Reverse Recovery Charge	$dI_{F}/dt = 100 \text{ A}/\mu\text{s}$		6.3		μC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 4. Essentially independent of operating temperature typical characteristics.

#### TYPICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise noted)



### **TYPICAL CHARACTERISTICS** ( $T_C = 25^{\circ}C$ unless otherwise noted)



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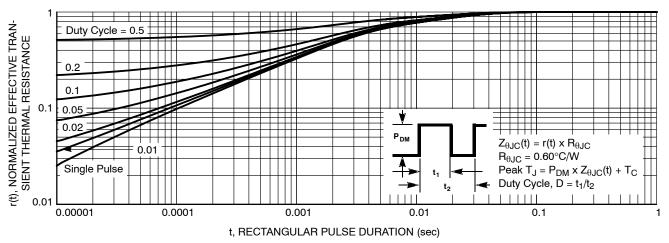


Figure 12. Transient Thermal Impedance

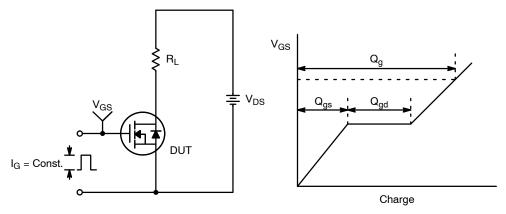


Figure 13. Gate Charge Test Circuit & Waveform

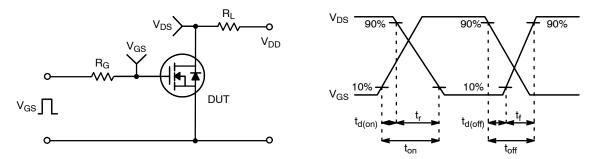


Figure 14. Resistive Switching Test Circuit & Waveforms

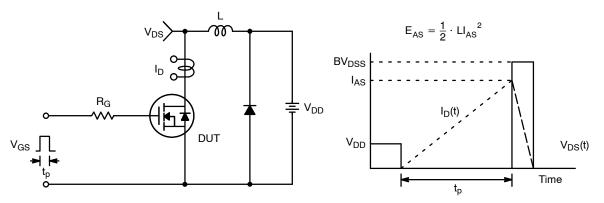


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms

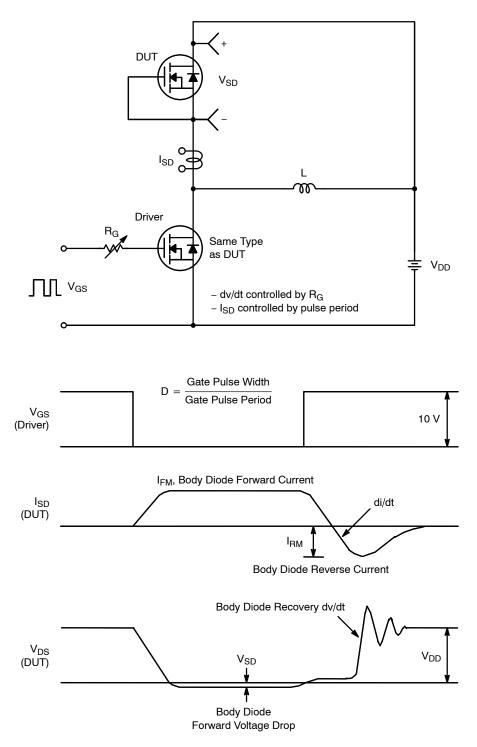
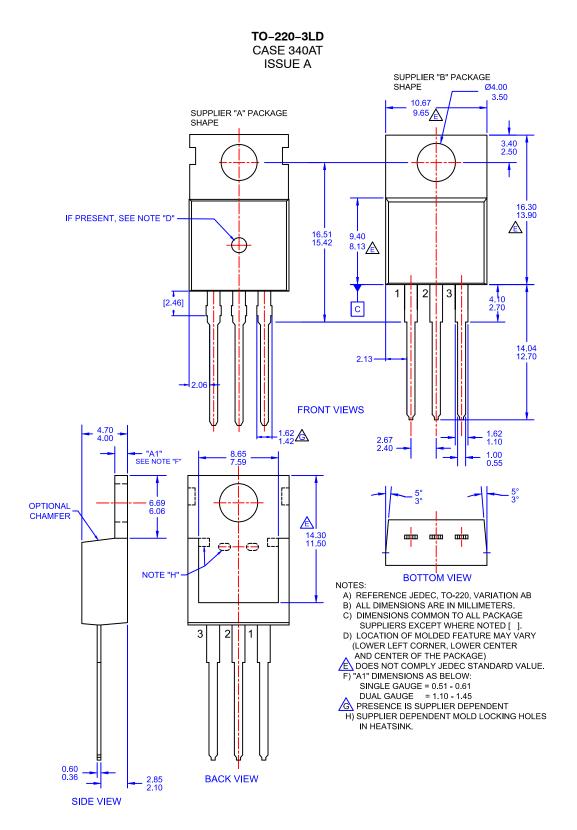


Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms

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