

Customer Information Notification

Issue Date: 03-Aug-2020 Effective Date: 04-Aug-2020 Dear Gordon Love,

Here's your personalized quality information concerning products Premier Farnell PLC purchased from NXP. For detailed information we invite you to <u>view this</u> notification online

This notice is NXP Company Proprietary.

2020070471



Management Summary

New Revisions Datasheet and User Manual for following products:

- JN5188(T), JN5189(T)
- QN9030(T), QN9090(T)
- K32W041/ K32W061

Change Category

[] Wafer Fab Process

[] Wafer Fab Materials

[] Wafer Fab Location

[] Firmware

JN5188/89 -QN9030/9090 - [] Assembly[] Product Marking [] Test [] Design Process Location [] Assembly[] Mechanical []Test [] Errata Specification Materials Process [] Assembly[] [] Test [] Electrical Location Packing/Shipping/Labeling Equipment spec./Test coverage

[X] Other - Datasheet / User Manual

New Revision

K32W041/061 Datasheet

Description

Datasheets and User Manuals for the following products were updated: JN5188, JN5188T, JN5189, JN5189T, QN9030, QN9030T, QN9090, QN9090T, K32W041, K32W061.

DS JN5188(T) - JN5189(T) v1.2 compared to initial v1.1

- Corrected typos, pin names aligned with pin descriptions.
- Updated DNL (Differential Non Linearity): ±0.85 LSB typ.
- Updated Sections "SPI-bus Flash Interface", "Deep-sleep mode" and "Hash module".
- Updated NTAG security features.
- Updated figure "Application diagram, battery powered solution".
- Updated VIL in Table 17 "IO characteristics".
- Updated IDD-typical values.

DS QN9030(T) - QM9090(T) v1.1 compared to initial v1.0

- Corrected typos, aligned Pin descriptions
- Updated Sections "Applications", "Random Number Generator", "NTAG I2C"
- Updated NTAG security features.
- Updated Figure "Application diagram/ battery powered solution".
- Updated VIL in Table 17 "IO characteristics".
- Updated the IDD-typical values.

DS K32W041/061 1.2 compared to initial 1.1

- Corrected the part numbers to be K32W061 and K32W041.
- Editorial changes.

UM11138 JN5188(T)/ JN5189(T) 1.4 compared to initial 1.3

- Added to support Thread as well as Zigbee across the whole book.

- Corrected NTAG device JN5189T/JN5188T to be NT3H2211.
- Editoral updates.
- Updated the Range of MAIN_CLK to be 12-48 MHz.
- Updated SPIFI features
- Updated the ISP usage restrictions
- Updated Sections "Memory mapping", "Antenna Diversity".

UM11141 QN9030(T), QM9090(T) 1.1 compared to initial 1.0

- Aligned device description, pin descriptions and pin names.

- Updated features of DMA engine and SPIFI, updated slave port 4.
- Editorial changes
- Updated Range of MAIN_CLK to be 12-48 MHz
- Updated SPIFI features.
- Updated ISP usage restrictions
- Corrected NTAG device in QN9030T and QN9090T to be NT3H2211.

UM11323 K32W041/061 1.1 compared to initial 1.0 - Updated ISP usage restrictions. Reason

Correction of unclarities.

Identification of Affected Products

Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No impact on form, fit, function, reliability or quality.

Data Sheet Revision

A new datasheet will be issued

Disposition of Old Products

Existing inventory will be shipped until depleted

Additional information

Affected products and sales history information: see attached file

Additional documents: view online

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please <u>contact NXP "Global Quality</u> <u>Support Team"</u>.

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

Name Kirsten Rongen Position Principal Quality Engineer, NXP BL Edge Processing e-mail address Kirsten.Rongen@nxp.com At NXP Semiconductors we are constantly striving to improve our product and processes to ensure they reach the highest possible Quality Standards.

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