ANALOG DEVICES

Product/Process Change Notice - PCN 12_0219 Rev. -

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This notice is to inform you of a change that will be made to certain ADI products (see Material Report). Any issues with this PCN or requirements to qualify the change (additional data or samples) must be sent to ADI within 30 days of publication date. ADI contact information is listed below.

PCN Title: DDR Timing Specification Correction in the ADV7181C datasheet

Publication Date: 01-Oct-2012

Effectivity Date: 01-Oct-2012 *(the earliest date that a customer could expect to receive changed material)*

Revision Description:

Initial Release

Description Of Change

ADV7181C datasheet Data Output Transition Time DDR specification correction. See attached document for details.

Reason For Change

Update datasheet to accurately reflect correct timing specification.

Impact of the change (positive or negative) on fit, form, function & reliability

There has been no change to the device silicon, there will be no impact on the device perfomance.

Summary of Supporting Information

The ADV7181C Datasheet Rev E reflects the correct timing specifications.

Supporting Documents

Attachment 1: Type: Datasheet Specification Comparison

ADI_PCN_12_0219_Rev_-_ADV7181C_TIMING_SPECIFICATION_UPDATE.pdf

For questions on this PCN, send email to the regional contacts below or contact your local ADI sales representative			
Americas: PCN_Americas@analog.com	**Europe:** PCN_Europe@analog.com	**Japan:**	PCN_Japan@analog.com
		Rest of Asia:	PCN_ROA@analog.com

Appendix A - Affected ADI Models				
Added Parts On This Revision - Product Family / Model Number (4)				
ADV7181C / ADV7181CBSTZ	ADV7181C / ADV7181CBSTZ-REEL	ADV7181C / ADV7181CWBSTZ	ADV7181C / ADV7181CWBSTZ-REEL	

Appendix B - Revision History			
Rev	Publish Date	Effectivity Date	Rev Description
Rev. -	01-Oct-2012	01-Oct-2012	Initial Release

Analog Devices, Inc. DocId:2093   Parent DocId:None   Layout Rev:7