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# Surface Mount Trench MOS Barrier Schottky Rectifiers



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### DESIGN SUPPORT TOOLS



PRIMARY CHARACTERISTICS							
I <sub>F(AV)</sub>	2.0 A						
V <sub>RRM</sub>	150 V						
I <sub>FSM</sub>	40 A						
$V_F$ at $I_F$ = 2 A ( $T_A$ = 125 °C)	0.69 V						
T <sub>J</sub> max.	175 °C						
Package	SMF (DO-219AB)						
Circuit configuration	Single						

### FEATURES

- Trench MOS Schottky technology
- Low profile package
- Ideal for automated placement
- Low forward voltage drop, low power losses
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Wave and reflow solderable
- AEC-Q101 qualified available
  Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

## **TYPICAL APPLICATIONS**

For use in high frequency inverters, freewheeling, DC/DC converters, and polarity protection in commercial, industrial, and automotive applications.

## **MECHANICAL DATA**

**Case:** SMF (DO-219AB) Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meet JESD 201 class 2 whisker test

Polarity: color band denotes the cathode end

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	V2FM15	UNIT				
Device marking code		2MC					
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	150	V				
Maximum average forward rectified current (fig.1)	I <sub>F(AV)</sub> <sup>(1)</sup>	2.0	A				
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	40	A				
Operating junction temperature range	T <sub>J</sub> <sup>(2)</sup>	T <sub>J</sub> <sup>(2)</sup> -40 to +175					
Storage temperature range	T <sub>STG</sub>	-55 to +175					

### Notes

<sup>(1)</sup> Free air, mounted on FR4 PCB, 2 oz. standard footprint

 $^{(2)}$  The heat generated must be less than the thermal conductivity from junction-to-ambient: dP<sub>D</sub>/dT<sub>J</sub> < 1/R<sub>0JA</sub>



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## V2FM15

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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)								
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT		
	I <sub>F</sub> = 1.0 A	T <sub>A</sub> = 25 °C		0.87	-	- V		
Instantance in farward valtage	I <sub>F</sub> = 2.0 A	$T_{A} = 25$ C	V <sub>E</sub> (1)	1.28	1.46			
Instantaneous forward voltage	I <sub>F</sub> = 1.0 A	T 105 %C	VF (1)	0.60	-			
	I <sub>F</sub> = 2.0 A	– T <sub>A</sub> = 125 °C		0.69	0.77			
Reverse current	V <sub>R</sub> = 100 V	T <sub>A</sub> = 25 °C		0.3	-	_ - μΑ		
		T <sub>A</sub> = 125 °C	– I <sub>R</sub> <sup>(2)</sup> –	300	-			
	V <sub>R</sub> = 150 V	T <sub>A</sub> = 25 °C	IR (2)	-	50			
		T <sub>A</sub> = 125 °C		550	2000			
Typical junction capacitance	4.0 V, 1 MHz		CJ	90	-	pF		

#### Notes

<sup>(1)</sup> Pulse test: 300 µs pulse width, 1 % duty cycle

 $^{(2)}$  Pulse test: Pulse width  $\leq 5\mbox{ ms}$ 

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25$ °c unless otherwise noted)						
PARAMETER	SYMBOL	V2FM15	UNIT			
Typical thermal resistance	R <sub>0JA</sub> (1)(2)	125	°C/W			
rypical thermal resistance	R <sub>0JM</sub> <sup>(2)</sup>	26	0/10			

#### Notes

<sup>(1)</sup> The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ 

<sup>(2)</sup> Device mounted on FR4 PCB, 2 oz. standard footprint, thermal resistance  $R_{0JA}$  – junction-to-ambient; thermal resistance  $R_{0JM}$  – junction-to-mount

ORDERING INFORMATION (Example)								
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE				
V2FM15-M3/H	0.015	Н	3000	7" diameter plastic tape and reel				
V2FM15-M3/I	0.015	I	10 000	13" diameter plastic tape and reel				
V2FM15HM3/H <sup>(1)</sup>	0.015	Н	3000	7" diameter plastic tape and reel				
V2FM15HM3/I <sup>(1)</sup>	0.015	l	10 000	13" diameter plastic tape and reel				

#### Note

(1) AEC-Q101 qualified



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## **RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25$ °C unless otherwise noted)

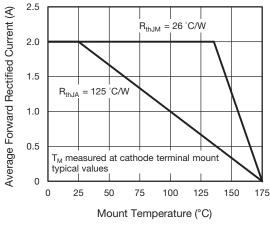
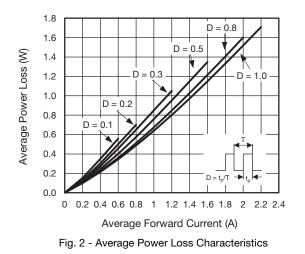
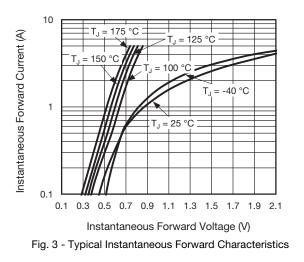
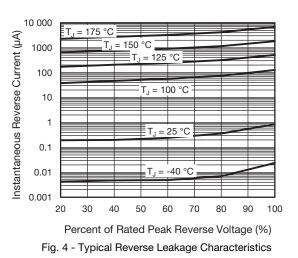
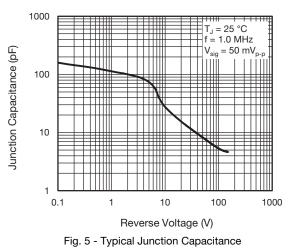


Fig. 1 - Maximum Forward Current Derating Curve









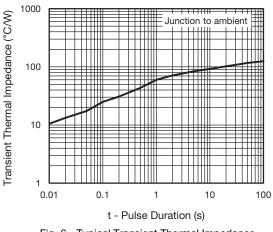


Fig. 6 - Typical Transient Thermal Impedance

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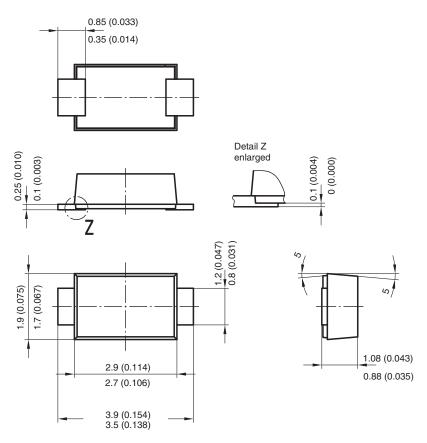
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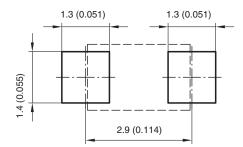


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### **PACKAGE OUTLINE DIMENSIONS** in millimeters (inches)



Foot print recommendation:

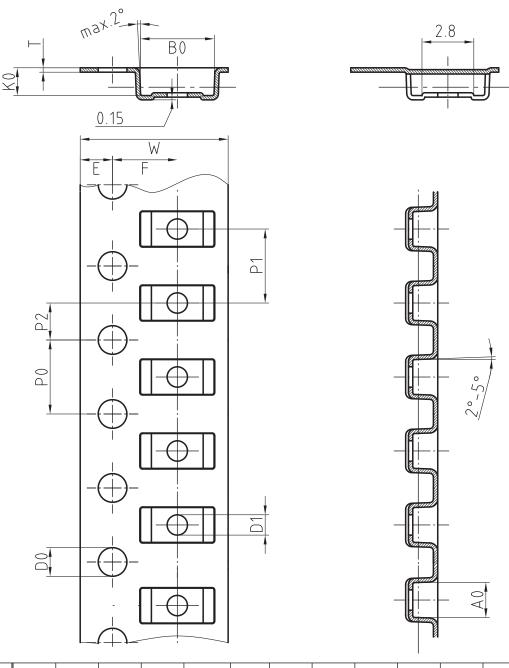


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### BLISTERTAPE DIMENSIONS in millimeters: SMF (DO-219AB)



Mat:	A0	B0	K0	W	Т	Ρ0	P2	P1	D0	D1	E	F
PS	1.9	4.0	1.5	8.0	0.235	4.0	2.0	4.0	1.5	1	1.75	3.5

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