

P.O. BOX 20912 • PHOENIX, ARIZONA 85036

MOC3030 MOC3031 M0C3032 MOC3033

ZERO VOLTAGE CROSSING **OPTICALLY ISOLATED TRIAC DRIVER**

These devices consist of gallium arsenide infrared-emitting diodes optically coupled to monolithic silicon detectors performing the functions of Zero Voltage crossing bilateral triac drivers.

They are designed for use with a triac in the interface of logic systems to equipment powered from 115 Vac lines, such as teletypewriters, CRTs, printers, motors, solenoids and consumer appliances, etc.

- Simplifies Logic Control of 110 Vac Power
- Zero Voltage Crossing
- High Breakdown Voltage: VDRM = 250 V Min
- High Isolation Voltage: VISO = 7500 V Min
- Small, Economical, 6-Pin DIP Package
- Same Pin Configuration as MOC3010/3011
- UL Recognized, File No. E54915
- dv/dt of 100 V/μs Typ

Derate above 25°C

Isolation Surge Voltage (1)

5 Second Duration)

Derate above 25°C

(Peak ac Voltage, 60 Hz,

Junction Temperature Range

Storage Temperature Range

Soldering Temperature (10 s)

TOTAL DEVICE MAXIMUM RATINGS

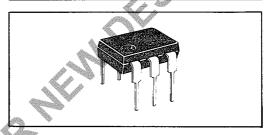
Total Power Dissipation @ $T_A = 25^{\circ}C$

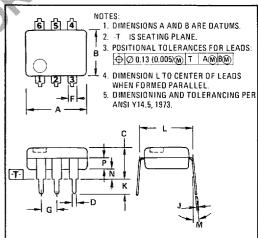
Ambient Operating Temperature Range

COUPLER/ISOLATOR

OPTO

ZERO CROSSING TRIAC DRIVER 250 VOLTS





	MILLIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	8.13	8.89	0.320	0.350	
В	6.10	6.60	0.240	0.260	
C	2.92	5.08	0.115	0.200	
D	0.41	0.51	0.016	0.020	
F	1.02	1.78	0.040	0.070	
G	2.54 BSC		0.100 BSC		
J	0.20	0.30	0.008 0.012		
К	2.54	3.81	0.100	0.150	
L	7.62 BSC		0.300 BSC		
M	0o	150	00	150	
N	0.38	2.54	0.015	0.100	
Р	1.27	2.03	0.050	0.080	

STYLE 6: 1. ANODE 2. CATHODE

NC MAIN TERMINAL

SUBSTRATE MAIN TERMINAL

CASE 730A-01

MAXIMUM RATINGS (TA = 25°C unless otherwise noted) Rating Symbol Value Unit INFRARED EMITTING DIODE MAXIMUM RATINGS Reverse Voltage 3.0 Valte V_R Forward Current — Continuous 50 mΑ ۱F Total Power Dissipation @ TA P_D 120 mW Negligible Power in Output Driver Derate above 25°C mW/°C 1.33 **OUTPUT DRIVER MAXIMUM RATINGS** Off-State Output Terminal Voltage 250 Volts V_{DRM} On-State RMS Current $T_A = 25^{\circ}C$ 100 mΑ IT(RMS) Peak Nonrepetitive Surge Current 1.2 Α **ITSM** (PW = 10 ms)Total Power Dissipation @ T_A = 25°C

PD

Viso

 P_{D}

ТJ

 T_A

T_{stg}

Isolation surge voltage, V_{ISO}, is an internal device dielectric breakdown rating.

300

7500

330

4.4

-40 to +100

-40 to +85

-40 to +150

260

mW

mW/°C

Vac

mW

mW/°C

°C

°C

°C

°C

┙	COUPLER SCHEN	TATIC
	Anode Cathode Zero Crossing Circuit	Main Terminal Substrate DO NOT Connect Main Terminal
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ELECTRICAL	CHARACTERISTICS	$(T_{\Delta} =$	25°C unless	otherwise noted)
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Characteristic	Symbol	Min	Тур	Max	Unit
LED CHARACTERISTICS					
Reverse Leakage Current (V _R = 3.0 V)	IR	_	0.05	100	μΑ
Forward Voltage (I _F = 30 mA)	VF	_	1.3	1.5	Volts
DETECTOR CHARACTERISTICS (IF = 0 unless otherwise noted	d)				
Peak Blocking Current, Either Direction (Rated VDRM, Note 1)	IDRM	—	10	100	nA
Peak On-State Voltage, Either Direction (ITM = 100 mA Peak)	٧ _{TM}	_	1.8	3.0	Volts
Critical Rate of Rise of Off-State Voltage	dv/dt	_	100	_	V/μs
COUPLED CHARACTERISTICS					
LED Trigger Current, Current Required to Latch Output (Main Terminal Voltage = 3.0 V, Note 2) MOC3030 MOC3031 MOC3032 MOC3033	lFT			30 15 10 5.0	mA
Holding Current, Either Direction	lΗ	4	100	_	μΑ
ZERO CROSSING CHARACTERISTICS					. , , , , , , , , , , , , , , , , , , ,
Inhibit Voltage (IF = Rated IFT, MT1-MT2 Voltage above which device will not trigger.)	VIH	_	15	25	Volts
Leakage in Inhibited State (IF = Rated IFT, Rated VDRM, Off State)	¹ R		100	200	μΑ

Note 1. Test voltage must be applied within dv/dt rating.

2. All devices are guaranteed to trigger at an IF value less than or equal to max IFT.

TYPICAL ELECTRICAL CHARACTERISTICS $T_A = 25^{\circ}C$



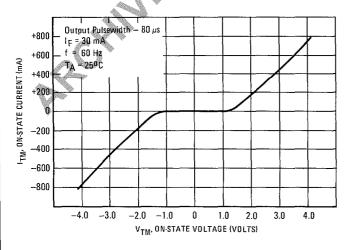
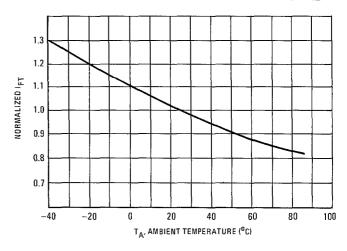


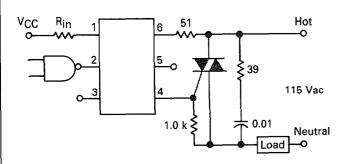
FIGURE 2 — TRIGGER CURRENT versus TEMPERATURE





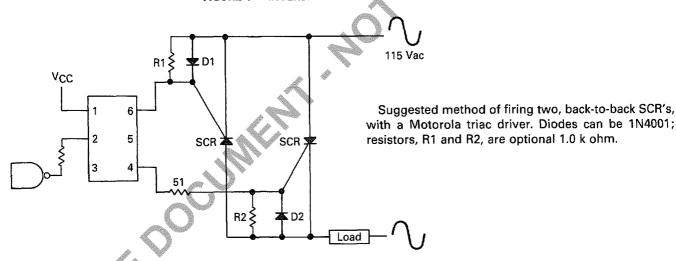
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FIGURE 3 — HOT-LINE SWITCHING APPLICATION CIRCUIT



Typical circuit for use when hot line switching is required. In this circuit the "hot" side of the line is switched and the load connected to the cold or neutral side. The load may be connected to either the neutral or hot line. The 39 ohm resistor and 0.01 µF capacitor are for snubbing of the triac and may or may not be necessary depending upon the particular triac and load used.

FIGURE 4 — INVERSE-PARALLEL SCR DRIVER CIRCUIT



When operating in environments subject to high line transients, it is suggested that an appropriate line suppressor be placed across the 115 Vac line to prevent voltage spikes from exceeding the maximum voltage rating of the device.

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