


PRODUCT / PROCESS CHANGE INFORMATION

1. PCI basic data

1.1 Company		STMicroelectronics International N.V
1.2 PCI No.		MMS/16/9718
1.3 Title of PCI		STM8AF product family errata sheets update
1.4 Product Category		STM8AF product family
1.5 Issue date		2016-04-28

2. PCI Team

2.1 Contact supplier	
2.1.1 Name	MARSHALL DAVE
2.1.2 Phone	
2.1.3 Email	dave.marshall@st.com
2.2 Change responsibility	
2.2.1 Product Manager	Michel BUFFA
2.1.2 Marketing Manager	Daniel COLONNA
2.1.3 Quality Manager	Pascal NARCHE

3. Change

3.1 Category	3.2 Type of change	3.3 Manufacturing Location
General Product & Design	Modification of datasheet	NA

4. Description of change

	Old	New
4.1 Description	<p>Previous errata sheets:</p> <ul style="list-style-type: none"> - Errata sheet DocID18392 revision 3 for STM8AF622x/4x/66/68, STM8AF612x/4x/66/68 products - Errata sheet DocID18393 revision 5 for STM8AF5xxx STM8AF6xxx products - Errata sheet DocID025112 revision 1 for STM8AF6223 STM8AF6226T products <p>The product datasheets below, specify VDD rise time parameter, implicitly for a VDD starting from 0V:</p> <ul style="list-style-type: none"> - Datasheet DocID14395 STM8AF526x/8x/Ax STM8AF6269/8x/Ax revision 12, - Datasheet DocID14952 STM8AF6246 STM8AF6248 STM8AF6266 STM8AF6268 revision 10, - Datasheet DocID025118 STM8AF6213 STM8AF6223 STM8AF6223A STM8AF6226 revision 5. 	<p>New errata sheets:</p> <ul style="list-style-type: none"> - Errata sheet DocID18392 revision 4 for STM8AF624x STM8AF626x products - Errata sheet DocID18393 revision 6 for STM8AF52xx STM8AF62xx products - Errata sheet DocID025112 revision 2 for STM8AF6213 STM8AF6223/26 products
4.2 Anticipated Impact on form,fit, function, quality, reliability or processability?	no change	

5. Reason / motivation for change

5.1 Motivation	<p>Errata sheets update to better specify VDD rise time rate conditions that may be encountered in very specific application use cases.</p> <p>The product datasheets specify VDD rise time parameter implicitly for a VDD starting from 0V. Some very specific applications could have a VDD starting from a residual voltage already above 0V and thus require that we explicitly specify these conditions.</p> <p>Even if such applications, cumulating a VDD starting from a residual voltage above 0V and VDD rise time rate slower than 50µs/V, could be affected by potential marginal start-up limited to the PPM range, we decided to inform our customers as a precautionary measure.</p> <p>Applications that are not presenting any residual voltage on VDD and having a VDD rise time rate faster than 50µs/V are not concerned by this errata sheet update.</p>
5.2 Customer Benefit	QUALITY IMPROVEMENT

6. Marking of parts / traceability of change	
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6.1 Description	NA
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7. Timing / schedule	
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7.1 Date of qualification results	2016-04-27
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7.2 Intended start of delivery	2016-04-27
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7.3 Qualification sample available?	Not Applicable
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8. Qualification / Validation			
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8.1 Description			
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8.2 Qualification report and qualification results	In progress	Issue Date	
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9. Attachments (additional documentations)	
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9718PpPrdtLst.pdf

10. Affected parts		
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10. 1 Current		10.2 New (if applicable)
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10.1.1 Customer Part No	10.1.2 Supplier Part No	10.1.2 Supplier Part No
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2488350	STM8AF6213PDU	
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2503305	STM8AF6223IPCU	
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2518172	STM8AF62A8TDY	
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