



Product Change Notification

PCN Number: RC145102

Notification Date: January 20, 2015

Title: New Errata in SAM D20 Revisions D and E and Update to NVM User Row Mapping		
Product Identification: All part numbers that start with "ATSAMD20" excluding development kits		
Reason for Change:	<input type="checkbox"/> Material / Composition <input type="checkbox"/> Processing / Manufacturing <input checked="" type="checkbox"/> Design / Firmware <input checked="" type="checkbox"/> Datasheet	<input type="checkbox"/> Manufacturing Location <input type="checkbox"/> Quality / Reliability <input type="checkbox"/> Logistics <input type="checkbox"/> Other:
New Errata in SAM D20 Revisions D and E Change Description: When Atmel introduced SAM D20 revision D to fix outstanding errata in the device family a new errata was introduced to the SERCOM module when used in I2C slave mode. This errata is also found on SAM D20 revision E. The software workaround described in the datasheet needs to be included in customer software to ensure correct operation. Errata: in I2C Slave mode, writing the CTRLB register when in the AMATCH or DRDY interrupt service routines can cause the state machine to reset. Errata reference: 13574 For details on the software workaround please see errata reference 13574 in the SAM D20 datasheet available at: http://www.atmel.com/Images/Atmel-42129-SAM-D20_Datasheet.pdf The software workaround is backwards compatible and will not cause any issues when implemented on the earlier die revisions that do not have errata 13574." NVM User Row Mapping Change Description: This is an update of the datasheet to correctly represent the behavior of the product. No design changes have been made. This change applies to all device revisions		

<p>Errata:</p> <p>In the table ""NVM User Row Mapping"", the WDT Window bitfield default value on silicon is not as specified in the datasheet. The datasheet defines the default value as 0x5, while it is 0xB on silicon. Errata reference: 13951</p> <p>Fix/Workaround:</p> <p>None.</p>			
<p>Identification Method to Distinguish Change:</p> <p>Not applicable. Contact Atmel if you need revision specific deliverables. If you have devices in stock, please send Atmel the lot ID marked on the devices and labels and we can advise what die revision it is.</p>			
Qualification Data:	<input type="checkbox"/> Available	<input type="checkbox"/> Will be available (mm/dd/yr):	<input checked="" type="checkbox"/> Not Applicable
Samples:	<input type="checkbox"/> Available	<input type="checkbox"/> Will be available (mm/dd/yr):	<input checked="" type="checkbox"/> Not Applicable
<p>Quantifiable Impact on Quality & Reliability:</p> <p>None</p>			
<p>Proposed First Ship Date*: November 27, 2014</p> <p><i>*The Proposed First Ship Date is the forecasted date that a customer may expect to receive changed product. This is determined by the estimated date of inventory depletion on the PCN issue date. This may be affected by fluctuations in supply and demand. Consequently, although customers should be prepared to receive changed product on this date, Atmel will continue to ship pre-changed product until a time in which inventory has been depleted. This may result in pre-changed product being shipped to customers after this forecasted date.</i></p>			
<p>Atmel Contact: Please contact your Atmel Sales Representative or Distributor for additional information (when replying via e-mail please include the PCN number in subject line).</p>			
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<p>To be completed by customer:</p> <p><input type="checkbox"/> <u>Approved</u></p> <p><input type="checkbox"/> <u>Rejected (Please state reason for rejection):</u> _____</p>			

Company: Name: Title: Date: Email Address: Location: Comments:	
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