

Product Change Notification / SYST-29FBUY793

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03-Nov-2021

Product Category:

Linear Regulators

PCN Type:

Document Change

Notification Subject:

Data Sheet - MIC5365/66 - High Performance Single 150mA LDO Data Sheet Document Revision

Affected CPNs:

SYST-29FBUY793_Affected_CPN_11032021.pdf SYST-29FBUY793_Affected_CPN_11032021.csv

Notification Text:

SYST-29FBUY793

Microchip has released a new Product Documents for the MIC5365/66 - High Performance Single 150mA LDO of devices. If you are using one of these devices please read the document located at MIC5365/66 - High Performance Single 150mA LDO.

Notification Status: Final

Description of Change:

- Converted Micrel document MIC5365/6 to Microchip data sheet DS20006605A.
- · Minor text changes throughout.
- Evaluation Board Schematic and BOM sections from original data sheet moved to the part's Evaluation Board User's Guide

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 03 Nov 2021

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

MIC5365/66 - High Performance Single 150mA LDO

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Affected Catalog Part Numbers (CPN)

MIC5365-1.0YC5-TR

MIC5365-1.0YMT-TZ

MIC5365-1.2YC5-TR

MIC5365-1.2YD5-T5

MIC5365-1.2YD5-TR

MIC5365-1.2YMT-TZ

MIC5365-1.3YC5-TR

MIC5365-1.5YC5-TR

MIC5365-1.5YMT-TZ

1,11000000 1,011,111 12

MIC5365-1.8YC5-EV

MIC5365-1.8YC5-TR

MIC5365-1.8YD5-T5

MIC5365-1.8YD5-TR

MIC5365-1.8YMT-EV

MIC5365-1.8YMT-TZ

MIC5365-2.0YC5-TR

MIC5365-2.0YMT-TZ

MIC5365-2.5YC5-TR

MIC5365-2.5YMT-EV

MIC5365-2.5YMT-TZ

MIC5365-2.6YC5-TR

MIC5365-2.6YMT-TZ

MIC5365-2.7YC5-TR

MIC5365-2.7YMT-TZ

MIC5365-2.85YC5-EV

MIC5365-2.85YC5-TR

MIC5365-2.85YD5-T5

MIC5365-2.85YD5-TR

MIC5365-2.85YMT-TZ

MIC5365-2.8YC5-TR

MIC5365-2.8YD5-T5

MIC5365-2.8YD5-TR

MIC5365-2.8YMT-TZ

MIC5365-2.9YC5-TR

MIC5365-2.9YMT-TZ

MIC5365-3.0/WFXT

MIC5365-3.0/WFXT-8

MIC5365-3.0CYW

MIC5365-3.0CYW-8

MIC5365-3.0YC5-EV

MIC5365-3.0YC5-TR

MIC5365-3.0YD5-T5

MIC5365-3.0YD5-TR

MIC5365-3.0YMT-EV MIC5365-3.0YMT-TZ

MIC5365-3.3YC5-TR

Date: Tuesday, November 02, 2021

- MIC5365-3.3YD5-T5
- MIC5365-3.3YD5-TR
- MIC5365-3.3YMT-TZ
- MIC5366-1.0YC5-TR
- MIC5366-1.0YMT-TZ
- MIC5366-1.2YC5-TR
- MIC5366-1.2YMT-TZ
- MIC5366-1.5YC5-TR
- MIC5366-1.5YMT-TZ
- MIC5366-1.8YC5-TR
- MIC5366-1.8YMT-TZ
- MIC5366-2.0YC5-TR
- MIC5366-2.0YMT-TZ
- MIC5366-2.1YMT-T5
- MIC5366-2.1YMT-TZ
- MIC5366-2.5YC5-TR
- MIC5366-2.5YMT-TZ
- MIC5366-2.6YC5-TR
- MIC5366-2.6YMT-TZ
- MIC5366-2.7YC5-TR
- MIC5366-2.7YMT-TZ
- MIC5366-2.85YC5-TR
- MIC5366-2.85YMT-TZ
- MIC5366-2.8YC5-TR
- MIC5366-2.8YMT-TZ
- MIC5366-2.9YC5-TR
- MIC5366-2.9YMT-TZ
- WIIC3300-2.9 I WII-12
- MIC5366-3.0YC5-TR MIC5366-3.0YMT-TZ
- MIC5366-3.1YMT-TR
- MIC5366-3.1YMT-TZ
- MIC5366-3.3YC5-TR
- MIC5366-3.3YMT-TZ



MIC5365/6

High-Performance Single 150 mA LDO

Features

- · 2.5V to 5.5V Input Voltage Range
- 150 mA Guaranteed Output Current
- Stable with 1 µF Ceramic Output Capacitors
- Low Dropout Voltage (155 mV at 150 mA)
- · Excellent Load/Line Transient Response
- Low Quiescent Current: 29 μA
- High PSRR: 70 dB
- · Output Discharge Circuit: MIC5366
- · High Output Accuracy
 - ±2% Initial Accuracy
- · Thermal Shutdown and Current Limit Protection
- Tiny 1 mm × 1 mm Thin DFN, SC-70-5, and Thin SOT23-5 Packages

Applications

- Mobile Phones
- · Digital Cameras
- · GPS, PMP, PDAs, and Handhelds
- · Portable Electronics

General Description

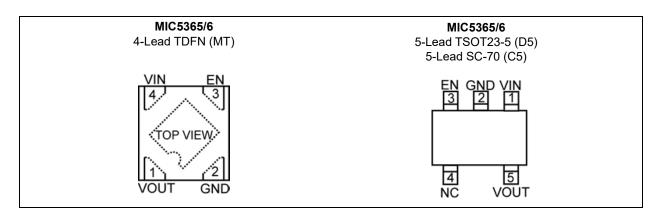
The MIC5365/6 are advanced general purpose linear regulators offering high power supply rejection (PSRR) in an ultra-small 1 mm × 1 mm package. The MIC5366 includes an auto-discharge feature that is activated when the enable pin is low. The MIC5365/6 is capable of sourcing 150 mA output current and offers high PSRR making it an ideal solution for any portable electronic application.

Ideal for battery-powered applications, the MIC5365/6 offers 2% initial accuracy, low dropout voltage (155 mV @ 150 mA), and low ground current (typically 29 $\mu A)$. The MIC5365/6 can also be put into a zero-off-mode current state, drawing virtually no current when disabled.

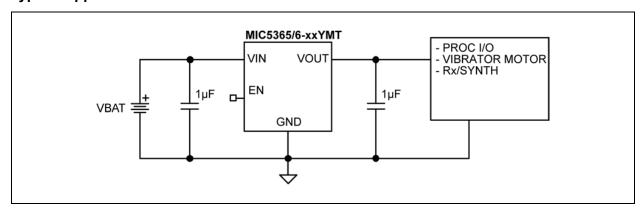
The MIC5365/6 is available in several advanced packages including a lead-free (RoHS-compliant) 1 mm × 1 mm Thin DFN occupying only 1 mm² of PCB area, a 75% reduction in board area compared to SC-70 and 2 mm × 2 mm TDFN packages. It is also available in a thin SOT23-5 package.

The MIC5365/6 have an operating junction temperature range of –40°C to 125°C.

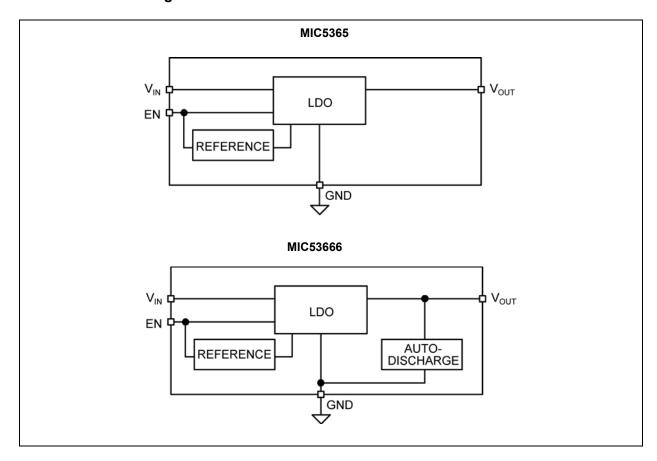
Package Types



Typical Application Circuit



Functional Block Diagrams



1.0 **ELECTRICAL CHARACTERISTICS**

Absolute Maximum Ratings †

Supply Voltage (V _{IN})	0V to +6V
Enable Voltage (V _{FN})	
Power Dissipation (P _D), Note 1	
ESD Rating, Note 2	_
Operating Ratings ±	

Supply Voltage (V _{IN})	+2.5V to +5.5V
Enable Voltage (V _{EN})

- † Notice: Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at those or any other conditions above those indicated in the operational sections of this specification is not intended. Exposure to maximum rating conditions for extended periods may affect device reliability.
- **‡ Notice:** The device is not guaranteed to function outside its operating ratings.
 - Note 1: The maximum allowable power dissipation of any T_A (ambient temperature) is $P_{D(MAX)} = (T_{J(MAX)} T_A)/\theta_{JA}$. Exceeding the maximum allowable power dissipation will result in excessive die temperature, and the regulator will go into thermal shutdown.
 - 2: Devices are ESD sensitive. Handling precautions are recommended. Human body model, $1.5~\text{k}\Omega$ in series with 100 pF.

ELECTRICAL CHARACTERISTICS

Electrical Characteristics: $V_{IN} = V_{EN} = V_{OUT} + 1V$; $C_{IN} = C_{OUT} = 1 \mu F$; $I_{OUT} = 100 \mu A$; $T_A = +25 ^{\circ}C$; Bold values are valid for $-40 ^{\circ}C$ to $+125 ^{\circ}C$ unless noted. (Note 1).							
Parameters	Symbol	Min.	Тур.	Max.	Units	Conditions	
		-2.0	_	+2.0		Variation from nominal V _{OUT}	
Output Voltage Accuracy	V _{OUT}	-3.0	_	+3.0	%	Variation from nominal V _{OUT} ; –40°C to +125°C	
Line Regulation	ΔV _{OUT} / V _{OUT}		0.02	0.3	%	$V_{IN} = V_{OUT} + 1V$ to 5.5V; $I_{OUT} = 100 \mu A$	
Load Regulation	$\Delta V_{OUT}/V_{OUT}$	_	0.3	1	%	I _{OUT} = 100 μA to 150 mA	
		_	55	110		I _{OUT} = 50 mA; V _{OUT} ≥ 2.8V	
Dropout Voltage	V _{DO}	_	155	310	mV	I _{OUT} = 150 mA; V _{OUT} ≥ 2.8V	
Dropout voltage		_	60	135	111 V	I _{OUT} = 50 mA; V _{OUT} < 2.8V	
		_	180	380		I _{OUT} = 150 mA; V _{OUT} < 2.8V	
Ground Pin Current	I _{GND}	_	29	39	μΑ	I _{OUT} = 0 mA	
Ground Pin Current in Shutdown	I _{SHDN}	_	0.05	1	μA	V _{EN} ≤ 0.2V	
Dinnle Dejection	PSRR	_	80	_	dB	f = up to 1 kHz; C _{OUT} = 1 μF	
Ripple Rejection		_	65	_	uБ	f = 1 kHz to 10 kHz, C _{OUT} = 1 μf	
Current Limit	I _{LIM}	200	325	550	mA	V _{OUT} = 0V	
Output Voltage Noise	e _N	_	200	_	μV_{RMS}	C _{OUT} = 1µF, 10 Hz to 100 kHz	
Auto-Discharge NFET Resistance	R _{DSCG}	_	30	_	Ω	MIC5366 Only; V _{EN} = 0V; V _{IN} = 3.6V, I _{OUT} = -3 mA	
Enable Inputs							
Enable Input Voltage	V _{IL}	_	_	0.2	V	Logic Low	
Enable Input Voltage	V _{IH}	1.2			v	Logic High	

ELECTRICAL CHARACTERISTICS

Electrical Characteristics: $V_{IN} = V_{EN} = V_{OUT} + 1V$; $C_{IN} = C_{OUT} = 1 \mu F$; $I_{OUT} = 100 \mu A$; $T_A = +25^{\circ}C$; **Bold** values are valid for $-40^{\circ}C$ to $+125^{\circ}C$ unless noted. (**Note 1**).

Parameters	Symbol		Тур.	Max.	Units	Conditions	
Enable Input Current	I _{IL}	_	0.01	1		V _{IL} ≤ 0.2V	
	I _{IH}	_	0.01	1	μΑ	V _{IH} ≥ 1.2V	
Turn-On Time	t _{ON}	_	50	125	μs	C _{OUT} = 1 μF; I _{OUT} = 150 mA	

Note 1: Specification for packaged product only.

TEMPERATURE SPECIFICATIONS

Parameters	Symbol	Min.	Тур.	Max.	Units	Conditions		
Temperature Ranges								
Maximum Junction Temperature Range	$T_{J(MAX)}$	-4 0	_	+150	°C	Note 1		
Storage Temperature Range	T _S	-65	_	+150	°C	_		
Lead Temperature	_	_	_	+260	°C	Soldering, 3 sec.		
Junction Temperature	TJ	-40	_	+125	°C	_		
Package Thermal Resistances								
Thermal Resistance, TDFN-4		_	240	_				
Thermal Resistance, TSOT23-5	θ_{JA}	_	253	_	°C/W	_		
Thermal Resistance, SC-70-5		_	256.5	_				

Note 1: The maximum allowable power dissipation is a function of ambient temperature, the maximum allowable junction temperature and the thermal resistance from junction to air (i.e., T_A, T_J, θ_{JA}). Exceeding the maximum allowable power dissipation will cause the device operating junction temperature to exceed the maximum +125°C rating. Sustained junction temperatures above +125°C can impact the device reliability.

2.0 TYPICAL PERFORMANCE CURVES

Note: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only. The performance characteristics listed herein are not tested or quaranteed. In some graphs or tables, the data presented may be outside the specified operating range (e.g., outside specified power supply range) and therefore outside the warranted range.

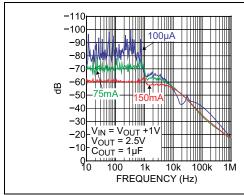


FIGURE 2-1: Ratio.

Power Supply Rejection

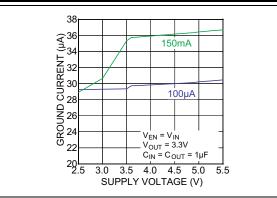


FIGURE 2-4: Voltage.

Ground Current vs. Supply

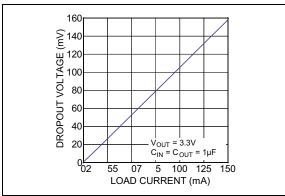


FIGURE 2-2: Current.

Dropout Voltage vs. Load

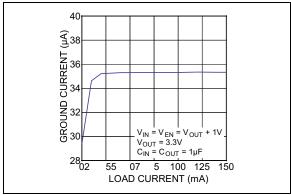


FIGURE 2-5: Current.

Ground Current vs. Load

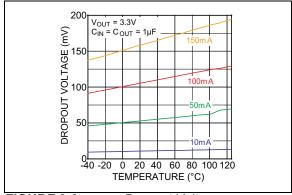


FIGURE 2-3: Temperature.

Dropout Voltage vs.

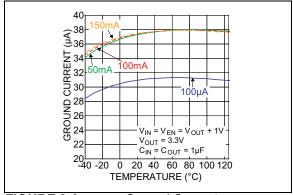


FIGURE 2-6: Temperature.

Ground Current vs.

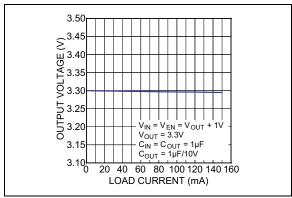


FIGURE 2-7: Current.

Output Voltage vs. Load

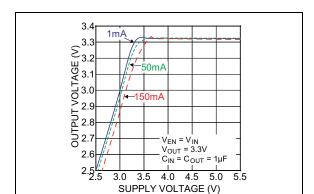


FIGURE 2-8: Voltage.

Output Voltage vs. Supply

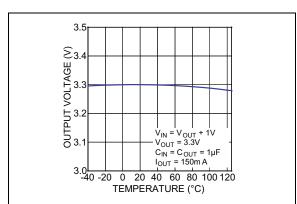


FIGURE 2-9:

Output Voltage vs.

Temperature.

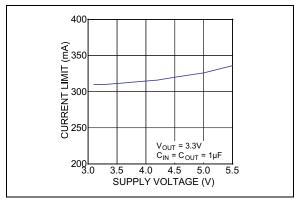


FIGURE 2-10:

Current Limit vs. Supply

Voltage.

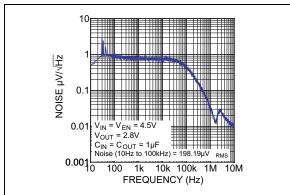


FIGURE 2-11:

Output Noise Spectral

Density.

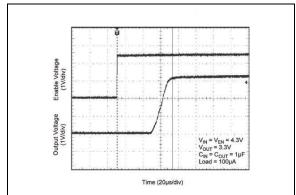


FIGURE 2-12:

Enable Turn-On.

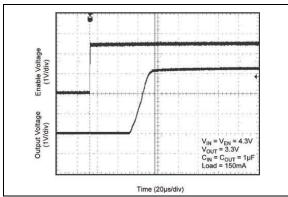


FIGURE 2-13: Enable Turn-On.

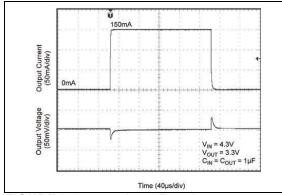


FIGURE 2-14: Load Transient.

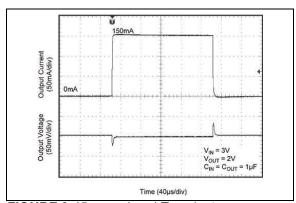


FIGURE 2-15: Load Transient.

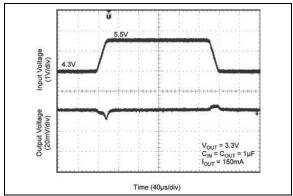


FIGURE 2-16: Line Transient.

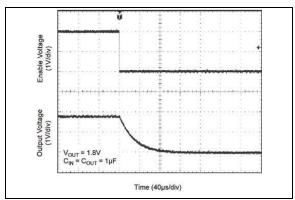


FIGURE 2-17: MIC5366 Auto Discharge (No Load).

MIC5365/6

3.0 PIN DESCRIPTIONS

The descriptions of the pins are listed in Table 3-1.

TABLE 3-1: PIN FUNCTION TABLE

Pin Number	Pin Name TDFN-4	Pin Name TSOT23-5	Pin Name SC70-5	Description
1	VOUT	_	_	Output Voltage.
1	_	VIN	VIN	Supply Input.
2	GND	GND	GND	Ground.
3	EN	EN	EN	Enable Input: Active-high. High = ON; Low = OFF. Do not leave floating.
4	VIN	_	_	Supply Input.
4	_	NC	NC	No Connect. Not internally connected.
5	_	VOUT	VOUT	Output Voltage.
EP	HS Pad	N/A	N/A	Exposed heat sink pad.

4.0 APPLICATION INFORMATION

MIC5365 and MIC5366 are low noise 150 mA LDOs. The MIC5366 includes an auto-discharge circuit that is switched on when the regulator is disabled through the enable pin. The MIC5365/6 regulator is fully protected from damage due to fault conditions, offering linear current limiting and thermal shutdown.

4.1 Input Capacitor

The MIC5365/6 is a high-performance, high-bandwidth device. An input capacitor of 1 μ F capacitor is required from the input to ground to provide stability. Low-ESR ceramic capacitors provide optimal performance at a minimum of space. Additional high-frequency capacitors, such as small valued NPO dielectric type capacitors, help filter out high-frequency noise and are good practice in any RF-based circuit. X5R or X7R dielectrics are recommended for the input capacitor. Y5V dielectrics lose most of their capacitance over temperature and are therefore, not recommended.

4.2 Output Capacitor

The MIC5365/6 requires an output capacitor of 1 μF or greater to maintain stability. The design is optimized for use with low-ESR ceramic chip capacitors. High-ESR capacitors may cause high frequency oscillation. The output capacitor can be increased, but performance has been optimized for a 1 μF ceramic output capacitor and does not improve significantly with larger capacitance.

X7R/X5R dielectric-type ceramic capacitors are recommended because of their temperature performance. X7R type capacitors change capacitance by 15% over their operating temperature range and are the most stable type of ceramic capacitors. Z5U and Y5V dielectric capacitors change value by as much as 50% and 60%, respectively, over their operating temperature ranges. To use a ceramic chip capacitor with Y5V dielectric, the value must be much higher than an X7R ceramic capacitor to ensure the same minimum capacitance over the equivalent operating temperature range.

4.3 No-Load Stability

Unlike many other voltage regulators, the MIC5365/6 will remain stable and in regulation with no load. This is especially important in CMOS RAM to keep applications alive.

4.4 Enable/Shutdown

The MIC5365/6 comes with an active-high enable pin that allows the regulator to be disabled. Forcing the enable pin low disables the regulator and sends it into a "zero" off-mode current state. In this state, current consumed by the regulator goes nearly to zero. Forcing

the enable pin high enables the output voltage. The active-high enable pin uses CMOS technology and the enable pin cannot be left floating; a floating enable pin may cause an indeterminate state on the output.

4.5 Thermal Considerations

The MIC5365/6 is designed to provide 150 mA of continuous current in a very small package. Maximum ambient operating temperature can be calculated based on the output current and the voltage drop across the part. For example if the input voltage is 3.6V, the output voltage is 2.8V, and the output current = 150 mA. The actual power dissipation of the regulator circuit can be determined using the equation Equation 4-1:

EQUATION 4-1:

$$P_D = (V_{IN} - V_{OUT})I_{OUT} + V_{IN} \times I_{GND}$$

Because this device is CMOS and the ground current is typically <100 μ A over the load range, the power dissipation contributed by the ground current is <1% and can be ignored for the calculation in Equation 4-2:

EQUATION 4-2:

$$P_D = (3.6V - 2.8V) \times 150 mA$$

$$P_D = 0.120W$$

To determine the maximum ambient operating temperature of the package, use the junction-to-ambient thermal resistance of the device and the following basic formula in Equation 4-3:

EQUATION 4-3:

$$P_{D(MAX)} = \left(\frac{T_{J(MAX)} - T_A}{\theta_{JA}}\right)$$

Where:

 $T_{J(MAX)} = 125^{\circ}C$

240°C/W (YMT Package)

 $\theta_{JA} = 256.5^{\circ}\text{C/W} (SC-70-5 \text{ Package})$

235°C/W (TSOT23-5 Package)

MIC5365/6

Substituting P_D for $P_{D(MAX)}$ and solving for the ambient operating temperature will give the maximum operating conditions for the regulator circuit. The junction-to-ambient thermal resistance for the minimum footprint is 250°C/W.

The maximum power dissipation must not be exceeded for proper operation.

For example, when operating the MIC5365-2.8YMT at an input voltage of 3.6V and 150 mA loads at each output with a minimum footprint layout, the maximum ambient operating temperature T_A can be determined in Equation 4-4:

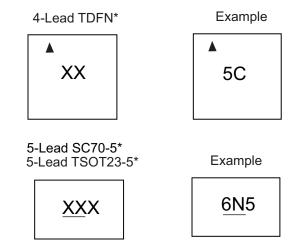
EQUATION 4-4:

$$0.120W = (125^{\circ}C - T_A)/(250^{\circ}C/W)$$
$$T_A = 95^{\circ}C$$

Therefore, the maximum ambient operating temperature of 95°C is allowed in a 1 mm × 1 mm TDFN package. For a full discussion of heat sinking and thermal effects of voltage regulators, refer to the "Regulator Thermals" section of Designing with Low-Dropout Voltage Regulators handbook.

5.0 PACKAGING INFORMATION

5.1 Package Marking Information



Legend: XX...X Product code or customer-specific information Year code (last digit of calendar year) Υ ΥY Year code (last 2 digits of calendar year) WW Week code (week of January 1 is week '01') Alphanumeric traceability code NNN Pb-free JEDEC® designator for Matte Tin (Sn) **e**3 This package is Pb-free. The Pb-free JEDEC designator (e3) can be found on the outer packaging for this package. •, ▲, ▼ Pin one index is identified by a dot, delta up, or delta down (triangle mark).

Note: In the event the full Microchip part number cannot be marked on one line, it will be carried over to the next line, thus limiting the number of available characters for customer-specific information. Package may or may not include the corporate logo.

Underbar (_) and/or Overbar (_) symbol may not be to scale.

TABLE 5-1: PACKAGE MARKING CODES FOR MIC5363/65

Part Number	Output Voltage	Marking Codes		
MIC5365-1.0YMT	1.0V	5C		
MIC5365-1.2YMT	1.2V	54		
MIC5365-1.3YMT	1.3V	55		
MIC5365-1.5YMT	1.5V	5F		
MIC5365-1.8YMT	1.8V	5G		
MIC5365-2.0YMT	2.0V	5H		
MIC5365-2.5YMT	2.5V	5J		
MIC5365-2.6YMT	2.6V	5K		
MIC5365-2.7YMT	2.7V	5L		
MIC5365-2.8YMT	2.8V	5M		
MIC5365-2.85YMT	2.85V	5N		
MIC5365-2.9YMT	2.9V	50		
MIC5365-3.0YMT	3.0V	5P		
MIC5365-3.3YMT	3.3V	5S		
MIC5365-1.0YC5	1.0V	65C		
MIC5365-1.2YC5	1.2V	654		
MIC5365-1.3YC5	1.3V	655		
MIC5365-1.5YC5	1.5V	65F		
MIC5365-1.8YC5	1.8V	65G		
MIC5365-2.0YC5	2.0V	65H		
MIC5365-2.5YC5	2.5V	65J		
MIC5365-2.6YC5	2.6V	65K		
MIC5365-2.7YC5	2.7V	65L		
MIC5365-2.8YC5	2.8V	65M		
MIC5365-2.85YC5	2.85V	65N		
MIC5365-2.9YC5	2.9V	65O		
MIC5365-3.0YC5	3.0V	65P		
MIC5365-3.3YC5	3.3V	65S		
MIC5365-1.2YD5	1.2V	645		
MIC5365-1.8YD5	1.8V	6G5		
MIC5365-2.8YD5	2.8V	6M5		
MIC5365-2.85YD5	2.85V	6N5		
MIC5365-3.3YD5	3.3V	6S5		
MIC5366-1.0YMT	1.0V	6C		
MIC5366-1.2YMT	1.2V	64		
MIC5366-1.3YMT	1.3V	65		
MIC5366-1.5YMT	1.5V	6F		
MIC5366-1.8YMT	1.8V	6G		
MIC5366-2.0YMT	2.0V	6H		
MIC5366-2.5YMT	2.5V	6J		
MIC5366-2.6YMT	2.6V	6K		
MIC5366-2.7YMT	2.7V	6L		
MIC5366-2.8YMT	2.8V	6M		

TABLE 5-1: PACKAGE MARKING CODES FOR MIC5363/65

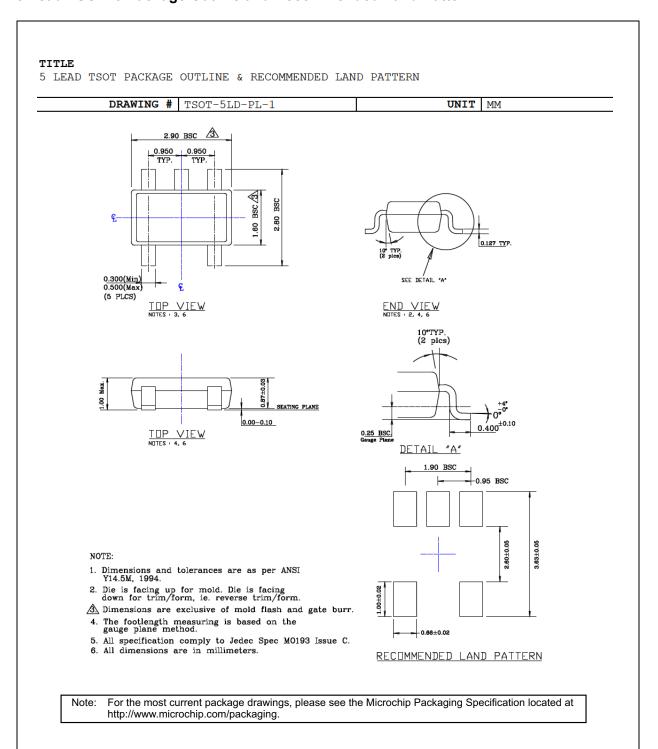
Part Number	Output Voltage	Marking Codes	
MIC5366-2.85YMT	2.85V	6N	
MIC5366-2.9YMT	2.9V	60	
MIC5366-3.0YMT	3.0V	6P	
MIC5366-3.3YMT	3.3V	6S	
MIC5366-1.0YC5	1.0V	66C	
MIC5366-1.2YC5	1.2V	664	
MIC5366-1.3YC5	1.3V	665	
MIC5366-1.5YC5	1.5V	66F	
MIC5366-1.8YC5	1.8V	66G	
MIC5366-2.0YC5	2.0V	66H	
MIC5366-2.5YC5	2.5V	66J	
MIC5366-2.6YC5	2.6V	66K	
MIC5366-2.7YC5	2.7V	66L	
MIC5366-2.8YC5	2.8V	66M	
MIC5366-2.85YC5	2.85V	66N	
MIC5366-2.9YC5	2.9V	66O	
MIC5366-3.0YC5	3.0V	66P	
MIC5366-3.3YC5	3.3V	66S	

4-Lead TDFN Package Outline and Recommended Land Pattern

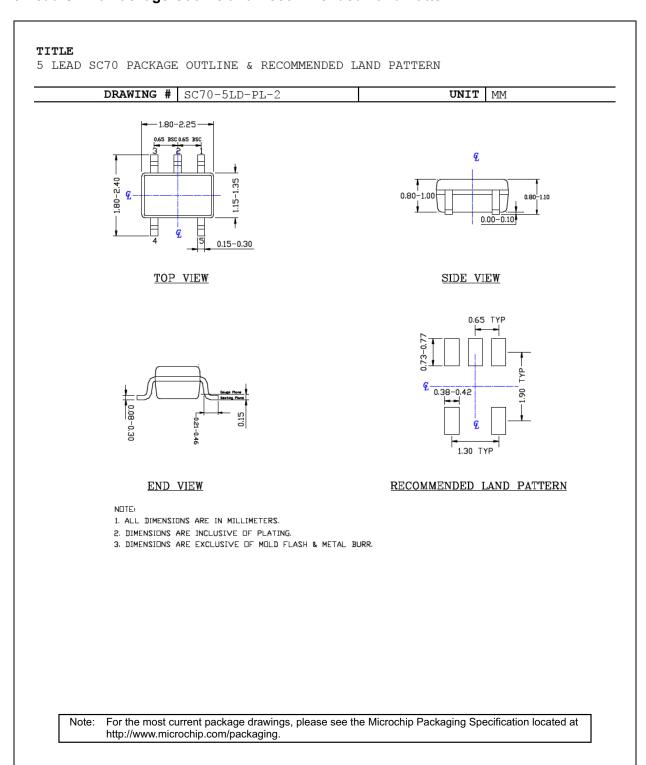
TITLE 4 LEAD TDFN 1.0x1.0mm PACKAGE OUTLINE & RECOMMENDED LAND PATTERN DRAWING # TDFN1010-4LD-PL-2 UNIT MM 0.225±0.050 1.000±0.050 4-C0.18 · 4 3 3 0.500±0.050 1.000±0.050 PIN #1 IDENTIFICATION 2 0.250±0.050 📑 PIN 1 TRIANGLE BY MARKING — CHAMFER R0.075mm 0.650 BSC TOP VIEW BOTTOM VIEW △ 0.05 C 1.30 SEATING PLANE 0.00~0.05 \square 0.152 Ref. -0.550±0.050 0.48 SIDE VIEW RECOMMENDED LAND PATTERN 1. MAX PACKAGE WARPAGE IS 0.05 MM 2. MAX ALLOWABLE BURR IS 0.076MM IN ALL DIRECTIONS 3. PIN #1 IS ON TOP WILL BE LASER MARKED 4. UNSPECIFIED TOLERANCE IS +/- 0.05 MM

Note: For the most current package drawings, please see the Microchip Packaging Specification located at http://www.microchip.com/packaging.

5-Lead TSOT23 Package Outline and Recommended Land Pattern



5-Lead SC-70 Package Outline and Recommended Land Pattern



APPENDIX A: REVISION HISTORY

Revision A (October 2021)

- Converted Micrel document MIC5365/6 to Microchip data sheet DS20006605A.
- Minor text changes throughout.
- Evaluation Board Schematic and BOM sections from original data sheet moved to the part's Evaluation Board User's Guide.



NOTES:

PRODUCT IDENTIFICATION SYSTEM

To order or obtain information, e.g., on pricing or delivery, contact your local Microchip representative or sales office.

PART NO.	- <u>xxx</u>	X	<u>xx</u>	<u>-xx</u>	Examples	:	
	Voltage Option	Junction Temperature Rang	Package ge	Media Type	a) MIC536	5-54YD5-T5	High-Performance Single 150 mA LDO, 1.2V, -40°C to +125°C, 5- Lead TSOT23-5 Package, 500/ Reel
Device:		MIC5365: High-Perform MIC5366: High-Perform Output Disc			b) MIC5369	5-5MYC5-TR	High-Performance Single 150 mA LDO, 2.8V, -40°C to +125°C, 5- Lead SC-70-5 Package, 5,000/ Reel
		5C = 1.0V (MIC5365)		V (MIC5366)	c) MIC5365	5-65NYMT-TZ	High-Performance Single 150 mA LDO, 2.85V, -40°C to +125°C, 4- Lead TDFN Package, 10,000/Reel
		54 = 1.2V (MIC5365) 55 = 1.3V (MIC5365) 5F = 1.5V (MIC5365) 5G = 1.8V (MIC5365) 5H = 2.0V (MIC5365)	65 = 1.3° 6F = 1.5° 6G = 1.8	V (MIC5366) V (MIC5366) V (MIC5366) BV (MIC5366) IV (MIC5366)	e) MIC5366	6-66LYC5-TR	High-Performance Single 150 mA LDO, 2.7V, -40°C to +125°C, 5- Lead SC-70-5 Package, 5,000/ Reel
		5J = 2.5V (MIC5365) 5K = 2.6V (MIC5365) 5L = 2.7V (MIC5365) 5M = 2.8V (MIC5365) 5N = 2.85V (MIC5365) 5O = 2.9V (MIC5365)	6J = 2.5\ 6K = 2.6 6L = 2.7\ 6M = 2.8 6N = 2.8 6O = 2.9	V (MIC5366) V (MIC5366) V (MIC5366) BV (MIC5366) SV (MIC5366) V (MIC5366)	f) MIC5366	6-66OYMT-TZ	High-Performance Single 150 mA LDO, 2.9V, -40°C to +125°C, 4-Lead TDFN Package, 10,000/Reel
Voltage Options	;	5P = 3.0V (MIC5365) 5S = 3.3V (MIC5365) 65C = 1.0V (MIC5365) 655 = 1.3V (MIC5365) 65F = 1.5V (MIC5365) 65G = 1.8V (MIC5365) 65H = 2.0V (MIC5365) 65H = 2.0V (MIC5365) 65L = 2.7V (MIC5365) 65K = 2.6V (MIC5365) 65M = 2.8V (MIC5365) 65N = 2.8V (MIC5365) 65N = 2.8V (MIC5365) 65P = 3.0V (MIC5365) 65P = 3.0V (MIC5365) 64S = 1.2V (MIC5365) 64S = 1.2V (MIC5365) 64S = 1.2V (MIC5365) 64S = 1.8V (MIC5365) 64S = 2.8V (MIC5365) 65S = 3.3V (MIC5365)	6Q = 3.0 6S = 3.3 66C = 1.1 664 = 1.1 665 = 1.1 66G = 1.1 66H = 2.1 66K = 2.1 66K = 2.1 66M = 2.1 66M = 2.1 66N = 2.1 66P = 3.1 66S = 3.1	V (MIC5366) V (MIC5366) V (MIC5366) V (MIC5366) 0V (MIC5366) 2V (MIC5366) 3V (MIC5366) 5V (MIC5366) 8V (MIC5366) 6V (MIC5366) 6V (MIC5366) 6V (MIC5366) 6V (MIC5366) 8V (MIC5366) 8V (MIC5366) 9V (MIC5366) 9V (MIC5366) 3V (MIC5366) 3V (MIC5366) 3V (MIC5366)	catalog part number desc used for ordering purpos the device package. Che Sales Office for package Tape and Reel option.		el identifier only appears in the number description. This identifier is ring purposes and is not printed on ckage. Check with your Microchip or package availability with the el option.
Junction Tempe Range:	erature	Y = -40°C to +1	25°C (RoHS C	compliant)			
Package:		(Pb-Free) (N C5 = 5-Lead SC- (MIC5365/6	MIC5365/6) 70 Package (Pl))T-23 Package	,			
Media Type:		T5 = 500/Reel TR = 5,000/Reel TZ = 10,000/Ree	I				



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