



# **PIC18F2585/2680/4585/4680**

## **Data Sheet**

28/40/44-Pin

Enhanced Flash Microcontrollers  
with ECAN™ Technology, 10-Bit A/D  
and nanoWatt Technology

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# MICROCHIP

# PIC18F2585/2680/4585/4680

## 28/40/44-Pin Enhanced Flash Microcontrollers with ECAN™ Technology, 10-Bit A/D and nanoWatt Technology

### Power Managed Modes:

- Run: CPU on, peripherals on
- Idle: CPU off, peripherals on
- Sleep: CPU off, peripherals off
- Idle mode currents down to 5.8  $\mu$ A typical
- Sleep mode currents down to 0.1  $\mu$ A typical
- Timer1 Oscillator: 1.1  $\mu$ A, 32 kHz, 2V
- Watchdog Timer: 2.1  $\mu$ A
- Two-Speed Oscillator Start-up

### Flexible Oscillator Structure:

- Four Crystal modes, up to 40 MHz
- 4x Phase Lock Loop (PLL) – available for crystal and internal oscillators
- Two External RC modes, up to 4 MHz
- Two External Clock modes, up to 40 MHz
- Internal oscillator block:
  - 8 user selectable frequencies, from 31 kHz to 8 MHz
  - Provides a complete range of clock speeds, from 31 kHz to 32 MHz when used with PLL
  - User tunable to compensate for frequency drift
- Secondary oscillator using Timer1 @ 32 kHz
- Fail-Safe Clock Monitor
  - Allows for safe shutdown if peripheral clock stops

### Special Microcontroller Features:

- C compiler optimized architecture with optional extended instruction set
- 100,000 erase/write cycle Enhanced Flash program memory typical
- 1,000,000 erase/write cycle Data EEPROM memory typical
- Flash/Data EEPROM Retention: > 40 years
- Self-programmable under software control
- Priority levels for interrupts
- 8 x 8 Single Cycle Hardware Multiplier
- Extended Watchdog Timer (WDT):
  - Programmable period from 41 ms to 131s
- Single-Supply 5V In-Circuit Serial Programming™ (ICSP™) via two pins
- In-Circuit Debug (ICD) via two pins
- Wide operating voltage range: 2.0V to 5.5V

### Peripheral Highlights:

- High current sink/source 25 mA/25 mA
- Three external interrupts
- One Capture/Compare/PWM (CCP1) module
- Enhanced Capture/Compare/PWM (ECCP1) module (40/44-pin devices only):
  - One, two or four PWM outputs
  - Selectable polarity
  - Programmable dead time
  - Auto-Shutdown and Auto-Restart
- Master Synchronous Serial Port (MSSP) module supporting 3-wire SPI™ (all 4 modes) and I<sup>2</sup>C™ Master and Slave modes
- Enhanced Addressable USART module:
  - Supports RS-485, RS-232 and LIN 1.3
  - RS-232 operation using internal oscillator block (no external crystal required)
  - Auto-Wake-up on Start bit
  - Auto-Baud Detect
- 10-bit, up to 11-channel Analog-to-Digital Converter module (A/D), up to 100 Ksps
  - Auto-acquisition capability
  - Conversion available during Sleep
- Dual analog comparators with input multiplexing

### ECAN Module Features:

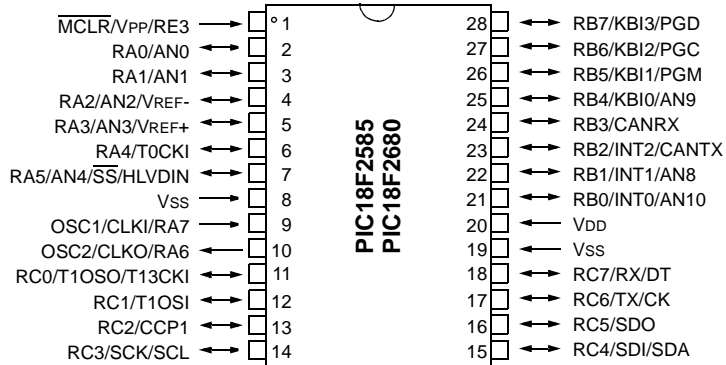
- Message bit rates up to 1 Mbps
- Conforms to CAN 2.0B ACTIVE Specification
- Fully backward compatible with PIC18XXX8 CAN modules
- Three modes of operation:
  - Legacy, Enhanced Legacy, FIFO
- Three dedicated transmit buffers with prioritization
- Two dedicated receive buffers
- Six programmable receive/transmit buffers
- Three full 29-bit acceptance masks
- 16 full 29-bit acceptance filters w/ dynamic association
- DeviceNet™ data byte filter support
- Automatic remote frame handling
- Advanced error management features

Device	Program Memory		Data Memory		I/O	10-Bit A/D (ch)	CCP1/ECCP1 (PWM)	MSSP		EUSART	Comp.	Timers 8/16-bit
	Flash (bytes)	# Single-Word Instructions	SRAM (bytes)	EEPROM (bytes)				SPI™	Master I <sup>2</sup> C™			
PIC18F2585	48K	24576	3328	1024	28	8	1/0	Y	Y	1	0	1/3
PIC18F2680	64K	32768	3328	1024	28	8	1/0	Y	Y	1	0	1/3
PIC18F4585	48K	24576	3328	1024	44	11	1/1	Y	Y	1	2	1/3
PIC18F4680	64K	32768	3328	1024	40/44	11	1/1	Y	Y	1	2	1/3

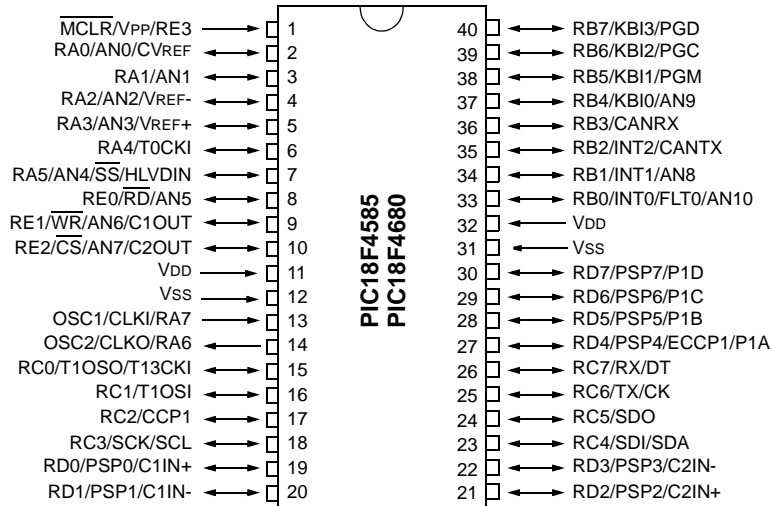
# PIC18F2585/2680/4585/4680

## Pin Diagrams

### 28-Pin PDIP, SOIC



### 40-Pin PDIP



# PIC18F2585/2680/4585/4680

## Pin Diagrams (Continued)

