



Product Change Notification / JAON-04DXFD964

Date:

09-Feb-2021

Product Category:

8-bit Microcontrollers, Analog to Digital Converters, Capacitive Touch Sensors, Charge Pump DC-to-DC Converters, Digital Potentiometers, Digital to Analog Converters, Instrumentation Amplifier, Interface-Controller Area Network (CAN), Interface- Infrared Products, Interface- LIN Transceiver, KEELOQ® Encoder Devices, Linear Comparators, Linear Op Amps, Linear Programmable Gain Amplifiers, Memory, Piezoelectric Horn Drivers and CO Detector, Power Management - PWM Controllers, Power MOSFET Drivers, Real-Time Clock/Calendar, RFID tag ICs - microID® ICs, Temperature Sensors

PCN Type:

Manufacturing Change

Notification Subject:

CCB 4541 Final Notice: Qualification of a new lead frame design for selected products available in 8L SOIC package using 8390A die attach and gold (Au) bond wire material assembled at MMT assembly site.

Affected CPNs:

[JAON-04DXFD964_Affected_CPN_02092021.pdf](#)

[JAON-04DXFD964_Affected_CPN_02092021.csv](#)

Notification Text:

PCN Status: Final notification

PCN Type: Manufacturing Change

Microchip Parts Affected: Please open one of the files found in the Affected CPNs section.

NOTE: For your convenience Microchip includes identical files in two formats (.pdf and .xls).

Description of Change: Qualification of a new lead frame design for selected products available in 8L SOIC package using 8390A die attach and gold (Au) bond wire material assembled at MMT assembly site.

Pre Change:

Using lead frame without lead lock

Post Change:Using lead frame with lead lock**Pre and Post Change Summary:**

	Pre Change	Post Change
Assembly Site	Microchip Technology Thailand (Branch) / MMT	Microchip Technology Thailand (Branch) / MMT
Wire material	Au	Au
Die attach material	8390A	8390A
Molding compound material	G600V	G600V
Lead frame material	CDA194	CDA194
Lead Frame Lead Lock	No	Yes
	See attached pre and post change comparison	

Impacts to Data Sheet: None**Change Impact:**None**Reason for Change:**To improve productivity by qualifying new lead frame design**Change Implementation Status:**In Progress**Estimated First Ship Date:**February 19, 2021 (date code: 2108)

NOTE: Please be advised that after the estimated first ship date customers may receive pre and post change parts.

Due to unforeseen circumstances, that are out of Microchip's control, full qualification will be made available as soon as it is approved which may be after the estimated first ship date so that Microchip can maintain continuity of supply and not disrupt customer orders.

Time Table Summary:

	February 2021				March 2021				
	06	07	08	09	10	11	12	13	14
Workweek									
Final PCN Issue Date		X							
Qual Report Availability							X		
Estimated First Ship Date			X						

Method to Identify Change: Traceability code

Estimated Qualification Completion Date: March 2021

Note: This final PCN will be updated to include the Qualification report as soon as it is completed.

Revision History: February 9, 2021: Issued final notification. Provided estimated first ship date to be February 19, 2021.

The change described in this PCN does not alter Microchip's current regulatory compliance regarding the material content of the applicable products.

Attachments:

[PCN_JAON-04DXFD964_Qual_Plan.pdf](#)

[PCN_JAON-04DXFD964_Pre and Post Change_Summary.pdf](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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If you wish to change your PCN profile, including opt out, please go to the [PCN home page](#) select login and sign into your myMicrochip account. Select a profile option from the left navigation bar and make the applicable selections.

Affected Catalog Part Numbers (CPN)

MCP14E6-E/SN
MCP14E6T-E/SN
MCP14E7-E/SN
MCP14E7T-E/SN
MCP14E8-E/SN
MCP14E8T-E/SN
MCP14E9-E/SN
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Pre and Post Change Summary

PCN # JAON-04DXFD964



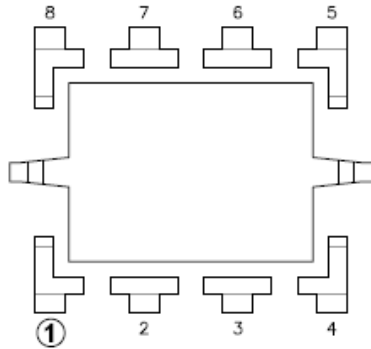
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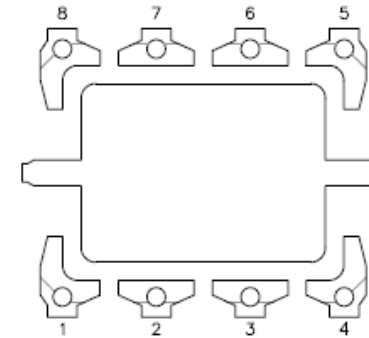
Lead frame Comparison

Pre Change

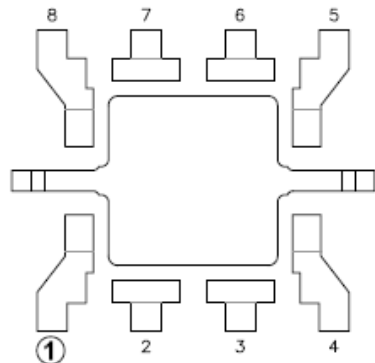


LF STRIP DENSITY: x140
LF PAD SIZE: 95x130 mils
LEAD LOCK: NO

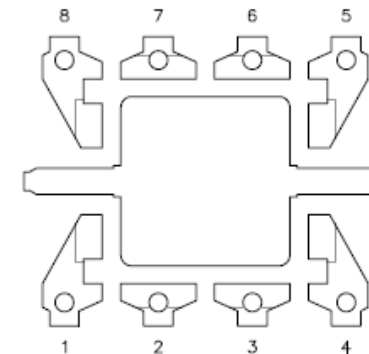
Post Change



LF STRIP DENSITY: x320
LF PAD SIZE: 95x130 mils
LEAD LOCK: YES



LF STRIP DENSITY: x140
LF PAD SIZE: 90x90 mils
LEAD LOCK: NO



LF STRIP DENSITY: x320
LF PAD SIZE: 90x90 mils
LEAD LOCK: YES



MICROCHIP

QUALIFICATION PLAN SUMMARY

PCN #: JAON-04DXFD964

Date

January 21, 2021

Qualification of a new lead frame design for selected products available in 8L SOIC package using 8390A die attach and gold (Au) bond wire material assembled at MMT assembly site.

Purpose: Qualification of a new lead frame design for selected products available in 8L SOIC package using 8390A die attach and gold (Au) bond wire material assembled at MMT assembly site.

CCB No.: 4541

<u>Misc</u>	Assembly site	MMT
	BD Number	BDM-002812/A
	MP Code (MPC)	MVAR14C2XFX3
	Part Number (CPN)	PIC16F15313-E/SN
	MSL information	MSL-1/260C
	Assembly Shipping Media (T/R, Tube/Tray)	Tube
	Base Quantity Multiple (BQM)	100
	Reliability Site	MTAI
<u>Lead-Frame</u>	Paddle size	95x130 mils
	Material	CDA194
	DAP Surface Prep	Bare Cu
	Treatment	BOT
	Process	Stamped
	Lead-lock	Yes
	Part Number	10100842
	Lead Plating	Matte Tin
	Strip Size	239.0x70.0mm
	Strip Density	320 pads/strip
<u>Bond Wire</u>	Material	Au
<u>Die Attach</u>	Part Number	8390A
	Conductive	Yes
<u>MC</u>	Part Number	G600V
<u>PKG</u>	PKG Type	SOIC
	Pin/Ball Count	8
	PKG width/size	150 mils

Test Name	Conditions	Sample Size	Min. Qty of Spares per Lot (should be properly marked)	Qty of Lots	Total Units	Fail Accept Qty	Est. Dur. Days	Test Site	Special Instructions
Standard Pb-free Solderability	J-STD-002 ; Perform 8 hour steam aging for Matte tin finish and 1 hour steam aging for NiPdAu finish prior to testing. Standard Pb-free: Matte tin/ NiPdAu finish, SAC solder, wetting temp 245°C for both SMD & through hole packages.	22	5	1	27	> 95% lead coverage	5		Standard Pb-free solderability is the requirement. SnPb solderability (backward solderability- SMD reflow soldering) is required for any plating related changes and highly recommended for other package BOM changes.
Wire Bond Pull - WBP	Mil. Std. 883-2011	5	0	3	15	0 fails after TC	5	MMT/MTAI	30 bonds from a minimum of 5 devices.
Wire Bond Shear - WBS	CDF-AEC-Q100-001	5	0	3	15		5	MMT/MTAI	30 bonds from a minimum of 5 devices.
Wire Sweep		5	0	3	15	0		MMT	Required for any reduction in wire bond thickness.
Physical Dimensions	Measure per JESD22 B100 and B108	10	0	3	30	0	5	MMT	
External Visual	Mil. Std. 883-2009/2010	All devices prior to submission for qualification testing	0	3	ALL	0	5	MMT/ MTAI	
Preconditioning - Required for surface mount devices	+150°C Bake for 24 hours, moisture loading requirements per MSL level + 3X reflow at peak reflow temperature per Jedec-STD-020E for package type; Electrical test pre and post stress at +25°C. MSL1/260C	231	15	3	738	0	15	MTAI	Spares should be properly identified. 77 parts from each lot to be used for HAST, Autoclave, Temp Cycle test.

Test Name	Conditions	Sample Size	Min. Qty of Spares per Lot (should be properly marked)	Qty of Lots	Total Units	Fail Accept Qty	Est. Dur. Days	Test Site	Special Instructions
HAST	+130°C/85% RH for 96 hours. Electrical test pre and post stress at +25°C and hot temp.	77	5	3	246	0	10	MTAI	Spares should be properly identified. Use the parts which have gone through Pre-conditioning.
Unbiased HAST	+130°C/85% RH for 96 hrs. Electrical test pre and post stress at +25°C.	77	5	3	246	0	10	MTAI	Spares should be properly identified. Use the parts which have gone through Pre-conditioning.
Temp Cycle	-65°C to +150°C for 500 cycles. Electrical test pre and post stress at hot temp; 3 gram force WBP, on 5 devices from 1 lot, test following Temp Cycle stress.	77	5	3	246	0	15	MTAI	Spares should be properly identified. Use the parts which have gone through Pre-conditioning.