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PRODUCT INFORMATION NOTIFICATION

PIN: PIN145268 **Date**: February 27, 2014

Subject: F-RAM Products Datasheet Format Changes

To: PHILIP MAILLET

FUTURE

pcn.system2@future.ca

Change Type: Minor

Description of Change:

Cypress Semiconductor will convert the F-RAM datasheets from the format used by Ramtron International to the Cypress standard format. There will be no changes to datasheet specifications and F-RAM products are under warranty against both formatted datasheets. The new format will maintain the same Cypress document numbers, but the revision letter will increment to the next alphabet letter for each datasheet. All content will remain in each datasheet, but may change location, with the following exceptions:

1) Package Marking Scheme (top mark) is not included in the Cypress standard format. This information can be found for each device on the unique product pages at:

www.cypress.com >

Products >

Nonvolatile Products >

F-RAM (Serial, Processor Companion, or Parallel) >

Select any product >

Quality and RoHS >

Marking

2) Moisture Sensitivity Level (MSL) is not included in the Cypress standard format. This information can also be found on the product pages at:

www.cvpress.com >

Products >

Nonvolatile Products >

F-RAM (Serial, Processor Companion, or Parallel) >

Select any product >

Quality and RoHS >

Moisture Sensitivity Level (MSL).

- 3) A Thermal Resistance table is included in the Cypress standard format.
- 4) The Maximum Ratings section in the Cypress standard format is expanded (includes junction temperature, latch up current, etc.).
- 5) The revision history predating the Cypress document number is removed.

Affected Part Numbers: 160

Affected Parts: See attached list of affected parts with implementation dates.

Approximate Implementation Date:

Refer to below table for implementation dates. The F-RAM datasheets will be released on www.cypress.com in groups according to device interface.

Going forward, all new F-RAM products will use the Cypress datasheet format.

Datasheet	Implementation Date	Number of Parts Affected
SPI Interface	Immediate	72
Async Interface	Immediate	33
I2C Interface	Immediate	39
Processor Companion	3/28/2014	16

Anticipated Impact:

Cypress applications and support engineering will reference the new datasheets in response to customer inquiries. Cypress recommends that customers update their product records to the new datasheets.

Method of Identification:

Cypress format datasheets have the Cypress Perform logo in the top left corner of the first page and include the Logic Block Diagram at the bottom of the first page. Ramtron format datasheets have the Cypress Perform logo in the top right corner of the first page and show the Pin Configuration on the first page. Examples of the two datasheet formats are shown in an attachment.

Response Required:

This is an information only announcement. No response is required.

For additional information regarding this change, contact your local sales representative or contact the PCN Administrator at pcn_adm@cypress.com.

Sincerely,

Cypress PCN Administration