

## DIL24W/TSSOP24 ZIF 170mil (ord.no. 70-0915)

- universal adapter, assigned for TSSOP devices up to 24 pins (for example EM Microelectronic EM6xxx series, AMI/Anachip/Atmel/Gould/ICT PLD)
- operation (mechanical) life of ZIF socket - 10.000 actuations
- made in Slovakia

<b>Ord. no</b>	70-0915
<b>Socket</b>	ZIF TSSOP24, open top type
<b>Bottom</b>	2 rows, 2x 12 pins, square, 0.6x0.6mm, rows spacing 600mil
<b>Class</b>	Universal
<b>Subclass</b>	DIL/(T)SSOP



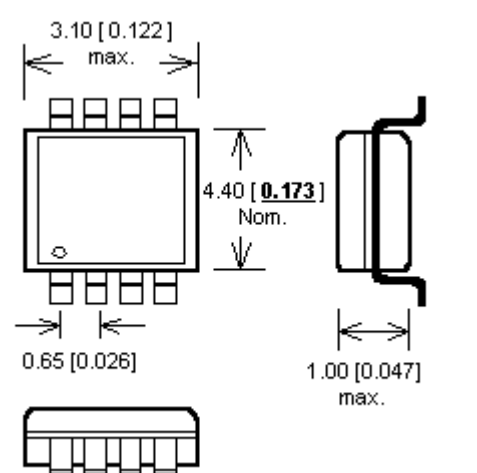
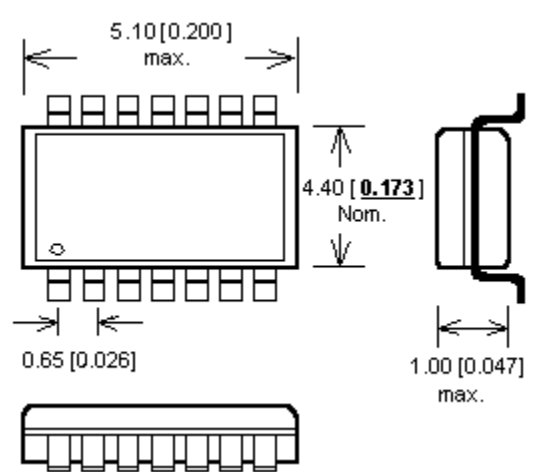
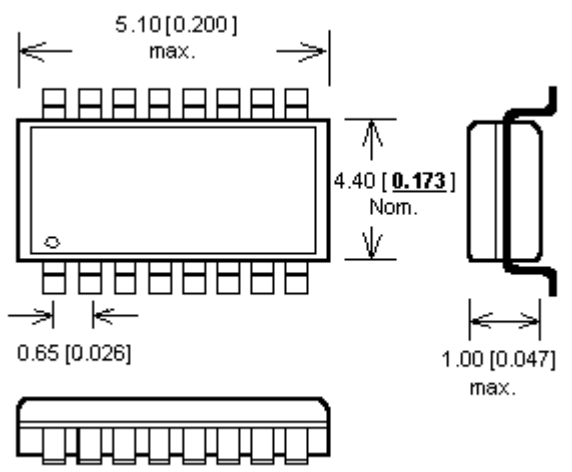
### Adapter manual

- Insert adapter to the device programmer ZIF socket according to the picture placed near of it. If you have some doubts about orientation of this adapter in device programmer ZIF socket, it is valid general rule, the orientation of the text of title is the same as the text on the top of the device programmer.
- Push the top of adapter ZIF socket to open it. Insert the device into the adapter ZIF socket. The right position of the programmed device in adapter ZIF socket is show at picture near (mainly left above) the adapter ZIF socket. Then release adapter ZIF socket. If you use this adapter for devices with less than 24 pins, then place device to the bottom edge of adapter ZIFsocket. Upper contacts stay unconnected.
- Visually check interconnection between device and adapter ZIF socket. If everything looks OK, the device is ready for programming.
- Be careful, because the incorrect insertion of adapter to the device programmer ZIF socket or device to the adapter ZIF socket can damage the programmed device.
- To take out the device push the top of adapter ZIF socket and remove device from it.
- When you finish work with adapter, remove it from the device programmer ZIF socket.
- Do not directly touch the pins of the adapter and adapter ZIF socket, because dirt may cause errors during programming of device.
- For handling with the device we recommended to use a vacuum pick up tool.
- If programming yields isn't 100% when using programming adapter or if some unreliability appears, try to connect one 22nF-100nF multilayer capacitor (that meet the EIA X7R or Z5U specification) between leads of adapter connected to pins VCC and GND pins of programmed chip.

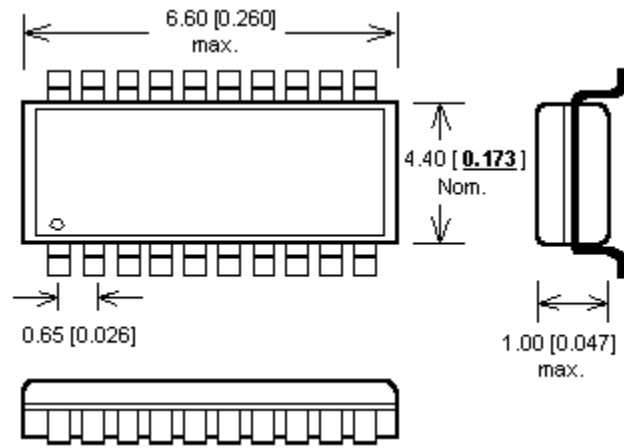
### Software note

- If software version, you're using currently, doesn't contain support for this programming adapter, please download the latest version of software - Regular or OnDemand - from our web site.

Accepted package(s)

<p><b>TSSOP8</b></p>	 <p>3.10 [0.122] max.</p> <p>4.40 [0.173] Nom.</p> <p>0.65 [0.026]</p> <p>1.00 [0.047] max.</p> <p>Technical drawing showing top, side, and bottom views of a TSSOP8 package with dimensions: 3.10 [0.122] max. (width), 4.40 [0.173] Nom. (height), 0.65 [0.026] (lead pitch), and 1.00 [0.047] max. (lead length).</p>
<p><b>TSSOP14</b></p>	 <p>5.10 [0.200] max.</p> <p>4.40 [0.173] Nom.</p> <p>0.65 [0.026]</p> <p>1.00 [0.047] max.</p> <p>Technical drawing showing top, side, and bottom views of a TSSOP14 package with dimensions: 5.10 [0.200] max. (width), 4.40 [0.173] Nom. (height), 0.65 [0.026] (lead pitch), and 1.00 [0.047] max. (lead length).</p>
<p><b>TSSOP16</b></p>	 <p>5.10 [0.200] max.</p> <p>4.40 [0.173] Nom.</p> <p>0.65 [0.026]</p> <p>1.00 [0.047] max.</p> <p>Technical drawing showing top, side, and bottom views of a TSSOP16 package with dimensions: 5.10 [0.200] max. (width), 4.40 [0.173] Nom. (height), 0.65 [0.026] (lead pitch), and 1.00 [0.047] max. (lead length).</p>

**TSSOP20**



**TSSOP24**

