



DMP2035U

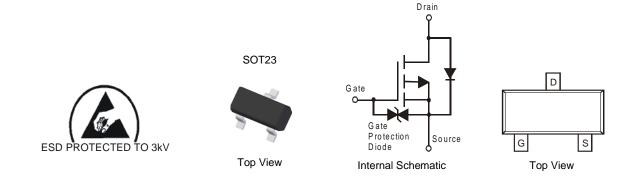
#### P-CHANNEL ENHANCEMENT MODE MOSFET

#### Features

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Up To 3KV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

#### **Mechanical Data**

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram Below
- Weight: 0.009 grams (Approximate)



#### Ordering Information (Note 4)

Part Number	Compliance	Case	Packaging
DMP2035U-7	Standard	SOT23	3,000 / 7" Tape & Reel
DMP2035UQ-7	Automotive	SOT23	3,000 / 7" Tape & Reel
DMP2035U-13	Standard	SOT23	10,000 / 13" Tape & Reel

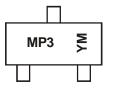
Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

 See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

## **Marking Information**



MP3 = Product Type Marking Code YM = Date Code Marking Y or  $\overline{\gamma}$  = Year (ex: D = 2016) M = Month (ex: 9 = September)

Date	Code	Key
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Year	2009		~	2016	2017	20	18	2019	2020	20	21	2022
Code	W		,	D	E	ŀ	=	G	Н			J
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characterist		Symbol	Value	Unit	
Drain-Source Voltage		V <sub>DSS</sub>	-20	V	
Gate-Source Voltage		V <sub>GSS</sub>	±8	V	
Continuous Drain Current (Note 5)	s Drain Current (Note 5) Steady $T_A = +25^{\circ}C$ State $T_A = +70^{\circ}C$		۱ <sub>D</sub>	-3.6 -2.9	А
Pulsed Drain Current (Note 6)		I <sub>DM</sub>	-24	А	

## **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	PD	0.81	W
Thermal Resistance, Junction to Ambient @T <sub>A</sub> = +25°C	R <sub>θJA</sub>	153.5	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

Electrical Characteristics (@T <sub>A</sub> = +25	°C, unless otherwise	specified	d.)				
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)	•			•	•	·	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-20	—	_	V	$V_{GS} = 0V, I_D = -250 \mu A$	
Zero Gate Voltage Drain Current $T_J = +25^{\circ}C$	I <sub>DSS</sub>		—	-1.0	μA	$V_{DS} = -20V, V_{GS} = 0V$	
Gate-Source Leakage	IGSS		—	±10	μA	$V_{GS} = \pm 8V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V <sub>GS(th)</sub>	-0.4	-0.7	-1.0	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$	
			23	35		$V_{GS} = -4.5V, I_D = -4.0A$	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	-	30	45 62	mΩ	$V_{GS} = -2.5V, I_D = -4.0A$	
			41			$V_{GS} = -1.8V, I_D = -2.0A$	
Forward Transfer Admittance	Y <sub>fs</sub>	_	14	_	S	$V_{DS} = -5V, I_D = -4A$	
Diode Forward Voltage	V <sub>SD</sub>	_	-0.7	-1.0	V	$V_{GS} = 0V, I_{S} = -1A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C <sub>iss</sub>		1,610	_	pF		
Output Capacitance	C <sub>oss</sub>		157	_	pF	$V_{DS} = -10V, V_{GS} = 0V$ - f = 1.0MHz	
Reverse Transfer Capacitance	Crss		145	_	pF	1 - 1.00012	
Gate Resistance	Rg		9.45	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge	Qg		15.4	_	nC		
Gate-Source Charge	Q <sub>gs</sub>	—	2.5	—	nC	$V_{GS} = -4.5V, V_{DS} = -10V,$ $I_{D} = -4A$	
Gate-Drain Charge	Q <sub>gd</sub>	—	3.3	—	nC		
Turn-On Delay Time	t <sub>D(on)</sub>	_	16.8		ns		
Turn-On Rise Time	tr		12.4		ns	V <sub>DS</sub> = -10V, V <sub>GS</sub> = -4.5V,	
Turn-Off Delay Time	t <sub>D(off)</sub>	_	94.1	_	ns	$R_L = 10\Omega, R_G = 6.0\Omega, I_D = -1A$	
Turn-Off Fall Time	t <sub>f</sub>	_	42.4	_	ns	1	

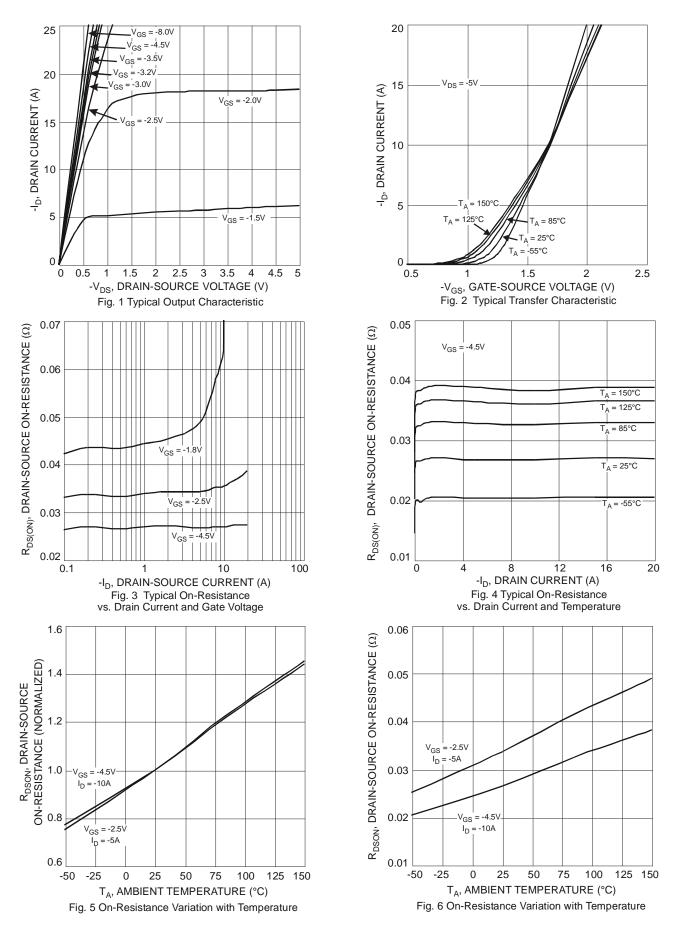
5. Device mounted on FR-4 PCB with 2oz. Copper and test pulse width t  $\leq$ 10s.

Bepetitive rating, pulse width limited by junction temperature.
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing.

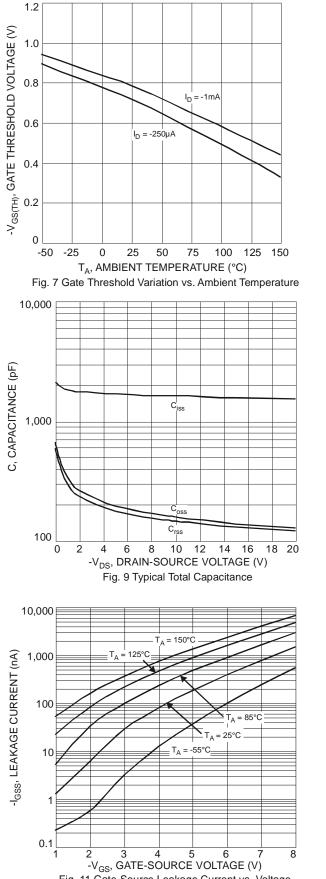
Notes:



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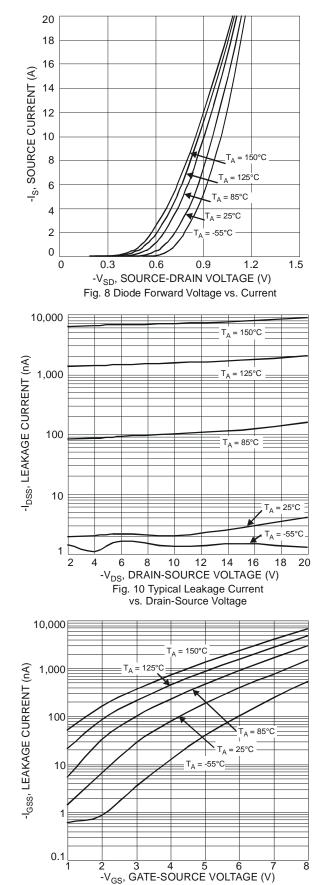
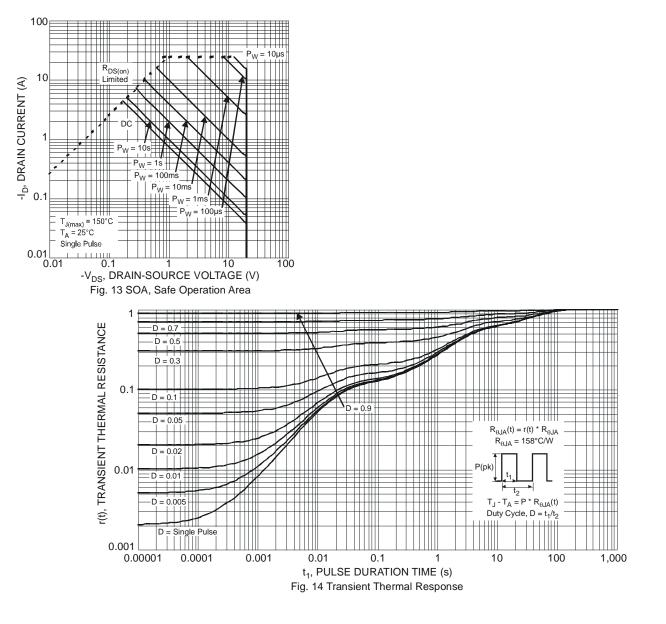


Fig. 12 Gate-Source Leakage Current vs. Voltage

Fig. 11 Gate-Source Leakage Current vs. Voltage

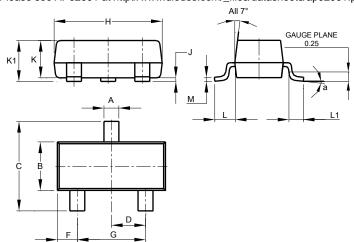






# **Package Outline Dimensions**

Please see AP02001 at http://www.diodes.com/\_files/datasheets/ap02001.pdf for the latest version.

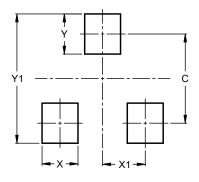


	SOT23							
Dim	Min	Max	Тур					
Α	0.37	0.51	0.40					
В	1.20	1.40	1.30					
С	2.30	2.50	2.40					
D	0.89	1.03	0.915					
F	0.45	0.60	0.535					
G	1.78	2.05	1.83					
Н	2.80	3.00	2.90					
J	0.013	0.10	0.05					
ĸ	0.890	1.00	0.975					
K1	0.903	1.10	1.025					
L	0.45	0.61	0.55					
L1	0.25	0.55	0.40					
М	0.085	0.150	0.110					
а	0°	8°	-					
All	All Dimensions in mm							



### Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/\_files/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9

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