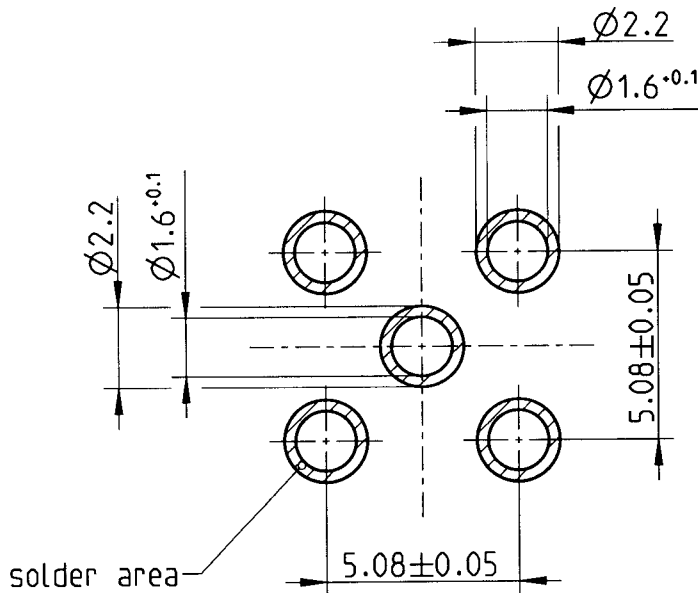


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Leiterplatten-Layout
PCB layout
B 30b



A wide variety of transmissionline topologies and pcb-parameters like permittivity, substrate thickness, and board-stackup are applied by customers. These parameters have a strong impact on the high frequency performance of the mounted connector. Please note, that the given layout is not optimised to fit all of the possible board configurations regarding RF-performance, it represents a recommendation for optimum solderability of the connector. In order to guarantee optimum high frequency properties of the connector, an RF-analysis of the connector to board transition is recommended.

Formelart: TCC-FB-05-PC-A4-EMR-INT
Date: 14.01.2006 10:00:00
Version: 1.0

-METRIC-



ISO-Projektion
Methode E

Rosenberger Hochfrequenztechnik 84526 Tittmoning Pro/ENGINEER		general tolerance ISO 2768 RN 006-01 m-H dimensions <0.5 and symmetry		scale: 5:1	weight(g): surface(mm ²):
		material:		title: Leiterplatten-Layout PCB layout	
part-no.: MB_30B		sheet: 1		of: 1	
rev. change-no a00 04-0709	name A_Nobis	date 22.11.2004	distribution to: FE X AZ . QSM . RMT .	remarks: .	