

# **Customer Information Notification**

Issue Date: 24-Jan-2019 Effective Date: 25-Jan-2019

Dear Product Change Notices Newark,

Here's your personalized quality information concerning products Newark purchased from NXP. For detailed information we invite you to view this notification online

This notice is NXP Company Proprietary.

# 2018120161



Change Category				
[] Wafer Fab Process	[] Assembly Process	[] Product Marking	[] Test Location	[] Design
[] Wafer Fab Materials	[] Assembly Materials	[] Mechanical Specification	[]Test Process	[] Errata
[] Wafer Fab Location	[] Assembly Location	[] Packing/Shipping/Labeling	[] Test   Equipment	[] Electrica spec./Test coverage
[] Firmware	[X] Other -	Safety Manual		3

MC9S12ZVxSM Safety Manual Update from Rev4 to Rev5

## **Description**

NXP Semiconductors announces a Safety Manual update for the MC9S12ZVx Family from rev.4 to rev.5. The revision history included in the updated document provides a detailed description of the changes. Main changes are summarized below.

- 1.Added SM\_1033 to section 3.3 Clock Safety Mechanisms.
- 2.Added SM\_1034 and SM\_1035 to section 3.4 -Power Supply Safety Mechanisms
- 3.Updated SM\_0051.
- 4.Added SM\_1031, SM\_1030, SM\_1032, and SM\_0039 to section 3.5 Processing Unit Safety Mechanisms.
- 5.Added SM\_1037 to section 3.3.
- 6.Added SM\_1036 to section 3.7.

The MC9S12ZVx Family Safety Manual is attached to this notice, and can be found under User Guide section at:

## S12ZVM MCUs

https://www.nxp.com/products/processors-and-microcontrollers/additional-processors-and-mcus/8-16-bit-mcus/16-bit-s12-magniv/s12zvm-mixed-signal-mcu-for-automotive-industrial-motor-control-applications:S12ZVM?tab=Documentation\_Tab

#### S12ZVMA MCUs

https://www.nxp.com/products/processors-and-microcontrollers/additional-processors-and-mcus/8-16-bit-mcus/16-bit-s12-magniv/mixed-signal-mcu-for-automotive-industrial-motor-control-applications:S12ZVMA?tab=Documentation\_Tab

#### S32ZVMB MCUs

https://www.nxp.com/products/processors-and-microcontrollers/additional-processors-and-mcus/8-16-bit-mcus/16-bit-s12-magniv/mixed-signal-mcu-for-automotive-industrial-motor-control-applications:S12ZVMB?tab=Documentation Tab

#### S12ZVC MCUs

https://www.nxp.com/products/processors-and-microcontrollers/additional-processors-and-mcus/8-16-bit-mcus/16-bit-s12-magniv/s12zvc-mixed-signal-mcu-for-automotive-industrial-can-applications:S12ZVC?tab=Documentation\_Tab

## S12ZVL

https://www.nxp.com/products/processors-and-microcontrollers/additional-processors-and-mcus/8-16-bit-mcus/16-bit-s12-magniv/s12zvl-mixed-signal-mcu-for-automotive-industrial-lin-applications:S12ZVL?tab=Documentation Tab

Corresponding ZVEI Delta Qualification Matrix ID: SEM-DS-03

#### Reason

The Safety Manual has been updated to provide additional technical clarification on some device features. **Identification of Affected Products** 

Product identification does not change

# Anticipated Impact on Form, Fit, Function, Reliability or Quality

No impact on form, fit, function, reliability or quality.

## **Additional information**

Affected products and sales history information: see attached file Additional documents: view online



## **Contact and Support**

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

NameTony DongPositionProduct Engineere-mail addressvingwei.dong@nxp.com

At NXP Semiconductors we are constantly striving to improve our product and processes to ensure they reach the highest possible Quality Standards.

Customer Focus, Passion to Win.

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# NXP Quality Management Team.

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High Tech Campus, 5656 AG Eindhoven, The Netherlands

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