

RD3P07BBH

Nch 100V 80A Power MOSFET

| | _ |
|----------------------------|-------|
| V_{DSS} | 100V |
| R _{DS(on)} (Max.) | 7.7mΩ |
| I _D | ±80A |
| P _D | 89W |

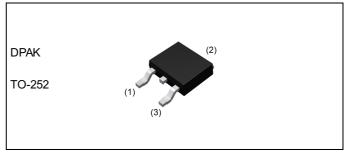
Features

- 1) Low on resistance
- 2) High Power Package(TO-252)
- 3) Pb-free plating; RoHS compliant
- 4) Halogen free

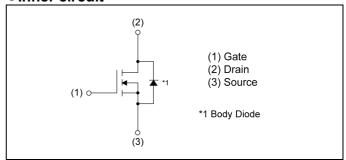
Application

Switching

Outline



●Inner circuit



Packaging specifications

| | jing opodinoanono | |
|------|-------------------|------------------|
| | Packing | Embossed Tape |
| | Reel size (mm) | 330 |
| Туре | Tape width (mm) | 16 |
| | Quantity (pcs) | 2500 |
| | Taping code | TL1 |
| | Marking | RD3P07BBH |

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

| Para | Symbol | Value | Unit | |
|-----------------------------|--|--------------------|-------------|----|
| Drain - Source voltage | V _{DSS} | 100 | V | |
| Cartinuous dusin suumant | Silicon limit (V _{GS} =10V) | I _D *1 | ±80 | А |
| Continuous drain current | T _c = 25°C (V _{GS} =10V) | I _D *2 | ±70 | А |
| Pulsed drain current | | I _{DP} *3 | ±320 | А |
| Gate - Source voltage | | V_{GSS} | ±20 | V |
| Avalanche current, single p | ulse | I _{AS} *4 | 23 | Α |
| Avalanche energy, single p | ulse | E _{AS} *4 | 43 | mJ |
| Power dissipation | | P _D *2 | 89 | W |
| Junction temperature | | T _j | 150 | °C |
| Operating junction and stor | age temperature range | T _{stg} | -55 to +150 | °C |

Thermal resistance

| Parameter | Symbol | Values | | | Lleit |
|-------------------------------------|----------------------|--------|------|------|-------|
| | | Min. | Тур. | Max. | Unit |
| Thermal resistance, junction - case | R _{thJC} *2 | 1 | 1 | 1.40 | °C/W |

● Electrical characteristics (T_a = 25°C)

| Doromotor | Symbol | Conditions | Values | | | Lleit |
|--|---|--|--------|------|------|-------|
| Parameter | Symbol | | | Тур. | Max. | Unit |
| Drain - Source breakdown voltage | V _{(BR)DSS} | $V_{GS} = 0V$, $I_D = 1mA$ | 100 | - | - | V |
| Breakdown voltage temperature coefficient | $\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$ | I _D = 1mA referenced to 25°C | - | 62.3 | - | mV/°C |
| Zero gate voltage drain current | I _{DSS} | V _{DS} = 100V, V _{GS} = 0V | - | - | 5 | μA |
| Gate - Source leakage current | I _{GSS} | $V_{GS} = \pm 20V, V_{DS} = 0V$ | 1 | - | ±500 | nA |
| Gate threshold voltage | V _{GS(th)} | $V_{DS} = V_{GS}$, $I_D = 1mA$ | 2.0 | - | 4.0 | V |
| Gate threshold voltage temperature coefficient | $\frac{\DeltaV_{GS(th)}}{\DeltaT_j}$ | I _D = 1mA referenced to 25°C | - | -4.5 | - | mV/°C |
| Static drain - source | D *5 | V _{GS} = 10V, I _D = 70A | - | 5.9 | 7.7 | O |
| on - state resistance | R _{DS(on)} *5 | V _{GS} = 6V, I _D = 35A | - | 7.5 | 11.2 | mΩ |
| Gate resistance | R _G - | | - | 0.9 | - | Ω |
| Forward Transfer Admittance | Y _{fs} *5 | V _{DS} = 5V, I _D = 35A | 26 | - | - | S |

^{*1} Limited by silicon chip capability.

^{*2} T_c =25°C, Limited only by maximum temperature allowed.

^{*3} Pw \leq 10 μ s , Duty cycle \leq 1%

^{*4} L \simeq 0.1mH, V_{DD} = 50V, R_G = 25 Ω , Starting T_j = 25 $^{\circ}$ C Fig.3-1,3-2

^{*5} Pulsed

● Electrical characteristics (T_a = 25°C)

| Dorameter | Symbol | Conditions | | Unit | | |
|------------------------------|------------------------|-----------------------------------|------|------|------|------|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Unit |
| Input capacitance | C _{iss} | V _{GS} = 0V | - | 2410 | - | |
| Output capacitance | C _{oss} | V _{DS} = 50V | - | 460 | - | pF |
| Reverse transfer capacitance | C _{rss} | f = 1MHz | - | 20 | - | |
| Turn - on delay time | t _{d(on)} *5 | $V_{DD} \simeq 50V, V_{GS} = 10V$ | 1 | 24 | 1 | |
| Rise time | t r*5 | I _D = 35A | 1 | 25 | 1 | no |
| Turn - off delay time | t _{d(off)} *5 | R _L ≃ 1.42Ω | - | 58 | - | ns |
| Fall time | t _f *4 | $R_G = 10\Omega$ | - | 29 | - | |

● Gate charge characteristics (T_a = 25°C)

| Doromotor | Cymahal | Conditions - | | | Values | | l lait |
|----------------------|--------------------|----------------------|-----------------------|------|--------|------|------------|
| Parameter | Symbol | | | Min. | Тур. | Max. | Unit |
| Total gate charge | O *5 | | V _{GS} = 10V | - | 38.0 | - | |
| Total gate charge | Q_g^{*5} | $V_{DD} \simeq 50V$ | | - | 25.0 | - | " C |
| Gate - Source charge | Q _{gs} *5 | I _D = 50A | V _{GS} = 6V | - | 8.5 | - | nC |
| Gate - Drain charge | Q _{gd} *5 | | | - | 9.8 | - | |

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

| Parameter | Symbol | Conditions | | Unit | | |
|----------------------------|--------------------|---|------|------|------|-------|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Offic |
| Continuous forward current | I _S | T _a = 25°C | 1 | - | 70 | Α |
| Pulse forward current | I _{SP} *3 | 1 _a - 25 C | - | - | 320 | Α |
| Forward voltage | V _{SD} *5 | $V_{GS} = 0V, I_{S} = 70A$ | - | - | 1.2 | V |
| Reverse recovery time | t _{rr} *5 | I _S = 50A, V _{GS} =0V | - | 63 | - | ns |
| Reverse recovery charge | Q _{rr} *5 | di/dt = 100A/μs | - | 125 | 1 | nC |

Fig.1 Power Dissipation Derating Curve

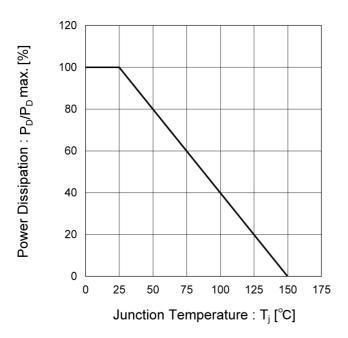
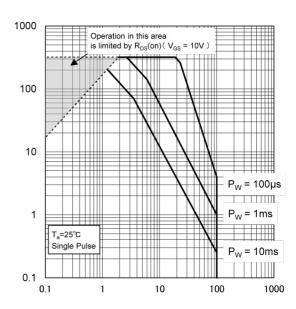


Fig.2 Maximum Safe Operating Area



Drain Current : I_D [A]

Drain - Source Voltage: V_{DS} [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

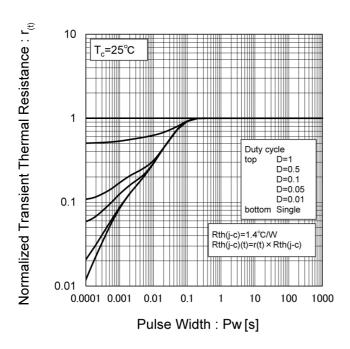


Fig.4 Single Pulse Maximum Power Dissipation

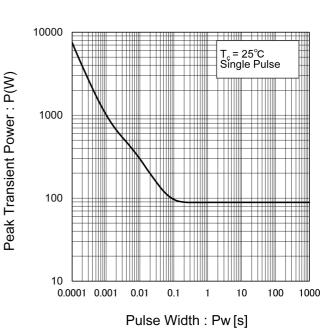


Fig.5 Typical Output Characteristics(I)

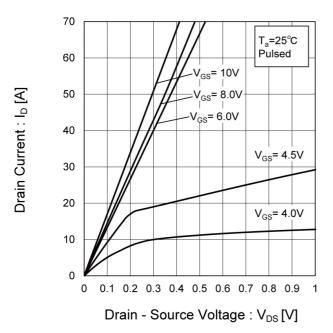
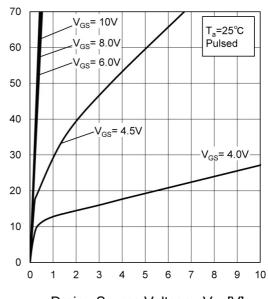


Fig.6 Typical Output Characteristics(II)



Drain Current : I_D [A]

Drain - Source Voltage : V_{DS} [V]

Fig.7 Breakdown Voltage vs.

Junction Temperature

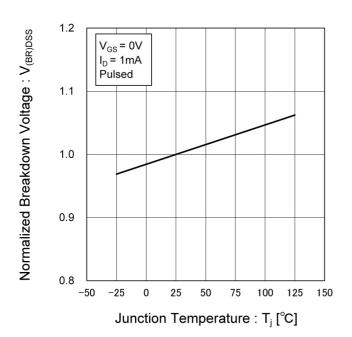
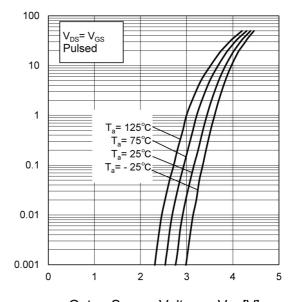


Fig.8 Typical Transfer Characteristics



Gate - Source Voltage : $V_{GS}[V]$

Drain Current : I_D [A]

Fig.9 Gate Threshold Voltage vs.
Junction Temperature

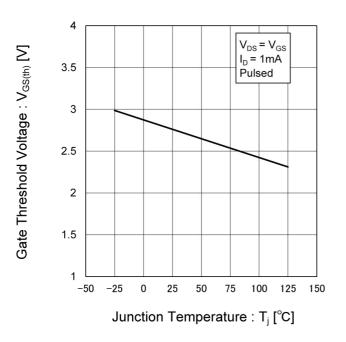


Fig.10 Forward Transfer Admittance vs.
Drain Current

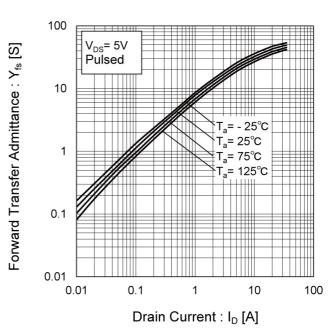


Fig.11 Drain Current Derating Curve

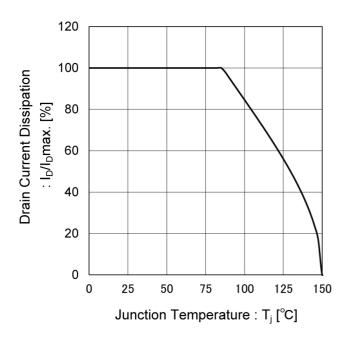


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

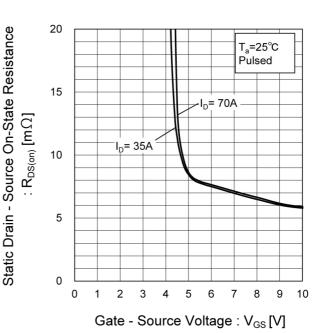


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

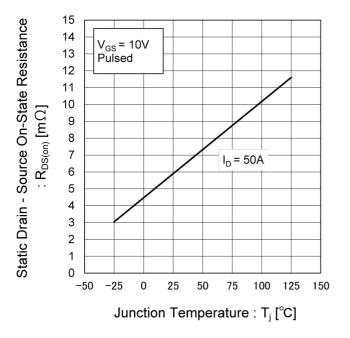


Fig.14 Static Drain - Source On - State
Resistance vs. Drain Current (I)

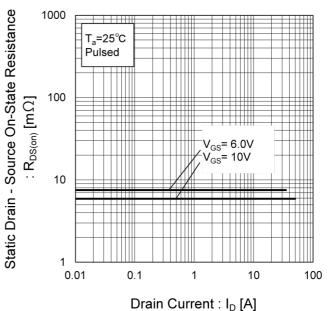


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

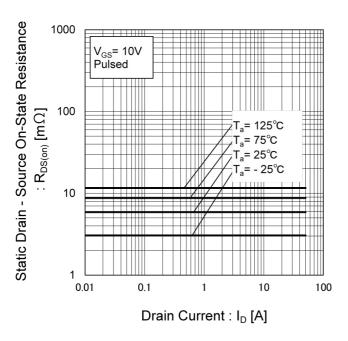


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

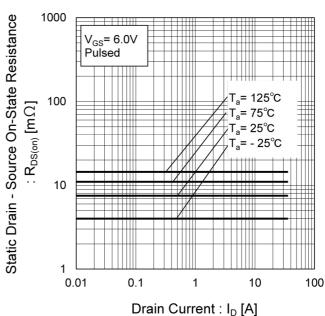


Fig.17 Typical Capacitances vs.

Drain - Source Voltage

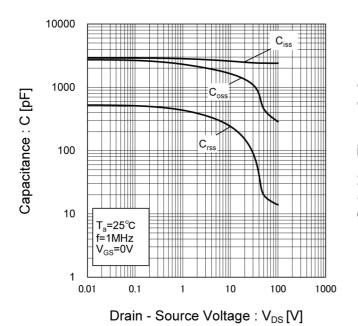


Fig.18 Switching Characteristics

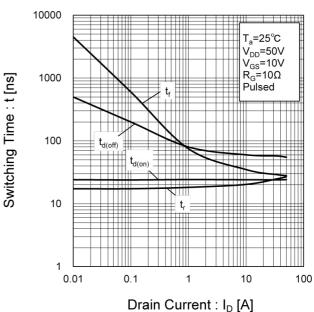


Fig.19 Typical Gate Charge

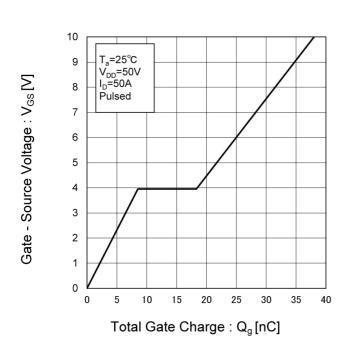
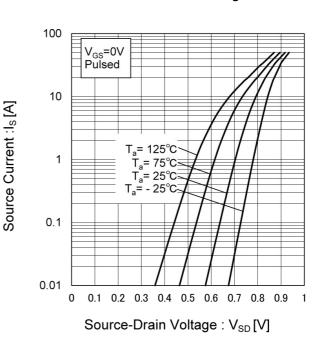


Fig.20 Source Current vs.

Source Drain Voltage



Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

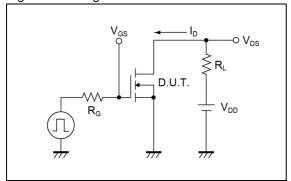


Fig.1-2 Switching Waveforms

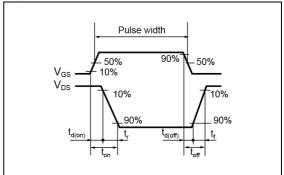


Fig.2-1 Gate Charge Measurement Circuit

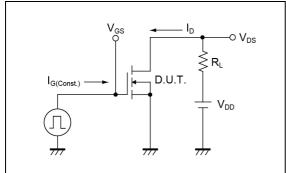


Fig.2-2 Gate Charge Waveform

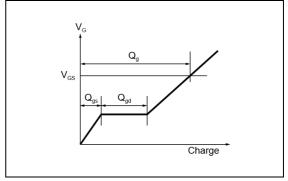


Fig.3-1 Avalanche Measurement Circuit

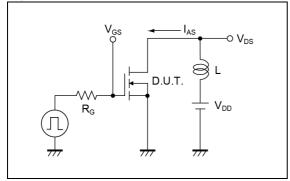
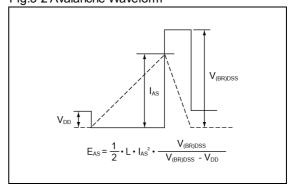
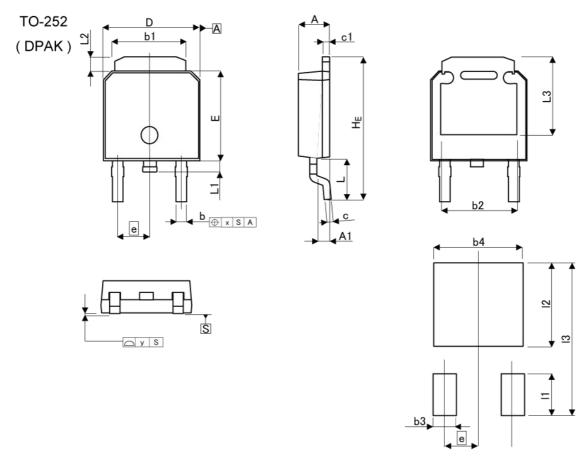


Fig.3-2 Avalanche Waveform



Dimensions



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

| DIM | MILIME | TERS | INC | HES |
|--------|--------|-------|------------------|-------|
| DIIVI | MIN | MAX | MIN | MAX |
| Α | 2.20 | 2.40 | 0.087 | 0.094 |
| A1 | 0.70 | 1.10 | 0.028 | 0.043 |
| b | 0.60 | 0.90 | 0.024 | 0.035 |
| b1 | 5.20 | 5.50 | 0.205 | 0.217 |
| b2 | 4. | 80 | 0.1 | 189 |
| С | 0.40 | 0.60 | 0.016 | 0.024 |
| c1 | 0.40 | 0.60 | 0.016 | 0.024 |
| D | 6.40 | 6.80 | 0.252 | 0.268 |
| е | 2. | 30 | 0.0 | 91 |
| E | 6.00 | 6.40 | 0.236 | 0.252 |
| HE | 9.40 | 10.40 | 0.370 | 0.409 |
| L | 2. | 90 | 0.1 | 114 |
| L1 | 0.60 | 1.00 | 0.024 | 0.039 |
| L2 | 0.70 | 1.30 | 0.028 | 0.051 |
| L3 | 5. | 30 | 0.2 | 209 |
| Х | - | 0.25 | - | 0.010 |
| у | - | 0.10 | ı - i | 0.004 |
| D.11.4 | MILIME | TERS | INC | HES |
| DIM | MIN | MAX | MIN | MAX |
| b3 | - | 1.15 | - | 0.045 |
| b4 | - | 5.55 | . . . | 0.219 |
| I1 | - | 2.77 | (-) | 0.109 |
| 12 | - | 5.50 | (5) | 0.217 |
| 13 | - | 10.40 | (4) | 0.409 |

Dimension in mm/inches



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|---------|-------------|------------|-----------|
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| CLASSIV | CLASSⅢ | CLASSⅢ | CLASSⅢ |

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 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
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- 8. Confirm that operation temperature is within the specified range described in the product specification.
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 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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