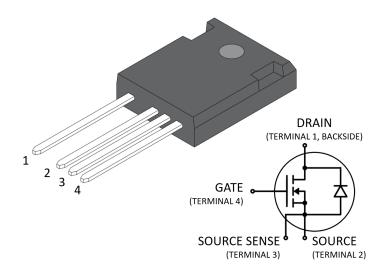


### MSC025SMA120B4 Silicon Carbide N-Channel Power MOSFET

## 1 Product Overview

The silicon carbide (SiC) power MOSFET product line from Microsemi increases the performance over silicon MOSFET and silicon IGBT solutions while lowering the total cost of ownership for high-voltage applications. The MSC025SMA120B4 device is a 1200 V, 25 m $\Omega$  SiC MOSFET in a TO-247 4-lead package with a source sense.



### 1.1 Features

The following are key features of the MSC025SMA120B4 device:

- Low capacitances and low gate charge
- Fast switching speed due to low internal gate resistance (ESR)
- Stable operation at high junction temperature, T<sub>J(max)</sub> = 175 °C
- Fast and reliable body diode
- Superior avalanche ruggedness
- RoHS compliant

#### 1.2 Benefits

The following are benefits of the MSC025SMA120B4 device:

- High efficiency to enable lighter, more compact system
- Simple to drive and easy to parallel
- Improved thermal capabilities and lower switching losses
- Eliminates the need for external freewheeling diode
- Lower system cost of ownership

### 1.3 Applications

The MSC025SMA120B4 device is designed for the following applications:

- PV inverter, converter, and industrial motor drives
- Smart grid transmission and distribution
- Induction heating and welding
- H/EV powertrain and EV charger
- Power supply and distribution



# **2** Device Specifications

This section shows the specifications for the MSC025SMA120B4 device.

## 2.1 Absolute Maximum Ratings

The following table shows the absolute maximum ratings for the MSC025SMA120B4 device.

**Table 1 • Absolute Maximum Ratings** 

Symbol	Characteristic	Ratings	Unit
VDSS	Drain source voltage	1200	V
ID	Continuous drain current at Tc = 25 °C	103	Α
	Continuous drain current at Tc = 100 °C	73	<del>-</del>
I <sub>DM</sub>	Pulsed drain current <sup>1</sup>	275	_
V <sub>G</sub> s	Gate-source voltage	23 to -10	V
PD	Total power dissipation at Tc = 25 °C	500	W
	Linear derating factor	3.33	W/°C

#### Note:

1. Repetitive rating: pulse width and case temperature limited by maximum junction temperature.

The following table shows the thermal and mechanical characteristics for the MSC025SMA120B4 device.

Table 2 • Thermal and Mechanical Characteristics

Symbol	Characteristic	Min	Тур	Max	Unit
Rөлс	Junction-to-case thermal resistance		0.20	0.30	°C/W
T <sub>J</sub>	Operating junction temperature	<b>-</b> 55		175	°C
Тѕтс	Storage temperature	<b>-</b> 55		150	-
TL	Soldering temperature for 10 seconds (1.6 mm from case)			260	=
	Mounting torque, 6-32 or M3 screw			10	lbf-in
				1.1	N-m
Wt	Package weight		0.22		OZ
			6.2		g



# 2.2 Electrical Performance

The following table shows the static characteristics for the MSC025SMA120B4 device.  $T_J = 25$  °C unless otherwise specified.

**Table 3 • Static Characteristics** 

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
V(BR) DSS	Drain-source breakdown voltage	$V_{GS} = 0 \text{ V, } I_D = 100  \mu\text{A}$	1200			V
R <sub>DS(on)</sub>	Drain-source on resistance 1	$V_{GS} = 20 \text{ V}, I_D = 40 \text{ A}$		25	31	mΩ
V <sub>GS(th)</sub>	Gate-source threshold voltage	$V_{GS} = V_{DS}$ , $I_D = 3 \text{ mA}$	1.8	2.8		V
$\Delta V_{GS(th)}/\Delta T_J$	Threshold voltage coefficient	$V_{GS} = V_{DS}$ , $I_D = 3 \text{ mA}$		-3.5		mV/°C
loss	Zero gate voltage drain current	V <sub>DS</sub> = 1200 V, V <sub>GS</sub> = 0 V			100	μΑ
		V <sub>DS</sub> = 1200 V, V <sub>GS</sub> = 0 V T <sub>J</sub> = 125 °C			500	-
Igss	Gate-source leakage current	V <sub>GS</sub> = 20 V/–10 V			±100	nA

#### Note:

1. Pulse test: pulse width  $< 380 \mu s$ , duty cycle < 2%.

The following table shows the dynamic characteristics for the MSC025SMA120B4 device.  $T_J = 25$  °C unless otherwise specified.

**Table 4 • Dynamic Characteristics** 

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
C iss	Input capacitance	$V_{GS} = 0 \text{ V}, V_{DD} = 1000 \text{ V}, V_{AC} = 25 \text{ mV},$		3020		pF
Crss	Reverse transfer	f = 1 MHz	-	25		-
	capacitance					
Coss	Output capacitance	_		270		_
Qg	Total gate charge	$V_{GS} = -5 \text{ V}/20 \text{ V}, V_{DD} = 800 \text{ V}$		232		nC
Qgs	Gate-source charge	I <sub>D</sub> = 40 A		41		=
Qgd	Gate-drain charge	_		50		=
td(on)	Turn-on delay time	$V_{DD} = 800 \text{ V}, V_{GS} = -5 \text{ V}/20 \text{ V}, I_D = 70 \text{ A}$		18		ns
tr	Current rise time	R <sub>G(ext)</sub> = 2.5 $\Omega^1$ , Freewheeling diode =		35		=
td(off)	Turn-off delay time	MSC025SMA120B4 (Vgs = -5 V)		35		=
tf	Current fall time	_		10		=
Eon	Turn-on switching energy <sup>2</sup>	_		1065		μЈ
Eoff	Turn-off switching energy	_		82		=
td(on)	Turn-on delay time	$V_{DD} = 800 \text{ V}, V_{GS} = -5 \text{ V}/20 \text{ V}, I_D = 50 \text{ A}$		18		ns
tr	Current rise time	R <sub>G(ext)</sub> = 2.5 $\Omega^1$ , Freewheeling diode =		30		=
td(off)	Turn-off delay time	MSC050SDA120B		35		=
tf	Current fall time	_		10		=
Eon	Turn-on switching energy <sup>2</sup>	_		873		μЈ
Eoff	Turn-off switching energy	_		77		=
ESR	Equivalent series resistance	f = 1 MHz, 25 mV, drain short		0.88		Ω
SCWT	Short circuit withstand time	V <sub>DS</sub> = 960 V, V <sub>GS</sub> = 20 V		3		μs



Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
Eas	Avalanche energy, single pulse	V <sub>DS</sub> = 150 V, I <sub>D</sub> = 40 A		3500		mJ

#### Notes:

- 1. R<sub>G</sub> is total gate resistance excluding internal gate driver impedance.
- 2. Eon includes energy of the freewheeling diode.

The following table shows the body diode characteristics for the MSC025SMA120B4 device.  $T_J$  = 25 °C unless otherwise specified.

**Table 5 • Body Diode Characteristics** 

Symbol	Characteristic	<b>Test Conditions</b>	Min	Тур	Max	Unit
V <sub>SD</sub>	Diode forward voltage	$I_{SD} = 40 \text{ A}, V_{GS} = 0 \text{ V}$		4.0		V
		I <sub>SD</sub> = 40 A, V <sub>GS</sub> = -5 V		4.2		V
trr	Reverse recovery time	$I_{SD} = 40 \text{ A}, V_{GS} = -5 \text{ V}$		90		ns
Qrr	Reverse recovery charge	V <sub>DD</sub> = 800 V dl/dt = -1000 A/μs		550		nC
RRM	Reverse recovery current	α, ατ 1000 γ, μ3		13.5		Α

## 2.3 Typical Performance Curves

This section shows the typical performance curves for the MSC025SMA120B4 device.

Figure 1 • Drain Current vs. Drain-to-Source Voltage

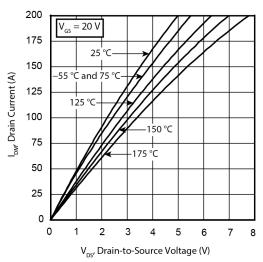


Figure 2 • Drain Current vs. Drain-to-Source Voltage

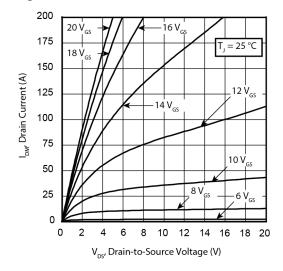




Figure 3 • Drain Current vs. Drain-to-Source Voltage

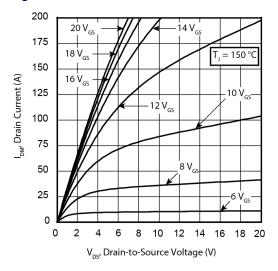


Figure 5 • RDS(on) vs. Junction Temperature

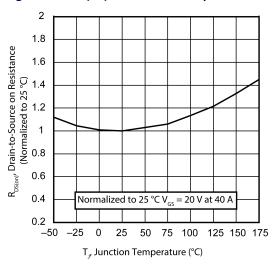


Figure 7 • Capacitance vs. Drain-to-Source Voltage

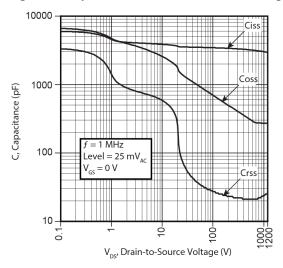


Figure 4 • Drain Current vs. Drain-to-Source Voltage

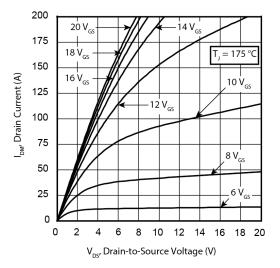


Figure 6 • Gate Charge Characteristics

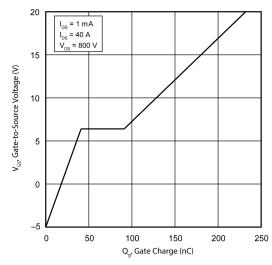


Figure 8 • IDM vs. Gate-to-Source Voltage

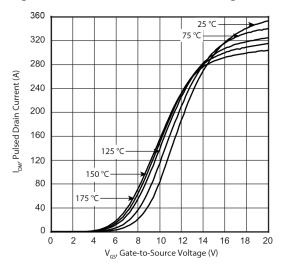




Figure 9 • IDM vs. VDS Third Quadrant Conduction

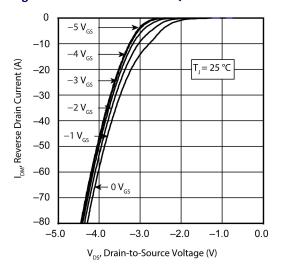


Figure 11 • Threshold Voltage vs. Junction Temp.

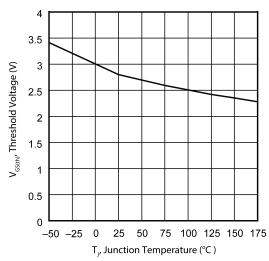


Figure 10 • IDM vs. VDS Third Quadrant Conduction

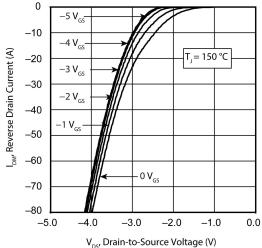


Figure 12 • Forward Safe Operating Area

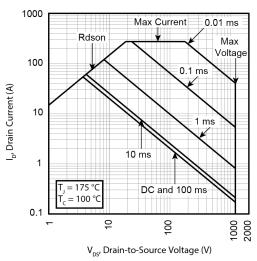
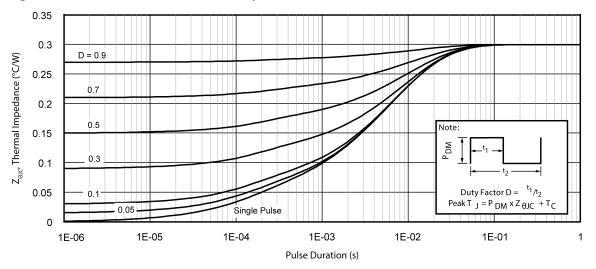


Figure 13 • Maximum Transient Thermal Impedance





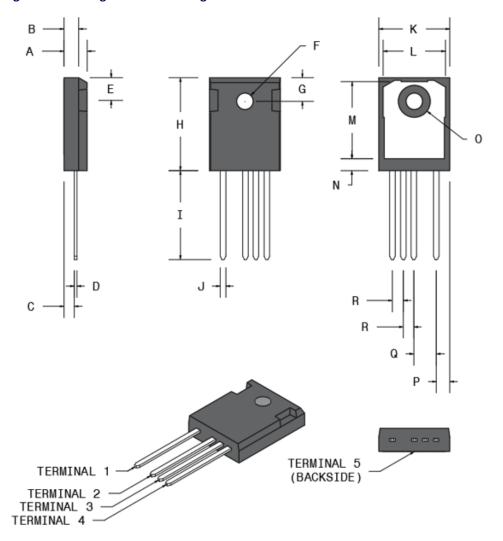
# **3** Package Specification

This section shows the package specification for the MSC025SMA120B4 device.

# 3.1 Package Outline Drawing

The following figure illustrates the TO-247 package drawing for the MSC025SMA120B4 device. The dimensions in the figure below are in millimeters and (inches).

Figure 14 • Package Outline Drawing





The following table shows the TO-247 4-lead dimensions and should be used in conjunction with the package outline drawing.

Table 6 • TO-247-4L Dimensions

Symbol	Min (mm)	Max (mm)	Min (in.)	Max (in.)
Α	4.90	5.17	0.193	0.204
В	1.85	2.11	0.073	0.083
С	2.25	2.51	0.089	0.099
D	0.55	0.68	0.022	0.027
E	5.49	5.74	0.216	0.226
F	3.56	3.66	0.140	0.144
G	6.15 BSC		0.242 BSC	
Н	20.83	21.08	0.820	0.830
I	19.81	20.32	0.780	0.800
J	1.07	1.33	0.042	0.052
K	15.77	16.03	0.621	0.631
L	13.89	14.15	0.547	0.557
М	16.25	16.85	0.640	0.663
N	2.00	2.75	0.079	0.108
0	7.10	7.50	0.280	0.295
Р	2.87 BSC		0.113 BSC	
Q	5.08 BSC		0.200 BSC	
R	2.54 BSC		0.100 BSC	
Terminal 1	Drain			
Terminal 2	Source			
Terminal 3	Source sense			
Terminal 4	Gate			
Terminal 5	Drain			





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