

1.5A Variable / Fixed Output LDO Regulators





BDxxHC5WEFJ BDxxHC5JEFJ

General Description

BDxxHC5WEFJ BDxxHC5JEFJ series devices are LDO regulators with an output current of 1.5A. The output accuracy is $\pm 1\%$ of the output voltage. Both fixed and variable output voltage devices are available. The output voltage of the variable output voltage device can be varied from 1.5 to 7.0V using external resistors. Various fixed output voltage devices that do not use external resistors are also available. It can be used for a wide range of digital appliance applications. It has a small package type: HTSOP-J8 (4.90mm x 6.00mm x 1.00mm). These devices have built in over current protection to protect the device when output is shorted, 0 μ A shutdown mode and thermal shutdown circuit to protect the device during over load conditions. These LDO regulators are usable with ceramic capacitors that enable a smaller layout and longer life.

Features

- +/-1% output voltage accuracy
- Built-in Over Current Protection (OCP)
- Built-in Thermal Shut Down circuit (TSD)
- Zero µA Shutdown mode

Key Specifications

Input Power Supply Voltage range: 4.5V to 8.0VOutput voltage range(Variable type): 1.5V to 7.0V

Output voltage(Fixed type):

1.5V/1.8V/2.5V/3.0V /3.3V/5.0V/6.0V/7.0V

■ Output current:

Shutdown current:

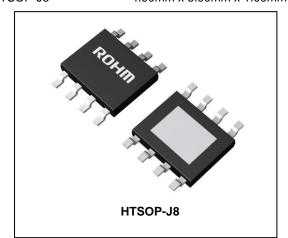
Operating temperature range:

1.54 (Max.)

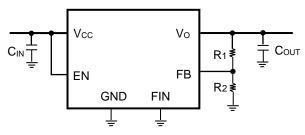
0µA(Typ.)

-25°C to +85°C

●Package HTSOP-J8 (Typ.) (Typ.) (Max.) 4.90mm x 6.00mm x 1.00mm

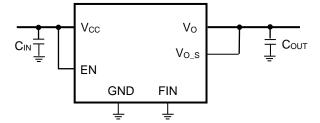


● Typical Application Circuit



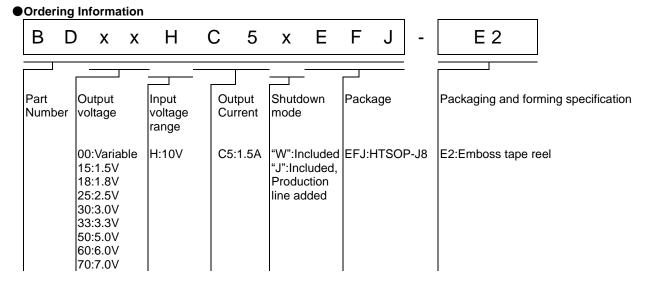
C_{IN}, C_{OUT}: Ceramic Capacitor

Output voltage variable type



C_{IN}, C_{OUT}: Ceramic Capacitor

Output voltage fixed type



Output Voltage	Package	Ordering Code	Production Line ^(Note 1)
Variable	_	BD00HC5WEFJ-E2	A
variable		BD00HC5JEFJ-E2	В
1.5V		BD15HC5WEFJ-E2	A
1.57		BD15HC5JEFJ-E2	В
1.8V		BD18HC5WEFJ-E2	A
1.0 V		BD18HC5JEFJ-E2	В
2.5V		BD25HC5WEFJ-E2	A
2.5 V		BD25HC5JEFJ-E2	В
3.0V	HTSOP-J8	BD30HC5WEFJ-E2	A
3.0 V	H130F-36	BD30HC5JEFJ-E2	В
3.3V		BD33HC5WEFJ-E2	A
3.3 V		BD33HC5JEFJ-E2	В
5.0V		BD50HC5WEFJ-E2	A
3.00		BD50HC5JEFJ-E2	В
6.0V		BD60HC5WEFJ-E2	A
0.07		BD60HC5JEFJ-E2	В
7.0V		BD70HC5WEFJ-E2	A
7.00		BD70HC5JEFJ-E2	В

(Note 1) For the purpose of improving production efficiency, Production Line A and B have a multi-line configuration. Electric characteristics noted in Datasheet does not differ between Production Line A and B. Production Line B is recommended for new product.

●Block Diagram

BD00HC5WEFJ BD00HC5JEFJ (Variable output voltage type)

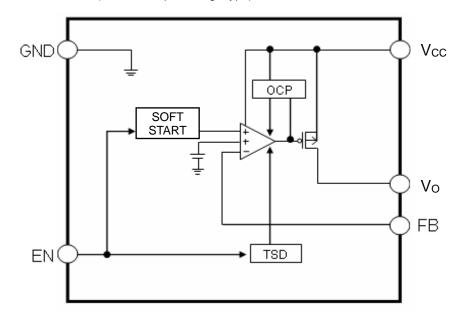
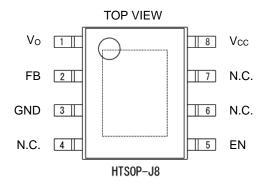


Fig.1 Block Diagram

●Pin Configuration



●Pin Description

Pin No.	Pin name	Pin Function
1	Vo	Output pin
2	FB	Feedback pin
3	GND	GND pin
4	N.C.	No Connect (Connect to GND or leave OPEN)
5	EN	Enable pin
6	N.C.	No Connect (Connect to GND or leave OPEN)
7	N.C.	No Connect (Connect to GND or leave OPEN)
8	Vcc	Input pin
Reverse	FIN	Substrate(Connect to GND)

Block Diagram

BDxxHC5WEFJ BDxxHC5JEFJ (Fixed output voltage type)

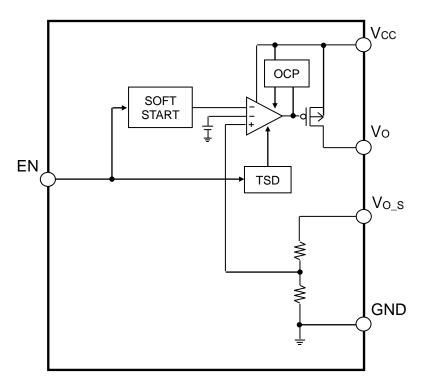
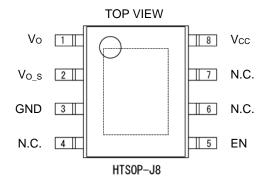


Fig.2 Block Diagram

●Pin Configuration



●Pin Description

Pin No.	Pin name	Pin Function
1	Vo	Output pin
2	V _{O_S}	Output voltage monitor pin
3	GND	GND pin
4	N.C.	No Connect (Connect to GND or leave OPEN)
5	EN	Enable pin
6	N.C.	No Connect (Connect to GND or leave OPEN)
7	N.C.	No Connect (Connect to GND or leave OPEN)
8	Vcc	Input pin
Reverse	FIN	Substrate(Connect to GND)

● Absolute Maximum Ratings (Ta=25°C)

Paramet	er	Symbol	Ratings	Unit
Power supply voltage)	Vcc	-0.3 to 10.0 *1	V
EN voltage		V _{EN}	-0.3 to 10.0	V
Power dissipation	HTSOP-J8	Pd*2	2110 ^{*2}	mW
Operating Temperatu	re Range	Topr	-25 to +85	°C
Storage Temperature	ture Range Tstg -55 to +150		°C	
Junction Temperature	9	Tjmax	+150	°C

^{*1} Not to exceed Pd

●Recommended Operating Ratings (Ta=25°C)

Demonstra	Council of	Rat	1.1-14	
Parameter	Symbol	Min.	Max.	Unit
Input power supply voltage	Vcc	4.5	8.0	V
EN voltage	V _{EN}	0.0	8.0	V
Output voltage setting range	Vo	1.5	7.0	V
Output current	lo	0.0	1.5	Α

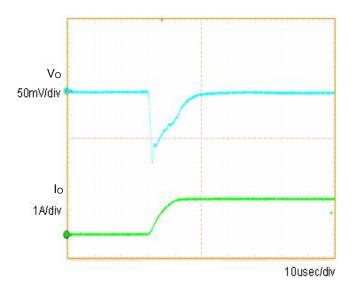
●Electrical Characteristics (Unless otherwise noted, Ta=25°C, EN=3V, V_{CC}=6V, R1=43kΩ, R2=8.2kΩ)

Downster	Curah al		Limits		l læit	Conditions	
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Circuit current at shutdown mode	I _{SD}	-	0	5	μΑ	V _{EN} =0V, OFF mode	
Bias current	Icc	-	600	900	μΑ		
Line regulation	Reg.I	-1	ı	1	%	Vcc=(Vo+0.93V)→8.0V	
Load regulation	Reg Io	-1.5	ı	1.5	%	Io=0→1.5A	
Minimum dropout voltage1	V _{CO1}	-	0.12	0.19	V	Vcc=5V, Io=300mA	
Minimum dropout voltage2	V _{CO2}	-	0.24	0.38	V	Vcc=5V, Io=600mA	
Minimum dropout voltage3	V _{CO3}	-	0.36	0.56	V	Vcc=5V, Io=900mA	
Minimum dropout voltage4	V _{CO4}	-	0.48	0.75	V	Vcc=5V, Io=1.2A	
Minimum dropout voltage5	V _{CO5}	-	0.60	0.93	V	Vcc=5V, Io=1.5A	
Output reference voltage(Variable type)	V _{FB}	0.792	0.800	0.808	V	Io=0A	
Output voltage(Fixed type)	Vo	Vo×0.99	Vo	Vo×1.01	V	Io=0A	
EN Low voltage	V _{EN} (Low)	0	ı	0.8	V		
EN High voltage	V _{EN} (High)	2.4	-	8.0	V		
EN Bias current	I _{EN}	1	3	9	μA		

^{*2} Reduced by 16.9mW/°C for each increase in Ta of 1°C over 25°C. (when mounted on a board 70mm×70mm×1.6mm glass-epoxy board, two layer)

●Typical Performance Curves

(Unless otherwise noted, Ta=25°C, EN=3V, V_{CC} =6V, R1=43k Ω , R2=8.2k Ω)



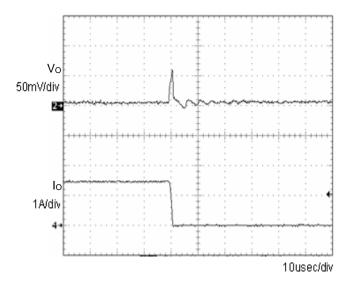
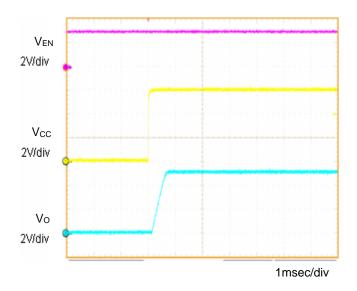


Fig.3 Transient Response (0→1.5A) Co=1µF

Fig.4
Transient Response
(1.5→0A)
Co=1µF



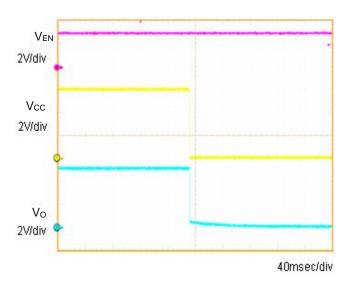
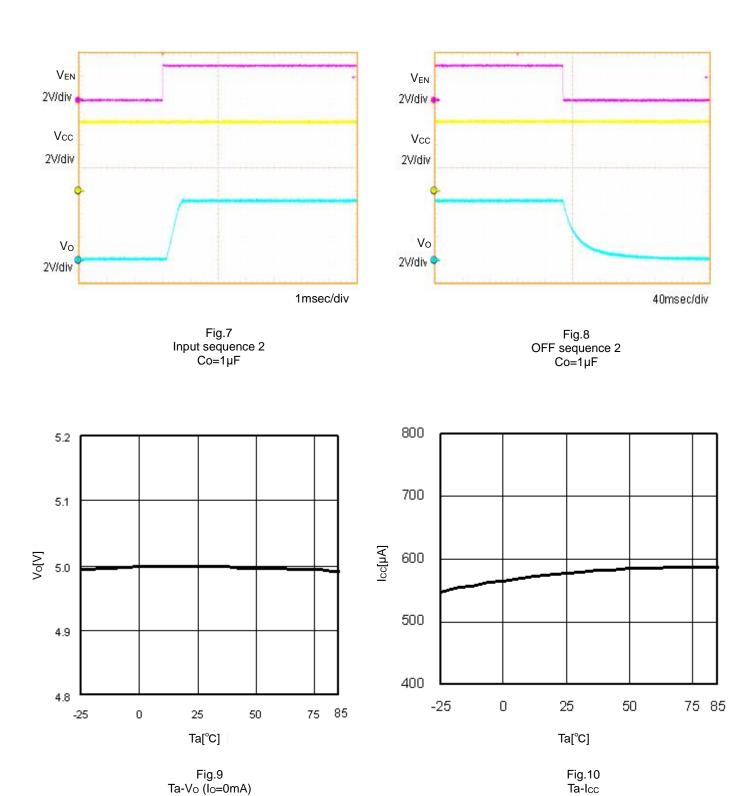
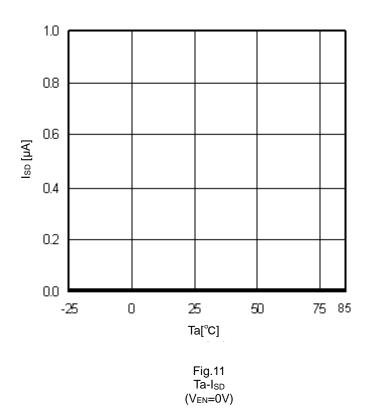
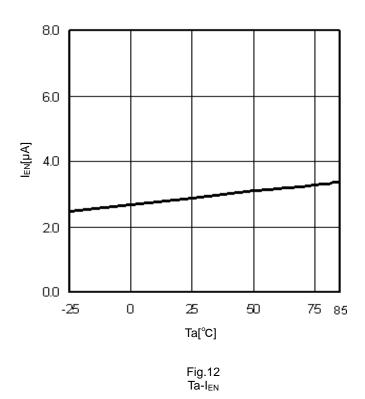


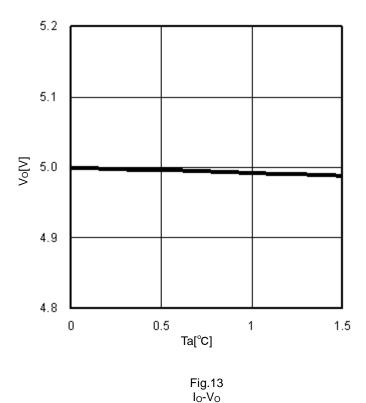
Fig.5 Input sequence 1 Co=1µF

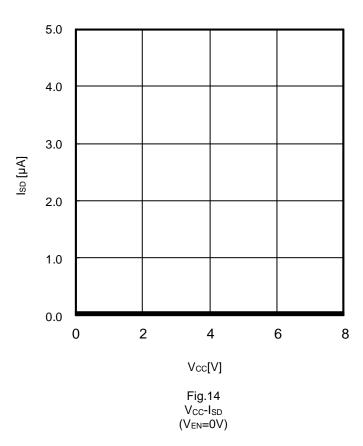
Fig.6 OFF sequence 1 Co=1µF

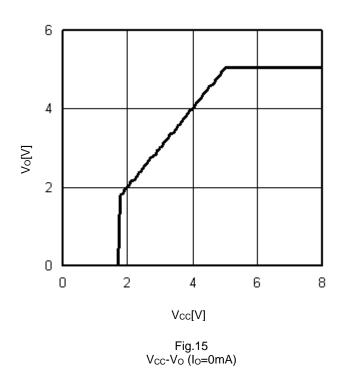


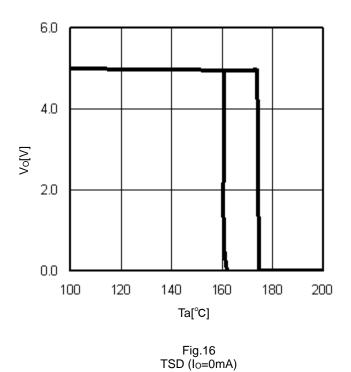


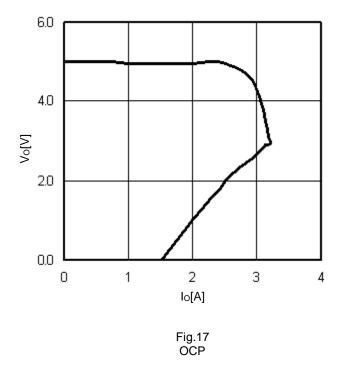












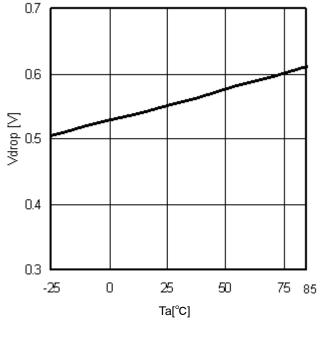
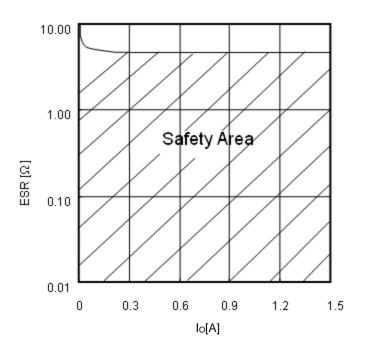


Fig.18
Minimum dropout Voltage1
(Vcc=5V、Io=-1.5A)



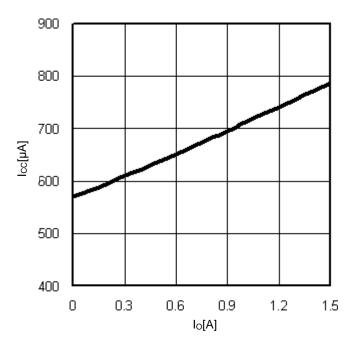


Fig.20

lo-lcc

Fig.19 ESR condencer

10

Frequency[KHz]

Fig.21

PSRR(Io=0mA)



0.7 0.6 0.5Vdrop[V] 0A0.3 0.201 0 0 0.3 0.80,9 1.5 12 lo[A]

Fig.22
Minimum dropout Voltage 2
(V_{CC}=4.5V, Ta=25°C)

1

100

80

60

40

20

0

0.1

PSRR [dB]

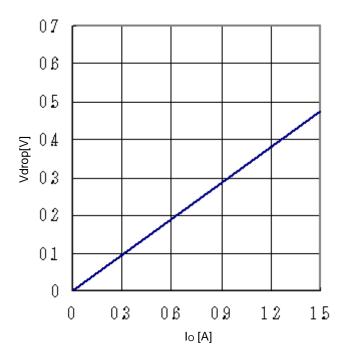


Fig.23 Minimum dropout Voltage 3 (Vcc=6V、Ta=25°C)

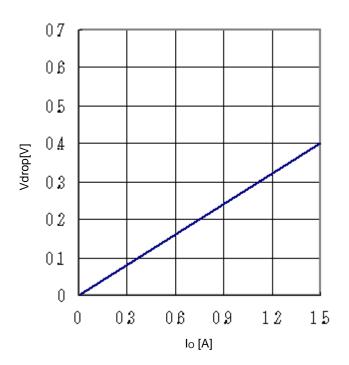
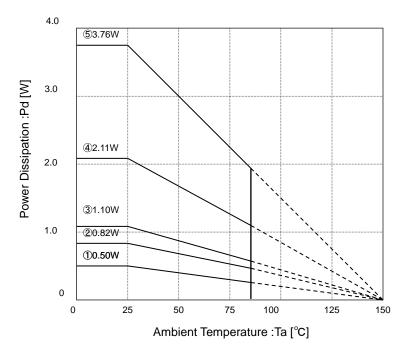


Fig.24
Minimum dropout Voltage 4
(V_{CC}=8V、Ta=25°C)

Power Dissipation

@HTSOP-J8



Measurement condition: mounted on a ROHM board,

Substrate size: 70mm × 70mm × 1.6mm (Substrate with thermal via)

- · Solder the thermal pad to Ground
- ① IC only θ i-a=249.5°C/W
- ② 1-layer (copper foil are :0mm × 0mm) θ j-a=153.2°C/W
- 3 2-layer (copper foil are :15mm × 15mm) θ j-a=113.6°C/W
- 4 2-layer (copper foil are :70mm × 70mm) θ j-a=59.2°C/W
- 5 4-layer (copper foil are :70mm × 70mm)θ j-a=33.3°C/W

Thermal design should ensure operation within the following conditions. Note that the temperatures listed are the allowed temperature limits and thermal design should allow sufficient margin beyond these limits.

- 1. Ambient temperature Ta can be no higher than 85°C.
- 2. Chip junction temperature (Tj) can be no higher than 150°C.

Chip junction temperature can be determined as follows:

Calculation based on ambient temperature (Ta)

Tj=Ta+ θ j-a×P

<Reference values>

 θ j-a: HTSOP-J8 153.2°C/W 1-layer substrate (copper foil density 0mm × 0mm) 113.6°C/W 2-layer substrate (copper foil density 15mm × 15mm) 59.2°C/W 2-layer substrate (copper foil density 70mm × 70mm) 33.3°C/W 4-layer substrate (copper foil density 70mm × 70mm) Substrate size: 70mm × 70mm × 1.6mm (substrate with thermal via)

Most of the heat loss that occurs in the BDxxHC5WEFJ BDxxHC5JEFJ series is generated from the output Pch FET. Power loss is determined by the total V_{CC} - V_{O} voltage and output current. Be sure to confirm the system input and output voltage as well as the output current conditions in relation to the heat dissipation characteristics of the V_{CC} and V_{O} in the design. Bearing in mind that heat dissipation may vary substantially depending on the substrate employed (due to the power package incorporated in the BDxxHC5WEFJ BDxxHC5JEFJ series) make certain to factor conditions such as substrate size into the thermal design.

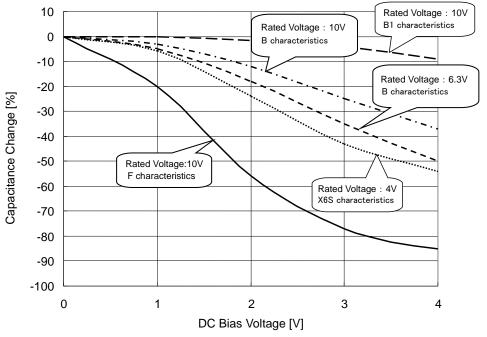
Power consumption [W] =
$$\Big\{ \text{Input voltage (Vcc)} - \text{Output voltage (Vo)} \Big\} \times \text{Io (Ave)} \Big\}$$

Example) Where V_{CC}=5.0V, V_O=3.3V, I_O(Ave) = 0.5A,
Power consumption [W] = $\Big\{ 5.0V - 3.3V \Big\} \times 0.5A$
=0.85[W]

●Input-to-Output Capacitor

It is recommended that a capacitor (over 1uF) is placed near pins between the input pin and GND as well as the output pin and GND. A capacitor, between input pin and GND, is valid when the power supply impedance is high or trace is long. Also, as for the capacitor between the output pin and GND, the greater the capacitance, the more sustainable the line regulation will be and the capacitor will make improvements of characteristics depending on the load. However, please check the actual functionality of this part by mounting it on a board for the actual application. Ceramic capacitors usually have different, thermal and equivalent series resistance characteristics, and moreover capacitance decreases gradually in use.

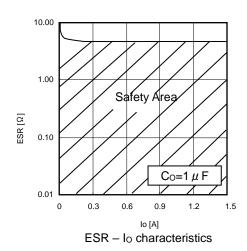
For additional details, please check with the manufacturer, and select the best ceramic capacitor for your application.



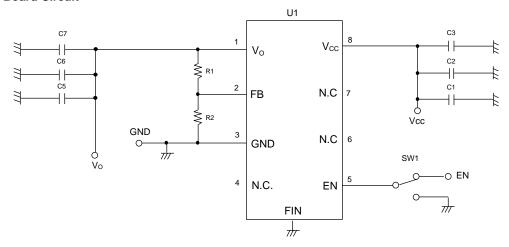
Ceramic capacitor capacity – DC bias characteristics (Characteristics example)

● Equivalent Series Resistance ESR (Output Capacitor)

To prevent oscillations, please attach a capacitor between V_0 and GND. Capacitors usually have ESR (Equivalent Series Resistance). Operation will be stable in the ESR-lo range shown to the right. Ceramic, tantalum and electrolytic Capacitors have different ESR values, so please ensure that you are using a capacitor that operates in the stable operating region shown on the right. Finally, please evaluate in the actual application.



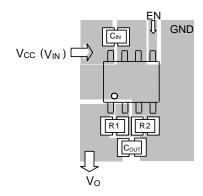
●Evaluation Board Circuit



●Evaluation Board Parts List

Designation	Value	Part No.	Company	Designation	Value	Part No.	Company
R1	43kΩ	MCR01PZPZF4302	ROHM	C4	-	-	-
R2	8.2kΩ	MCR01PZPZF8201	ROHM	C5	1µF	CM105B105K16A	KYOCERA
R3	-	-	-	C6			
R4	-	-	-	C7	-	-	-
R5	-	-	-	C8	ı	•	-
R6	-	-	-	C9	-	-	-
C1	1µF	CM105B105K16A	KYOCERA	C10	ı	•	-
C2	-	-		U1	-	BD00HC5WEFJ BD00HC5JEFJ	ROHM
C3	-	=		U2	-	-	-

Board Layout



PCB layout considerations:

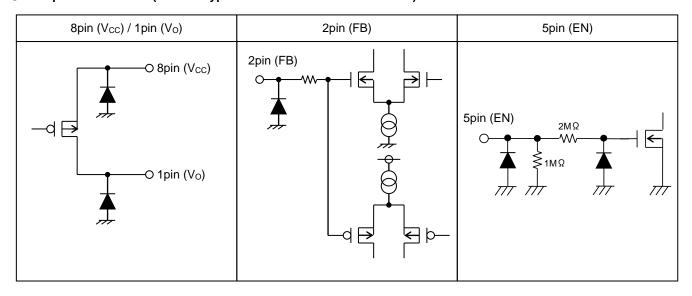
- Input capacitor C_{IN} connected to V_{CC} (Vin) should be placed as close to V_{CC}(V_{IN}) pin as possible. Output capacitor C_{OUT} also should be placed as close to IC pin as possible. In case the part is connected to inner layer GND plane, please use several through holes.
- FB pin has comparatively high impedance and can be affected by noise, so stray capacitance should be as small as possible. Please take care of this during layout.
- Please make GND pattern wide enough to handle thermal dissipation.
- For output voltage setting(BD00HC5WEFJ BD00HC5JEFJ)

 Output voltage can be set by FB pin voltage (0.800V typ.) and external resistance R1, R2.

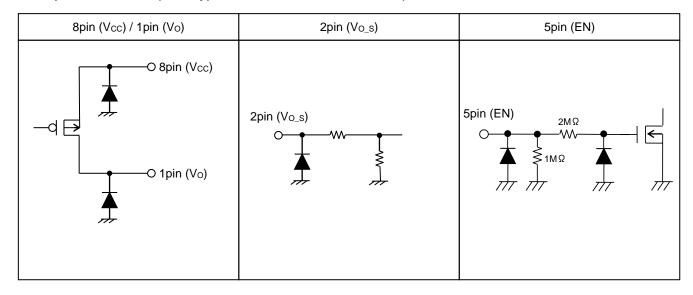
$$V_0 = V_{FB} \times \frac{R1+R2}{R2}$$

(The use of resistors with R1+R2=1k to 90k is recommended)

●I/O Equivalent Circuits (Vairable type :BD00HC5WEFJ BD00HC5JEFJ)



●I/O Equivalent Circuits (Fixed type :BDxxHC5WEFJ BDxxHC5JEFJ)



Operational Notes

(1) Absolute maximum ratings

An excess in the absolute maximum ratings, such as supply voltage, temperature range of operating conditions, etc., can break down the device, thus making it impossible to identify the damage mode, such as a short circuit or an open circuit. If there is any possibility of exposure over the rated values, please consider adding circuit protection devices such as fuses.

(2) Connecting the power supply connector backward

Connecting of the power supply in reverse polarity can damage the IC. Take precautions when connecting the power supply lines. An external direction diode can be added.

(3) Power supply lines

Design the PCB layout pattern to provide low impedance GND and supply lines. To obtain a low noise ground and supply line, separate the ground section and supply lines of the digital and analog blocks. Furthermore, for all power supply terminals to ICs, connect a capacitor between the power supply and GND terminal. When using electrolytic capacitors in a circuit, note that capacitance values are reduced at low temperatures and over time.

(4) GND voltage

The potential of the GND pin must be minimum potential under all operating conditions.

(5) Thermal design

Use a thermal design that allows for a sufficient margin in light of the power dissipation (Pd) in actual operating conditions.

(6) Inter-pin shorts and mounting errors

Use caution when positioning the IC for mounting on printed circuit boards. The IC may be damaged if there is any connection error or if pins are shorted together.

(7) Actions in strong electromagnetic field

Use caution when using the IC in the presence of a strong electromagnetic field as doing so may cause the IC to malfunction.

(8) ASO

When using the IC, set the output transistor so that it does not exceed absolute maximum ratings or ASO.

(9) Thermal shutdown circuit

The IC incorporates a built-in thermal shutdown circuit (TSD circuit). The thermal shutdown circuit (TSD circuit) is designed only to shut the IC off to prevent thermal runaway. It is not designed to protect the IC or guarantee its operation. Do not continue to use the IC after operating this circuit or use the IC in an environment where the operation of this circuit is assumed.

	TSD ON Temperature [°C] (typ.)	Hysteresis Temperature [°C] (typ.)
BDxxHC5WEFJ BDxxHC5JEFJ	175	15

(10) Testing on application boards

When testing the IC on an application board, connecting a capacitor to a pin with low impedance subjects the IC to stress. Always discharge capacitors after each process or step. Always turn the IC's power supply off before connecting it to or removing it from a jig or fixture during the inspection process. Ground the IC during assembly steps as an antistatic measure. Use similar precaution when transporting or storing the IC.

(11) Regarding input pin of the IC

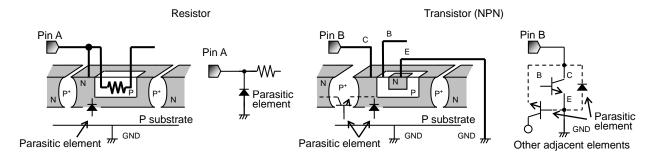
This monolithic IC contains P+ isolation and P substrate layers between adjacent elements in order to keep them isolated. P-N junctions are formed at the intersection of these P layers with the N layers of other elements, creating a parasitic diode or transistor. For example, the relation between each potential is as follows:

When GND > Pin A and GND > Pin B, the P-N junction operates as a parasitic diode.

When GND > Pin B, the P-N junction operates as a parasitic transistor.

Parasitic diodes can occur inevitable in the structure of the IC.

The operation of parasitic diodes can result in mutual interference among circuits, operational faults, or physical damage. Accordingly, methods by which parasitic diodes operate, such as applying a voltage that is lower than the GND (P substrate) voltage to an input pin, should not be used.

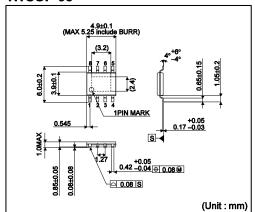


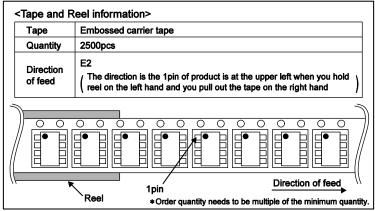
(12) Ground Wiring Pattern.

When using both small signal and large current GND patterns, it is recommended to isolate the two ground patterns, placing a single ground point at the ground potential of application so that the pattern wiring resistance and voltage variations caused by large currents do not cause variations in the small signal ground voltage. Be careful not to change the GND wiring pattern of any external components, either.

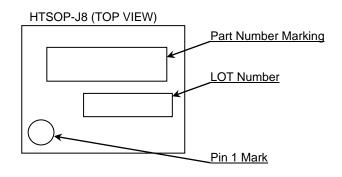
● Physical Dimension Tape and Reel Information

HTSOP-J8





Marking Diagram



Part Number Marking	Part Number
00HC5W	BD00HC5WEFJ
15HC5W	BD15HC5WEFJ
18HC5W	BD18HC5WEFJ
25HC5W	BD25HC5WEFJ
30HC5W	BD30HC5WEFJ
33HC5W	BD33HC5WEFJ
50HC5W	BD50HC5WEFJ
60HC5W	BD60HC5WEFJ
70HC5W	BD70HC5WEFJ
00HC5J	BD00HC5JEFJ
15HC5J	BD15HC5JEFJ
18HC5J	BD18HC5JEFJ
25HC5J	BD25HC5JEFJ
30HC5J	BD30HC5JEFJ
33HC5J	BD33HC5JEFJ
50HC5J	BD50HC5JEFJ
60HC5J	BD60HC5JEFJ
70HC5J	BD70HC5JEFJ

Revision History

<u></u>		
Date	Revision	Changes
25.July.2012	001	New Release
27.Dec.2012	002	The description was modified.
5.Aug.2013	003	The description was modified.
28.July.2014	004	In order to match the revision number of Japanese.
14.July.2014	005	P5 Power supply voltage, EN voltage 10V -> -0.3 to 10V
		P8 Modify Fig14
22.Jan.2024	006	BDxxHC5JEFJ added

Notice

Precaution on using ROHM Products

1. Our Products are designed and manufactured for application in ordinary electronic equipment (such as AV equipment, OA equipment, telecommunication equipment, home electronic appliances, amusement equipment, etc.). If you intend to use our Products in devices requiring extremely high reliability (such as medical equipment (Note 1), transport equipment, traffic equipment, aircraft/spacecraft, nuclear power controllers, fuel controllers, car equipment including car accessories, safety devices, etc.) and whose malfunction or failure may cause loss of human life, bodily injury or serious damage to property ("Specific Applications"), please consult with the ROHM sales representative in advance. Unless otherwise agreed in writing by ROHM in advance, ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of any ROHM's Products for Specific Applications.

(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA	EU	CHINA
CLASSⅢ	CI ACCIII	CLASS II b	CI VCCIII
CLASSIV	CLASSII	CLASSⅢ	CLASSⅢ

- 2. ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
 - [a] Installation of protection circuits or other protective devices to improve system safety
 - [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure
- 3. Our Products are designed and manufactured for use under standard conditions and not under any special or extraordinary environments or conditions, as exemplified below. Accordingly, ROHM shall not be in any way responsible or liable for any damages, expenses or losses arising from the use of any ROHM's Products under any special or extraordinary environments or conditions. If you intend to use our Products under any special or extraordinary environments or conditions (as exemplified below), your independent verification and confirmation of product performance, reliability, etc, prior to use, must be necessary:
 - [a] Use of our Products in any types of liquid, including water, oils, chemicals, and organic solvents
 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

Precautions Regarding Application Examples and External Circuits

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
- 2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

Precaution for Product Label

A two-dimensional barcode printed on ROHM Products label is for ROHM's internal use only.

Precaution for Disposition

When disposing Products please dispose them properly using an authorized industry waste company.

Precaution for Foreign Exchange and Foreign Trade act

Since concerned goods might be fallen under listed items of export control prescribed by Foreign exchange and Foreign trade act, please consult with ROHM in case of export.

Precaution Regarding Intellectual Property Rights

- 1. All information and data including but not limited to application example contained in this document is for reference only. ROHM does not warrant that foregoing information or data will not infringe any intellectual property rights or any other rights of any third party regarding such information or data.
- 2. ROHM shall not have any obligations where the claims, actions or demands arising from the combination of the Products with other articles such as components, circuits, systems or external equipment (including software).
- 3. No license, expressly or implied, is granted hereby under any intellectual property rights or other rights of ROHM or any third parties with respect to the Products or the information contained in this document. Provided, however, that ROHM will not assert its intellectual property rights or other rights against you or your customers to the extent necessary to manufacture or sell products containing the Products, subject to the terms and conditions herein.

Other Precaution

- 1. This document may not be reprinted or reproduced, in whole or in part, without prior written consent of ROHM.
- 2. The Products may not be disassembled, converted, modified, reproduced or otherwise changed without prior written consent of ROHM.
- In no event shall you use in any way whatsoever the Products and the related technical information contained in the Products or this document for any military purposes, including but not limited to, the development of mass-destruction weapons.
- 4. The proper names of companies or products described in this document are trademarks or registered trademarks of ROHM, its affiliated companies or third parties.

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General Precaution

- 1. Before you use our Products, you are requested to carefully read this document and fully understand its contents. ROHM shall not be in any way responsible or liable for failure, malfunction or accident arising from the use of any ROHM's Products against warning, caution or note contained in this document.
- 2. All information contained in this document is current as of the issuing date and subject to change without any prior notice. Before purchasing or using ROHM's Products, please confirm the latest information with a ROHM sales representative.
- 3. The information contained in this document is provided on an "as is" basis and ROHM does not warrant that all information contained in this document is accurate and/or error-free. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties resulting from inaccuracy or errors of or concerning such information.

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