



## Product Change Notification - KSRA-07HVQF409

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**Date:**

11 Dec 2019

**Product Category:**

Others; 8-bit Microcontrollers; Temperature Sensors; Memory; Capacitive Touch Sensors

**Affected CPNs:****Notification subject:**

CCB 4031 Initial Notice: Qualification of Microchip Technology Tempe – Fab 2 (TMGR) as an additional fabrication site for selected Atmel products.

**Notification text:****PCN Status:**

Initial notification.

**PCN Type:**

Manufacturing Change

**Microchip Parts Affected:**

Please open one of the icons found in the Affected CPNs section above.

NOTE: For your convenience Microchip includes identical files in two formats (.pdf and .xls).

**Description of Change:**

Qualification of Microchip Technology Tempe - Fab 2 (TMGR) as an additional fabrication site for selected Atmel products.

**Pre Change:**

Fabricated at Microchip Technology Colorado - Fab 5 (MCSO) using 6 inch wafer

**Post Change:**

Fabricated at Microchip Technology Colorado - Fab 5 (MCSO) using 6 inch wafer or Microchip Technology Tempe - Fab 2 (TMGR) using 8 inch wafer

**Pre and Post Change Summary:**

	Pre Change	Post Change	
<b>Fabrication Supplier and Location</b>	Microchip Technology Colorado - Fab 5 (MCSO)	Microchip Technology Colorado - Fab 5 (MCSO)	Microchip Technology Tempe - Fab 2 (TMGR)
<b>Wafer Diameter</b>	6 inches	6 inches	8 inches
<b>Die size</b>	No change	No change	No change
<b>Quality Certification</b>	ISO9001/TS16949 or IATF16949	ISO9001/TS16949 or IATF16949	ISO9001/TS16949 or IATF16949

**Impacts to Data Sheet:**

None

**Change Impact:**

None

**Reason for Change:**

To improve manufacturability and on-time delivery performance by qualifying a second fabrication source at Microchip Technology Tempe - Fab 2 (TMGR) fabrication site.

**Change Implementation Status:**

In Progress

**Estimated Qualification Completion Date:**

June 2020

Note: Please be advised the qualification completion times may be extended because of unforeseen business conditions however implementation will not occur until after qualification has completed and



a final PCN has been issued. The final PCN will include the qualification report and estimated first ship date. Also note that after the estimated first ship date guided in the final PCN customers may receive pre and post change parts.

**Time Table Summary:**

Workweek	December 2019				-->	June 2020				
	49	50	51	52		23	24	25	26	27
Initial PCN Issue Date		X								
Qual Report Availability										X
Final PCN Issue Date										X

**Method to Identify Change:**

Traceability code

**Qualification Plan:**

Please open the attachments included with this PCN labeled as PCN\_#\_Qual\_Plan.

**Revision History:**

**December 11, 2019:** Issued initial notification.

The change described in this PCN does not alter Microchip's current regulatory compliance regarding the material content of the applicable products.

**Attachment(s):**

[PCN\\_KSRA-07HVQF409\\_Qual\\_Plan.pdf](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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If you wish to [change your PCN profile, including opt out](#), please go to the [PCN home page](#) select login and sign into your myMicrochip account. Select a profile option from the left navigation bar and make the applicable selections.

Affected Catalog Part Numbers (CPN)

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ATMEGA1284P-MU  
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ATMEGA1284P-AU  
ATMEGA1284-AU  
ATMEGA1284P-MUR  
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ATMEGA1284P-AUR  
ATMEGA1284-AUR  
ATTINY88-PU  
ATTINY88-MMU  
ATTINY88-MMH  
ATTINY88-MU  
ATTINY88-AU  
ATTINY88-MMUR  
ATTINY88-MMHR  
ATTINY88-MUR  
AT42QT1110-MUR  
AT42QT2100-MUR  
ATTINY88-MURA1  
ATTINY88-AUR  
AT42QT1110-AUR  
ATTINY88-15MZ  
AT42QT1110-MZ  
ATTINY88HGM-15MZ  
ATTINY88HGM-15MZ-V01  
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AT25040B-XPD-T  
AT25040B-XPD-T-35KV01  
AT25040B-MAPD-T  
AT25040B-MAPD-E  
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AT25020B-SSHL-B-899  
AT25020B-XHL-B  
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AT25020B-SSHL-T-899  
AT25020B-SS9M-T  
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AT25020B-X9M-T  
AT25020B-MAHL-T  
AT25020B-MAHL-T-899  
AT25020B-MAHL-E  
AT25020B-MA9M-T  
AT25020B-MA9M-E  
AT25020B-SSPD-T  
AT25020B-SSPD-T-010  
AT25020B-SSPD-T-808  
AT25020B-XPD-T  
AT25020B-MAPD-T  
AT25020B-MAPD-E  
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AT25010B-WWU6L  
AT25010B-WWU27L  
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AT25010B-CUL-T  
AT25010B-MAHL-T  
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AT25010B-MA9M-T  
AT25010B-MA9M-E  
AT25010B-SSPD-T  
AT25010B-XPD-T  
AT25010B-MAPD-T  
AT25010B-MAPD-E  
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AT34C04-MA5MAU-T  
AT34C04-MA5M-T  
AT34C04-MA5MAK-T  
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ATTINY87-XU  
ATTINY87-SU

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ATTINY87-MU  
ATTINY167-MMU  
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ATTINY87-XUR  
ATTINY87-SUR  
ATTINY167-SUR  
ATTINY87-MUR  
ATTINY167-MUR  
ATTINY167-MMUR  
ATTINY167-MMURB20  
ATTINY87-A15XZ  
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ATTINY87-A15SZ  
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ATTINY167-A15MZ-V02  
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ATMEGA169PA-MCHR  
ATMEGA165PA-MUR  
ATMEGA165A-MUR  
ATMEGA169A-MUR  
ATMEGA169PA-MUR  
ATMEGA165PA-AUR  
ATMEGA165A-AUR  
ATMEGA169A-AUR  
ATMEGA169PA-AUR  
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ATMEGA32U2-AU  
ATMEGA32U2-MUR  
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AT90PWM161OS-16MF  
AT90PWM161OS-16SN  
AT90PWM161-16SN

AT90PWM161OS-16MN  
AT90PWM161-16MN  
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AT90PWM161-16MNR  
AT90PWM161OS-16SFR  
AT90PWM161OS-16MFR



# **QUALIFICATION PLAN SUMMARY**

**PCN #: KSRA-07HVQF409**

**Date:  
November 01, 2019**

**Qualification of Microchip Technology Tempe – Fab 2 (TMGR)  
as an additional fabrication site for selected Atmel products.**

**Purpose: Qualification of Microchip Technology Tempe – Fab 2 (TMGR) as an additional fabrication site for selected Atmel products.**

**CCB No: 4031**

**Die Qualification Plan: 3 lots per QCI-39000**

<b>Test / Evaluation</b>	<b>Test Conditions / Parameters</b>
Operating (Dynamic) Life	<ul style="list-style-type: none"><li>• 150°C for 408 hrs at Vdd max</li></ul>
High Temp. Retention Bake (DLT) / High Temperature Operating Life (HTOL)	<ul style="list-style-type: none"><li>• 504 hrs @ 175°C.</li></ul>
Endurance Cycling	<ul style="list-style-type: none"><li>• 10,000 flash Erase/Write cycles at 85°C</li><li>• 100,000 EEPROM Erase/Write cycles at 85°C</li></ul>
ESD HBM	<ul style="list-style-type: none"><li>• Tested at 500V, 1000V, and 2000V</li></ul>
Latch-up	<ul style="list-style-type: none"><li>• Should meet a minimum of +/- 105mA injection on I/O pins with a clamp of 1.5 x VMax on the positive injection and 0.5 x VMax on the negative injection. Power supply pin shall be stressed at 1.5 x VMax with a 200mA clamp.</li></ul>
Electrical Validation	<ul style="list-style-type: none"><li>• 30 units from one wafer lot. Datalog parameters at room, hot and cold at vcc min/max and frequency min/max. Collection of this data may be done as part of any qualification test's pre or post functional testing or from a separate group of parts"</li></ul>



## Package Qualification Plan

<b><u>Misc.</u></b>	Assembly site	MMT
	BD Number	BDM-002184/A
	MP Code (MPC)	TBD
	Part Number (CPN)	ZL88601LDG1
	MSL information	MSL-1/260
	Assembly Shipping Media (T/R, Tube/Tray)	Tray
	Base Quantity Multiple (BQM)	1300
	Reliability Site	MTAI
<b><u>Lead-Frame</u></b>	Paddle size	311x311 mils
	Material	A194
	DAP Surface Prep	Ag Ring Plated
	Treatment	BOT
	Process	Etched
	Lead-lock	Yes
	Part Number	TBD
	Lead Plating	Matte Tin
	Strip Size	70x250mm
	Strip Density	120 units//strip
<b><u>Bond Wire</u></b>	Material	CuPdAu
<b><u>Die Attach</u></b>	Part Number	3280
	Conductive	Yes
<b><u>MC</u></b>	Part Number	G700LTD
<b><u>PKG</u></b>	PKG Type	VQFN
	Pin/Ball Count	64
	PKG width/size	9x9x1.0mm

Test Name	Conditions	Sample Size	Min. Qty of Spares per Lot (should be properly)	Qty of Lots	Total Units	Fail Accept Qty	Est. Dur. Days	Test Site	Special Instructions
Standard Pb-free Solderability	J-STD-002; Perform 8 hour steam aging for Matte tin finish and 1 hour steam aging for NiPdAu finish prior to testing. Standard Pb-free: Matte tin/ NiPdAu finish, SAC solder, wetting temp 245°C for both SMD & through hole packages.	22	5	1	27	> 95% lead coverage	5	MTAI	Standard Pb-free solderability is the requirement. SnPb solderability (backward solderability- SMD reflow soldering) is required for any plating related changes and highly recommended for other package BOM changes.
Backward Solderability	J-STD-002 ;Perform 8 hours steam aging for Matte tin finish and 1 hour steam aging for NiPdAu finish prior to testing. Backward: Matte tin/ NiPdAu finish, SnPb solder, wetting temp 215°C for SMD.	22	5	1	27	> 95% lead coverage	5	MTAI	
Wire Bond Pull - WBP	Mil. Std. 883-2011	5	0	3	15	0 fails after TC	5	MMT/MTAI	30 bonds from a minimum of 5 devices.
Wire Bond Shear - WBS	CDF-AEC-Q100-001	5	0	3	15	0	5	MMT/MTAI	30 bonds from a minimum of 5 devices.
Wire Sweep		5	0	3	15	0		MMT	Required for any reduction in wire bond thickness.
Die Shear	Mil. Std. 883-2011	5	0	3	15	0			
Physical Dimensions	Measure per JESD22 B100 and B108	10	0	3	30	0	5	MMT	
External Visual	Mil. Std. 883-2009/2010	All devices prior to submission for qualification testing	0	3	ALL	0	5	MMT/ MTAI	
Preconditioning - Required for surface mount devices	+150°C Bake for 24 hours, moisture loading requirements per MSL level + 3X reflow at peak reflow temperature per Jedec-STD-020E for package type; Electrical test pre and post stress at +25°C MSL1/MSL3 @ 260°C	231	15	3	738	0	15	MTAI  Post test: ASEM	Spares should be properly identified. 77 parts from each lot to be used for HAST, Autoclave, Temp Cycle test.
Biased HAST	+130°C/85% RH for 96 hrs. Electrical test pre and post stress at +25°C.	77	5	3	246	0	10	MTAI  Post test: ASEM	Spares should be properly identified. Use the parts which have gone through Pre-conditioning.
Unbiased HAST	+130°C/85% RH for 96 hrs. Electrical test pre and post stress at +25°C.	77	5	3	246	0	10	MTAI  Post test: ASEM	Spares should be properly identified. Use the parts which have gone through Pre-conditioning.
Temp Cycle	-65°C to +150°C for 500 cycles. Electrical test pre and post stress at hot temp; 3 gram force WBP, on 5 devices from 1 lot, test following Temp Cycle stress.	77	5	3	246	0	15	MTAI  Post test: ASEM	Spares should be properly identified. Use the parts which have gone through Pre-conditioning.