

## DIL24W/SOIC24 ZIF 300mil (ord.no. 70-0878)

- universal adapter, assigned for 300mil SOIC/SOP devices up to 24-pins (for example PLDs, STM ST7xxx series, Cypress CY8Cxxxxx series)
- operation (mechanical) life of ZIF socket - 10.000 actuations
- made in Slovakia

<b>Ord. no</b>	70-0878
<b>Socket</b>	ZIF SOIC24, open top type
<b>Bottom</b>	2 rows, 2x 14 pins, square, 0.6x0.6mm, rows spacing 600mil
<b>Class</b>	Universal
<b>Subclass</b>	DIL/SOIC



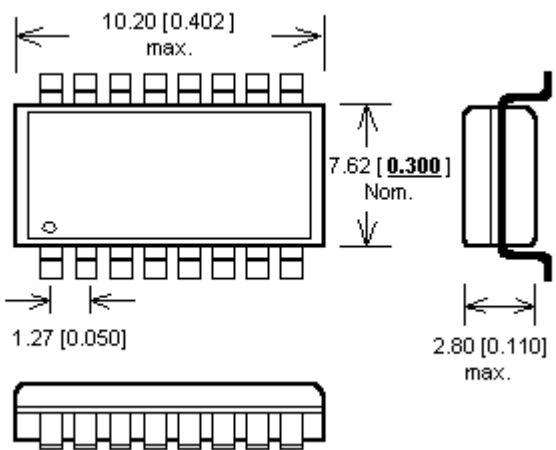
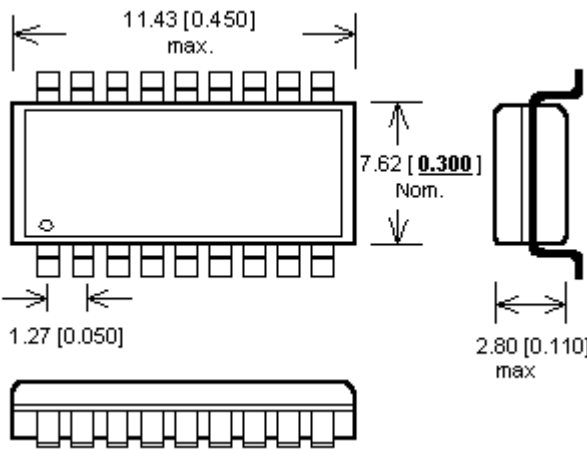
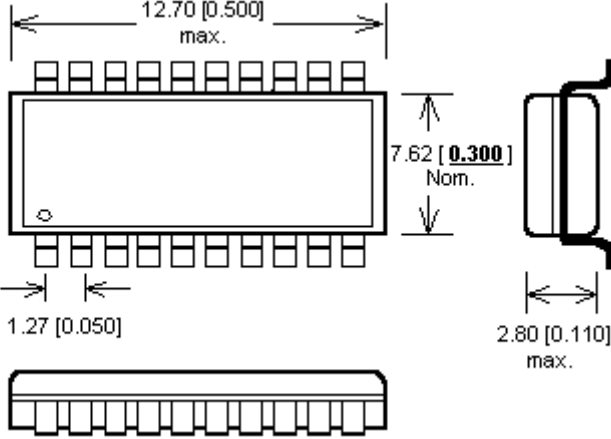
### Adapter manual

- Insert adapter to the device programmer ZIF socket according to the picture placed near of it. If you have some doubts about orientation of this adapter in device programmer ZIF socket, it is valid general rule, the orientation of the text of title is the same as the text on the top of the device programmer.
- Push the top of adapter ZIF socket to open it. Insert the device into the adapter ZIF socket. The right position of the programmed device in adapter ZIF socket is show at picture near (mainly left above) the adapter ZIF socket. Then release adapter ZIF socket. If you use this adapter for devices with less than 20 pins, then place device to the bottom edge of adapter ZIFsocket. Upper contacts stay unconnected.
- Visually check interconnection between device and adapter ZIF socket. If everything looks OK, the device is ready for programming.
- Be careful, because the incorrect insertion of adapter to the device programmer ZIF socket or device to the adapter ZIF socket can damage the programmed device.
- To take out the device push the top of adapter ZIF socket and remove device from it.
- When you finish work with adapter, remove it from the device programmer ZIF socket.
- Do not directly touch the pins of the adapter and adapter ZIF socket, because dirt may cause errors during programming of device.
- For handling with the device we recommended to use a vacuum pick up tool.
- If programming yields isn't 100% when using programming adapter or if some unreliability appears, try to connect one 22nF-100nF multilayer capacitor (that meet the EIA X7R or Z5U specification) between leads of adapter connected to pins VCC and GND pins of programmed chip.

### Software note

- If software version, you're using currently, doesn't contain support for this programming adapter, please download the latest version of software - Regular or OnDemand - from our web site.

**Accepted package(s)**

<p><b>SOIC16</b></p>	 <p>Technical drawing of a SOIC16 package showing dimensions: 10.20 [0.402] max. (width), 7.62 [0.300] Nom. (height), 1.27 [0.050] (pitch), and 2.80 [0.110] max. (lead width). Includes isometric, top, and side views.</p>
<p><b>SOIC18</b></p>	 <p>Technical drawing of a SOIC18 package showing dimensions: 11.43 [0.450] max. (width), 7.62 [0.300] Nom. (height), 1.27 [0.050] (pitch), and 2.80 [0.110] max. (lead width). Includes isometric, top, and side views.</p>
<p><b>SOIC20</b></p>	 <p>Technical drawing of a SOIC20 package showing dimensions: 12.70 [0.500] max. (width), 7.62 [0.300] Nom. (height), 1.27 [0.050] (pitch), and 2.80 [0.110] max. (lead width). Includes isometric, top, and side views.</p>

**SOIC24**

