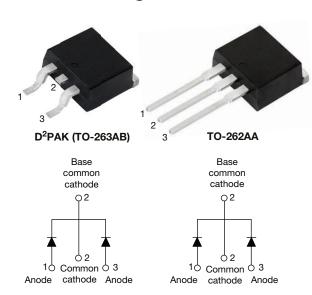


VS-40CTQ045S-M3

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Vishay Semiconductors

High Performance Schottky Rectifier, 2 x 20 A



PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 20 A			
V _R	45 V			
V _F at I _F	0.48 V			
I _{RM} max.	115 mA at 125 °C			
T _J max.	150 °C			
E _{AS}	20 mJ			
Package	D ² PAK (TO-263AB), TO-262AA			
Circuit configuration	Common cathode			

VS-40CTQ045-1-M3

FEATURES

- 150 °C T_J operation
- Center tap configuration
- Very low forward voltage drop
- High frequency operation



- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

This center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS VALUES UNITS					
I _{F(AV)}	Rectangular waveform	40	Α			
V _{RRM}		45	V			
I _{FSM}	t _p = 5 μs sine	1240	Α			
V _F	20 A _{pk} , T _J = 125 °C (per leg)	0.48	V			
T _J	Range	-55 to +150	°C			

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-40CTQ045S-M3 VS-40CTQ045-1-M3	UNITS		
Maximum DC reverse voltage	V_{R}	45	V		
Maximum working peak reverse voltage	V_{RWM}	45	V		



VS-40CTQ045S-M3, VS-40CTQ045-1-M3

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ABSOLUTE MAXIMUM RATINGS						
PARAMETER	AMETER SYMBOL TEST CONDITIONS		VALUES	UNITS		
Maximum average	per leg		50 % duty cycle at T _C = 116 °C, rectangular waveform		20	
forward current See fig. 5	per device	I _{F(AV)}			40	Α
Maximum peak one cycle non-repetitive			5 μs sine or 3 μs rect. pulse	Following any rated load	1240	_ ^
surge current per leg See fig. 7		I _{FSM}	10 ms sine or 6 ms rect. pulse condition and with rated V _{RRM} applied		350	
Non-repetitive avalanche energy per leg		E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 3 \text{A}, L = 4.40 \text{mH}$		20	mJ
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _B typical		3	Α

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST COI	TEST CONDITIONS		
		20 A	T _{.1} = 25 °C	0.53	V
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	40 A	1 J = 25 C	0.68	
	V _{FM} (1)	20 A	T _{.1} = 125 °C	0.48	
		40 A	1j = 125 C	0.67	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V DetectV	3	mA
See fig. 2	IRM '''	T _J = 125 °C	V _R = Rated V _R	115	
Threshold voltage	V _{F(TO)}	T T mayimum		0.27	V
Forward slope resistance	r _t	$T_J = T_J$ maximum		8.72	mΩ
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal range	2800	pF	
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body 8.0			nΗ
Maximum voltage rate of change	dV/dt	Rated V _R 10 000			

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to 150	°C
Maximum thermal resistance, junction to case per leg		D	DC operation	2.0	
Maximum thermal resistance, junction to case per package		- R _{thJC}	DC Operation	1.0	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50	
Approximate weight				2	g
Approximate weight				0.07	OZ.
Mounting torque	minimum			6 (5)	kgf · cm
woulding torque	maximum			12 (10)	(lbf \cdot in)
Madina de de			Case style D ² PAK (TO-263AB)	40CTQ	045S
Marking device			Case style TO-262AA	40CTQ	045-1



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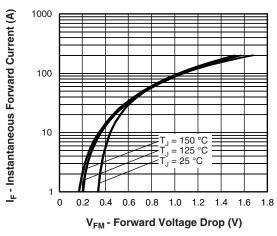


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

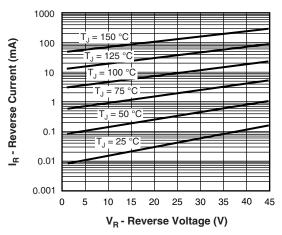


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

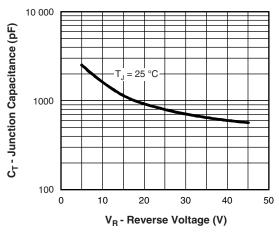


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

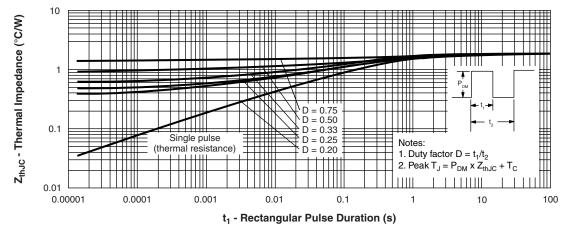


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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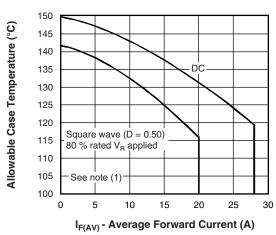


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

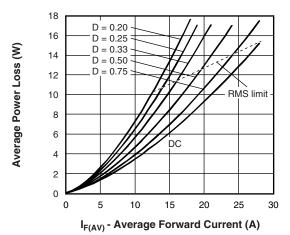


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

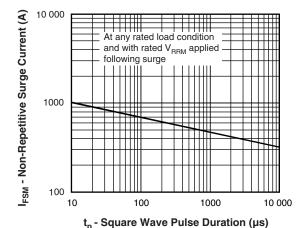


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

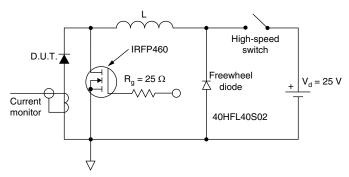


Fig. 8 - Unclamped Inductive Test Circuit

Note

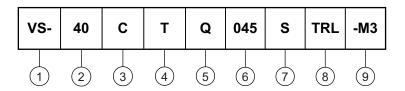
Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{th,JC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 10 V



Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (40 A)

3 - Circuit configuration: C = common cathode

4 - T = TO-220

5 - Schottky "Q" series

6 - Voltage rating (045 = 45 V)

7 - • S = D^2PAK (TO-263AB)

• -1 = TO-262AA

8 - • None = tube

• TRL = tape and reel (left oriented - for D²PAK (TO-263AB) only)

• TRR = tape and reel (right oriented - for D²PAK (TO-263AB) only)

9 - -M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-40CTQ045S-M3	50	1000	Antistatic plastic tubes			
VS-40CTQ045STRR-M3	800	800	13" diameter reel			
VS-40CTQ045STRL-M3	800	800	13" diameter reel			
VS-40CTQ045-1-M3	50	1000	Antistatic plastic tubes			

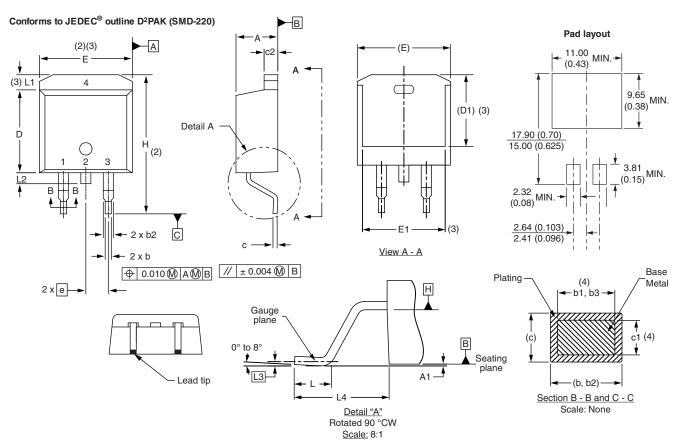
LINKS TO RELATED DOCUMENTS				
Dimensions	D ² PAK (TO-263AB)	www.vishay.com/doc?96164		
Differsions	TO-262AA	www.vishay.com/doc?96165		
Dout moulding information	D ² PAK (TO-263AB)	www.vishay.com/doc?95444		
Part marking information	TO-262AA	www.vishay.com/doc?95443		
Packaging information		www.vishay.com/doc?96424		



Vishay Semiconductors

D²PAK

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INC	INCHES		SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES	STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190		D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010		Е	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039		E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4	е	2.54	BSC	0.100) BSC	
b2	1.14	1.78	0.045	0.070		Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4	L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029		L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4	L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065		L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2	L4	4.78	5.28	0.188	0.208	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch
- (7) Outline conforms to JEDEC® outline TO-263AB

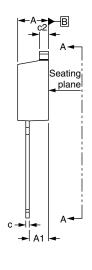


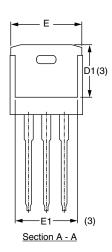
Vishay Semiconductors

TO-262

DIMENSIONS in millimeters and inches

Modified JEDEC outline TO-262 (Datum A) (2) (3) (3) L1 D D D C C C C A (2) A (2) A (3) L2 B B B B C C C A (2)



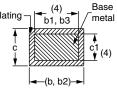


⊕ 0.010**⋒**|A**⋒**|B

Lead assignments



<u>Diodes</u>
1. - Anode (two die)/open (one die)
2., 4. - Cathode
3. - Anode



Section B - B and C - C Scale: None

CVMPOL	MILLIN	METERS	INCH	INCHES		
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	4.06	4.83	0.160	0.190		
A1	2.03	3.02	0.080	0.119		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
С	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	
D1	6.86	8.00	0.270	0.315	3	
E	9.65	10.67	0.380	0.420	2, 3	
E1	7.90	8.80	0.311	0.346	3	
е	2.54	BSC	0.100	BSC		
L	13.46	14.10	0.530	0.555		
L1	-	1.65	-	0.065	3	
L2	3.56	3.71	0.140	0.146		

Notes

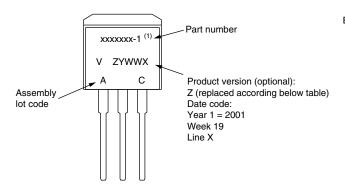
- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- $^{(3)}$ Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline



Part Marking Information

Vishay Semiconductors

TO-262



Example: This is a xxxxxxx-1 ⁽¹⁾ with assembly lot code AC, assembled on WW 19, 2001

in the assembly line "X"

Note

(1) If part number contain "H" as last digit, product is AEC-Q101 qualified

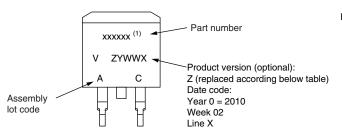
ENVIRONMENTAL NAMING CODE (Z)	PRODUCT DEFINITION
A	Termination lead (Pb)-free
В	Totally lead (Pb)-free
E	RoHS-compliant and termination lead (Pb)-free
F	RoHS-compliant and totally lead (Pb)-free
М	Halogen-free, RoHS-compliant and termination lead (Pb)-free
N	Halogen-free, RoHS-compliant and totally lead (Pb)-free
G	Green



Part Marking Information

Vishay Semiconductors

D²PAK



Example: This is a xxxxxx ⁽¹⁾ with assembly lot code AC, assembled on WW 02, 2010

Note

(1) If part number contain "H" as last digit, product is AEC-Q101 qualified

ENVIRONMENTAL NAMING CODE (Z) PRODUCT DEFINITION			
А	Termination lead (Pb)-free		
В	Totally lead (Pb)-free		
E	RoHS-compliant and termination lead (Pb)-free		
F	RoHS-compliant and totally lead (Pb)-free		
M	Halogen-free, RoHS-compliant, and termination lead (Pb)-free		
N	Halogen-free, RoHS-compliant, and totally lead (Pb)-free		
G	Green		



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