## u·p·t·o·d·a·t·e Newsletter 🐼 T 🗅 🤇

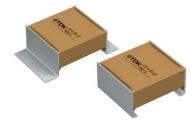


March 24, 2017

### Product news

## CeraLink types with higher rated voltage

The CeraLink™ series with two low-profile (LP) types designed for a rated voltage of 900 V DC has been extended. The SMT capacitors have a capacitance of 0.25 µF and feature different terminations: the B58031I9254M062 type with L-style termination has dimensions of 10.84 mm x 7.85 mm x 4 mm, while the B58031U9254M062 with J-style termination measures only 7.14 mm x 7.85 mm x 4 mm.



The new capacitors supplement the existing LP portfolio which until now has included types for 500 V DC (1  $\mu$ F) and 700 V DC (0.5  $\mu$ F). Thanks to their compact design and high permissible continuous operating temperature of 150 °C, all LP types can, for example, also be embedded directly into IGBT modules as snubber capacitors.

Another advantage of the CeraLink capacitors based on PLZT (lead lanthanum zirconate titanate) ceramic is their extremely low ESR and ESL values. For example, these components feature a minimum ESR value of 12 mΩ and an ESL value of just 2.5 nH. These capacitors are therefore well suited to converter topologies based on fast-switching semiconductors such as GaN or SiC. Voltage overshoots and ringing when switching are significantly lower than with conventional capacitor technologies. Another feature of the LP types is the high steady state ripple current of up to  $11.4 A_{RMS}$ .

If higher capacitances are required, CeraLink types with solder pin terminations are available. They are designed for rated voltages of 500 V or 700 V DC and offer capacitances of 20  $\mu$ F (500 V DC) or 10  $\mu$ F (700 V DC). The ESL value of these types is typically around 3.5 nH.

#### Key data of CeraLink LP series

Туре	Rated voltage [V DC]	ESR [mΩ] *	ESL [nH]	Current (max.) ** [A <sub>RMS</sub> ]
B58031I5105M062 (L-style)	500	12	2.5	11.4
B58031U5105M062 (J-style)				
B58031I7504M062 (L-style)	700	29		7.1
B58031U7504M062 (J-style)				
B58031I9254M062 (L-style)	900	45		5.1
B58031U9254M062 (J-style)				

<sup>\*25 °</sup>C, 1 MHz, zero power. Significant decrease of up to 80% in operation due to self-heating.

<sup>\*\*100</sup> kHz, T<sub>A</sub> = 85 °C

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Further information on the products can be found under <a href="www.epcos.com/ceralink">www.epcos.com/ceralink</a>.

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Customers are asked to address inquiries directly to their sales contacts.