## Vishay Techno



HALOGEN

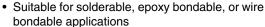
FREE

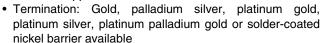
# Thick Film Chip Dividers, High Voltage



### **FEATURES**

- High voltage up to 3000 V
- Typical resistance ratios of 250:1, 500:1, etc.
- Flow solderable
- Tape and reel packaging available
- Available with either wraparound terminations or as a single termination flip chip





- Multiple styles, termination materials and configurations, allow wide design flexibility
- Non-magnetic terminations available
- Compliant to RoHS directive 2002/95/EC
- Halogen-free according to IEC 61249-2-21 definition

STANDARD ELECTRICAL SPECIFICATIONS										
GLOBAL MODEL			TOLERANCE (2) ± %	TEMPERATURE COEFFICIENT <sup>(3)</sup> (- 55 °C to + 150 °C) ± ppm/°C	MAXIMUM WORKING VOLTAGE (4) V  TCR TRACKING ± ppm/°C					
CDHV 2512	Contact factory	12M to 1G	1, 2, 5, 10, 20	100	3000	50 (typical)				

#### Notes

- (1) Resistance values are calibrated at 100 V<sub>DC</sub>. Calibration at other voltages available upon request. Contact factory for lower values.
- (2) Contact factory for tighter tolerances.
- (3) Reference only: Not for all values specified. Consult factory for your value.
- (4) Continuous working voltage shall be  $\sqrt{P \times R}$  or maximum working voltage, whichever is less.

VOLTAGE AND TEMPERATURE COEFFICIENTS OF RESISTANCE CHART TYPICAL								
RESISTANCE ( $\Omega$ )	RATIO (typical)	VCR (ppm/V)	TCR (ppm/°C) - 55 °C to + 150 °C					
20M	250:1	10	100					
150M	300:1	10	150					
800M	500:1	10	200					

#### Note

• Contact factory for other ratios.

GLOBAL PART NUMBER INFORMATION																
New Global Part Numbering: CDHVAF20M0J2500GFB (preferred part number format)																
C D H V A F 2 0 M 0 J 2 5 0 0 G F B								В								
GLOBAL MODEL	TEF STY	RM /LE	TER	M MATERIAL		RESISTANCE VALUE (R1)	то	LERANCE		RAT R1/			ATIO RANCE	SOLE TERMIN		PACKAGING
CDHV = CDHV2512	CDHV = A = 3-sided B = Top only		F = Nickel barrier A = Palladium silver B = Platinum gold			$M = M\Omega$ $G = G\Omega$ $20M0 = 20 M\Omega$		F = ± 1 % G = ± 2 % J = ± 5 %		3 digit significant figure, followed		G = ± 2 % H = ± 3 % J = ± 5 %		<b>E</b> = Sn100 <b>F</b> = Sn95/Ag5 <b>N</b> = No solder		
			<b>D</b> = 1	C = Gold Platinum silver	8	$800M = 800 \text{ M}\Omega$ $1600 = 1 \text{ G}\Omega$	K	X = ± 10 % I = ± 20 %	by a <b>250</b>	mu 10 =	ultiplier 250:1	<u> </u>	10 /0	<b>S</b> = Sn62/Pb	= 36/Ag2	<b>W</b> = Waffle
				= Platinum lladium gold							300:1 500:1			<b>T</b> = Sn90	J/PDTC	<u>'</u>
Historical Part Numbering: CDHV2512AF2005J2500Ge2 (will continue to be accepted)																
CDHV25	12	A		F		2005	]	J		]	250	0		G		e2
HISTORIC MODEL		TERN STYL		TERM MATERIAL		RESISTANCE VALUE (R1)		TOLERAN	NCE		RATI R1/F	_		TIO RANCE	ТІ	SOLDER ERMINATION

<sup>\*</sup> Pb containing terminations are not RoHS compliant, exemptions may apply





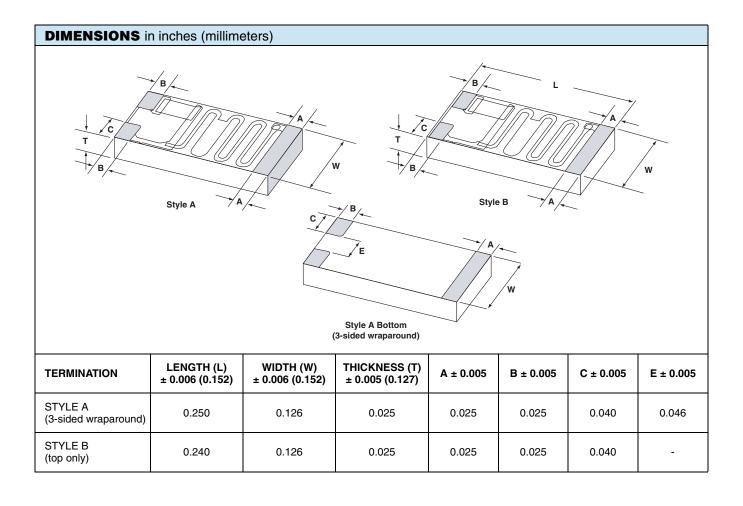
# Thick Film Chip Dividers, High Voltage

MECHANICAL SPECIFICATIONS							
Resistive element	Ruthenium oxide						
Encapsulation	Glass						
Substrate	96 % alumina						
Termination	Solder-coated nickel barrier standard. Gold, palladium silver, platinum gold, platinum silver, platinum palladium gold terminations available.						
Solder finish	Pure tin or tin/lead solder alloys standard. Hot solder dipped tin/silver or tin/lead/silver solder alloys available.						

### **ENVIRONMENTAL SPECIFICATIONS**

Operating temperature: - 55 °C to + 150 °C

**Life:** Less than 0.5 % change when tested at full rated power (Reference only: Not for all values specified. Consult factory for your size and value.)



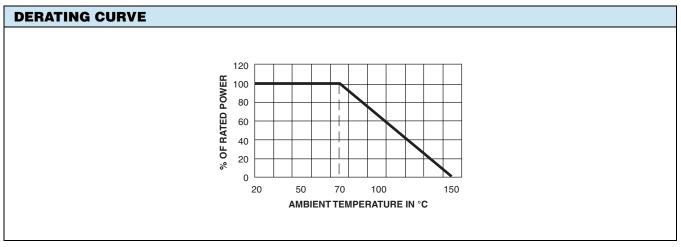
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## Thick Film Chip Dividers, High Voltage



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(Reference only: Not for all values specified. Consult factory for your specific value.)

TYPE	TERMINATION MATERIAL	TERMINATION STYLE	TERMINATION STYLE/ MATERIAL CODE	SOLDER TERMINATION CODE		
Solderable	Nickel barrier	3-sided (wraparound)	AF	E, F, S or T <sup>(3)</sup>		
Solderable	Nickel Damei	Top only (flip chip)	BF			
Wire bondable/ solderable	Platinum palladium gold	Top only (flip chip)	BE	N, F or S <sup>(1)</sup>		
Wire bondable/ epoxy bondable	Gold	Top only (flip chip)	BC	N		
	Palladium silver (2)		ВА			
Epoxy bondable	Platinum gold	Top only (flip chip)	BB	N		
	Platinum silver		BD			

## Notes

For technical questions, contact: telresistors@vishay.com

<sup>(1)</sup> Use solder termination N for applications requiring wire bondable mounting, and solder terminations F or S for applications requiring solderable mounting.

<sup>(2)</sup> While not recommended, palladium silver terminations could be used for solderable applications when using a solder alloy containing silver.

<sup>(3)</sup> Standard solder plating for the nickel barrier parts are solder terminations E or T. Hot solder dipped terminations F or S are also available.



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