

### Is Now Part of



## ON Semiconductor®

To learn more about ON Semiconductor, please visit our website at <a href="https://www.onsemi.com">www.onsemi.com</a>

ON Semiconductor and the ON Semiconductor logo are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any EDA Class 3 medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, emplo



March 2015

# 74VHC14 Hex Schmitt Inverter

#### **Features**

- High Speed:  $t_{PD}$  = 5.5 ns (Typ.) at  $V_{CC}$  = 5 V
- Low Power Dissipation:  $I_{CC} = 2 \mu A$  (Max.) at  $T_A = 25$ °C
- High Noise Immunity:  $V_{NIH} = V_{NIL} = 28\% V_{CC}$  (Min.)
- · Power down protection is provided on all inputs
- Low Noise: V<sub>OLP</sub> = 0.8 V (Max.)
- Pin and Function Compatible with 74HC14

## **General Description**

The VHC14 is an advanced high speed CMOS Hex Schmitt Inverter fabricated with silicon gate CMOS technology. It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation. Pin configuration and function are the same as the VHC04 but the inputs have hysteresis between the positive-going and negative-going input thresholds, which are capable of transforming slowly changing input signals into sharply defined, jitterfree output signals, thus providing greater noise margin than conventional inverters.

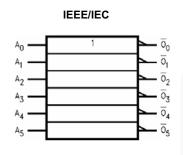
An input protection circuit ensures that 0 V to 7 V can be applied to the input pins without regard to the supply voltage. This device can be used to interface 5 V to 3 V systems and two supply systems such as battery back up. This circuit prevents device destruction due to mismatched supply and input voltages.

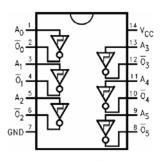
## **Ordering Information**

Part Number	Top Mark	Package	Packing Method		
74VHC14M	74VHC14	SOIC 14L	Rail		
74VHC14MX	74VHC14	SOIC 14L Tape and Rec			
74VHC14SJX	VHC14	SOP 14L Tape and F			
74VHC14MTC	V14	V14 TSSOP 14L Rail			
74VHC14MTCX	V14	TSSOP 14L	Tape and Reel		

## Logic Symbol/s

## **Connection Diagram/s**





## **Pin Descriptions**

Pin Names	Description
A <sub>n</sub>	Inputs
$\overline{O}_n$	Outputs

## **Truth Table/s**

Α	0
L	Н
н	L

## **Absolute Maximum Ratings**(1)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Value	Unit
V <sub>CC</sub>	Supply Voltage	-0.5 to +7.0	V
V <sub>IN</sub>	DC Input Voltage	-0.5 to +7.0	V
V <sub>OUT</sub>	DC Output Voltage	-0.5 to V <sub>CC</sub> +0.5	V
I <sub>IK</sub>	Input Diode Current	-20	mA
I <sub>OK</sub>	Output Diode Current	±20	mA
I <sub>OUT</sub>	DC Output Current	±25	mA
I <sub>CC</sub>	DC V <sub>CC</sub> / GND Current	±50	mA
T <sub>STG</sub>	Storage Temperature Range	-65 to +150	°C
TL	Lead Temperature (Soldering 10 seconds)	260	°C

#### Note:

1. Absolute maximum ratings are values beyond which the device may be damaged or have its useful life impaired. The data book specifications should be met, without exception, to ensure that the system design is reliable over its power supply, temperature, and output/input loading variables. Fairchild does not recommend operation outside databook specifications.

## **Recommended Operating Conditions**(2)

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to absolute maximum ratings.

Symbol	Parameter	Min.	Max.	Unit
V <sub>CC</sub>	Supply Voltage	2.0	5.5	V
V <sub>IN</sub>	Input Voltage	0	5.5	V
V <sub>OUT</sub>	Output Voltage	0	V <sub>CC</sub>	V
T <sub>OPR</sub>	Operating Temperature Range	-40	85	°C

#### Note:

2. Unused inputs must be held HIGH or LOW. They may not float.

## **DC Electrical Characteristics**

Comple of	Domenton	V		T <sub>A</sub> = 25°0	С	T <sub>A</sub> = -40	to 85°C	Unit	Conditions	
Symbol	Parameter	v <sub>cc</sub>	Min.	Тур.	Max.	Min.	Max.			
	- /	3.0			2.20		2.20			
$V_{P}$	Positive Threshold	4.5			3.15		3.15	V		
	Voltage	5.5			3.85		3.85			
		3.0	0.90			0.90				
$V_N$	Negative	4.5	1.35			1.35		V		
	Threshold Voltage	5.5	1.65			1.65				
		3.0	0.30		1.20	0.30	1.20			
$V_{H}$	Hysteresis Voltage	4.5	0.40		1.40	0.40	1.40	V		
		5.5	0.50		1.60	0.50	1.60			
		2.0	1.9	2.0		1.9		V		I <sub>OH</sub> = -50 μA
		3.0	2.9	3.0		2.9			., .,	
V <sub>OH</sub>	HIGH Level Output	4.5	4.4	4.5		4.4			$V_{IN} = V_{IL}$	
	Voltage	3.0	2.58			2.48		7		$I_{OH} = -4 \text{ mA}$
		4.5	3.94			3.80	2			$I_{OH} = -8 \text{ mA}$
		2.0		0.0	0.1		0.1			
		3.0		0.0	0.1		0.1		., .,	$I_{OL} = 50 \mu A$
$V_{OL}$	LOW Level Output	4.5		0.0	0.1		0.1	V	$V_{IN} = V_{IH}$	
	Voltage	3.0			0.36		0.44			$I_{OL} = 4 \text{ mA}$
		4.5			0.36		0.44			$I_{OL} = 8 \text{ mA}$
I <sub>IN</sub>	Input Leakage Current	0 - 5.5			±0.1		±1.0	μΑ	V <sub>IN</sub> = 5.5	V or GND
I <sub>CC</sub>	Quiescent Supply Current	5.5			2.0		20.0	μΑ	V <sub>IN</sub> = V <sub>CC</sub>	or GND

## Noise Characteristics(2)

Symbol	Parameter	V <sub>CC</sub>	T <sub>A</sub> = 25°C		Unit	Conditions	
	i arameter		Тур.	Max.			
V <sub>OLP</sub>	Quiet Output Maximum Dynamic V <sub>OL</sub>	5.0	0.4	0.8	V	C <sub>L</sub> = 50 pF	
V <sub>OLV</sub>	Quiet Output Minimum Dynamic V <sub>OL</sub>	5.0	-0.4	0.8	V	C <sub>L</sub> = 50 pF	
V <sub>IHD</sub>	Minimum HIGH Level Dynamic Input Voltage	5.0		3.5	V	C <sub>L</sub> = 50 pF	
V <sub>ILD</sub>	Maximum LOW Level Dynamic Input Voltage	5.0		1.5	V	C <sub>L</sub> = 50 pF	

#### Note:

2. Parameter guaranteed by design.

## **AC Electrical Characteristics**

Symbol	Parameter	V <sub>CC</sub>	T <sub>A</sub> = 25°C			$T_A = -40 \text{ to } 85^{\circ}\text{C}$		Unit	Conditions
Syllibol		▼ CC	Min.	Тур.	Max.	Min.	Max.	Offic	Conditions
		$3.3 \pm 0.3$		8.3	12.8	1.0	15.0		C <sub>L</sub> = 15 pF
t <sub>PLH</sub> P	Propagation Delay Time	3.3 ± 0.3		10.8	16.3	1.0	18.5	no	C <sub>L</sub> = 50 pF
		5.0 ± 0.5		5.5	8.6	1.0	10.0	ns	C <sub>L</sub> = 15 pF
				7.0	10.6	1.0	12.0		C <sub>L</sub> = 50 pF
C <sub>IN</sub>	Input Capacitance			4	10		10	pF	V <sub>CC</sub> = Open
C <sub>PD</sub>	Power Dissipation Capacitance			21				pF	(3)

#### Note:

3. C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the opening current consumption without load.

Average operating current can be obtained by the equation:  $I_{CC}$  (Opr) =  $C_{PD}$  \*  $V_{CC}$  \*  $f_{IN}$  +  $I_{CC}$  /6 (per Gate)

## **Physical Dimensions**

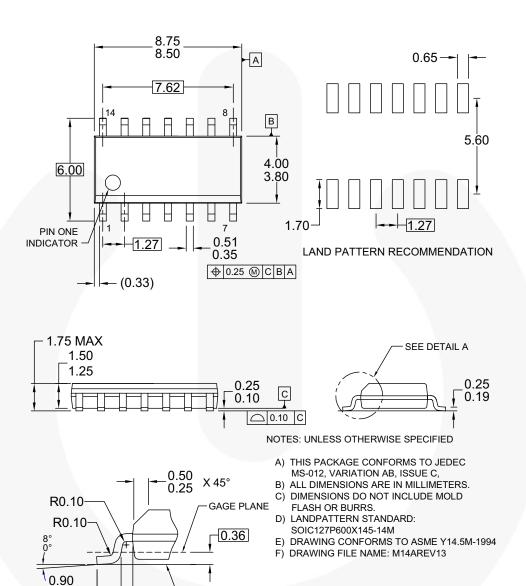


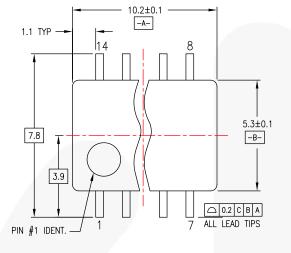
Figure 1. 14-LEAD, SOIC, JEDEC MS-012, 0.150 INCH, NARROW BODY

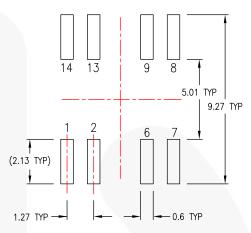
SEATING PLANE

DETAIL A

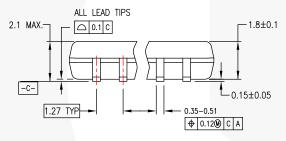
0.50 (1.04)

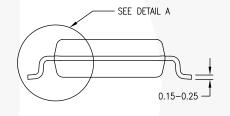
## Physical Dimensions (Continued)





#### LAND PATTERN RECOMMENDATION

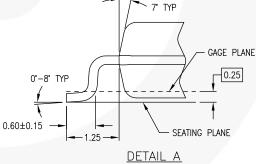




#### DIMENSIONS ARE IN MILLIMETERS

#### NOTES:

- NOTES:
  A. CONFORMS TO EIAJ EDR-7320 REGISTRATION, ESTABLISHED IN DECEMBER, 1998.
  B. DIMENSIONS ARE IN MILLIMETERS.
  C. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.



M14DREVC

Figure 2. 14-LEAD, SOP, EIAJ TYPE II, 5.0 MM WIDE

## Physical Dimensions (Continued) 0.65 5.00±0.10 В 6.4 4.40±0.10 6.10 3.2 1.65 0.2 C B A ALL LEAD TIPS PIN#1 IDENT\_/ TOP VIEW SEE DETAIL A -1.2 MAX 0.90<sup>+0.15</sup> 0.10±0.05 0.30 0.19 ALL LEAD TIPS ○ 0.1 C ⊕ 0.13M A BS CS 0.65 12.00° TOP & BOTTOM FRONT VIEW 0.09 MIN NOTES: GAGE PLANE A. CONFORMS TO JEDEC REGISTRATION MO-153, VARIATION AB, REF NOTE 6 B. DIMENSIONS ARE IN MILLIMETERS. 0.25 0.09 M C. DIMENSIONS ARE EXCLUSIVE OF BURRS, -0.6±0.1**-**MOLD FLASH, AND TIE BAR EXTRUSIONS SEATING PLANE D. DIMENSIONING AND TOLERANCES PER ANSI DETAIL A Y14.5M, 2009. E. LANDPATTERN STANDARD: SOP65P640X110-14M. F. DRAWING FILE NAME: MKT-MTC14rev7.

Figure 3. 14-LEAD, TSSOP, JEDEC MO-153, 4.4 MM WIDE





#### TRADEMARKS

The following includes registered and unregistered trademarks and service marks, owned by Fairchild Semiconductor and/or its global subsidiaries, and is not intended to be an exhaustive list of all such trademarks.

BitSiC™ Green FPS™
Build it Now™ Green FPS™ e-Series™

Current Transfer Logic™ Making Small Speakers Sound Louder DEUXPEED® and Better™

Dual Cool™

EcoSPARK®

EfficientMax™

ESBC™

MicroPak™

MicroPak™

MicroPak™

MicroPak™

Fairchild®
Fairchild®
Fairchild Semiconductor®
FACT Quiet Series™
FACT®
FAST®
FastvCore™
FETBench™
FPS™

MillerDrive™
MotionGrid®
MotionGrid®
MTr®
MVN®
MVN®
MVN®
mWSaver®
OptoHiT™
OPTOLOGIC®

OPTOPLANAR®

® PowerTrench® PowerXS™

Programmable Active Droop™

QFET<sup>®</sup>
QS™
Quiet Series™
RapidConfigure™

Saving our world, 1mW/W/kW at a time™

SignalWise™ SmartMax™ SMART START™

Solutions for Your Success™

SPM®
STEALTH™
SuperFET®
SuperSOT™-3
SuperSOT™-6
SuperSOT™-6
SuperSOT™-8
SuperSOT™-8
SuperSOT™-8
SuperSOT™-8
SuperSOT™-8
SuperSOT™-8
SuperSOT™-8
SuperSOT™-8

SYSTEM GENERAL®

TinyBoost®
TinyBuck®
TinyCalc™
TinyLogic®
TINYOPTO™
TinyPower™
TinyPWM™
TinyWire™
TranSiC™
TriFault Detect

TriFault Detect™
TRUECURRENT®\*
µSerDes™

SerDes\* UHC®\* Ultra FRFET™ UniFET™ VCX™ VisualMax™ VoltagePlus™ XS™ Msens™ Misual™ UniFeT™ Misual™ M

#### DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. TO OBTAIN THE LATEST, MOST UP-TO-DATE DATASHEET AND PRODUCT INFORMATION, VISIT OUR WEBSITE AT <a href="http://www.fairchildsemi.com">http://www.fairchildsemi.com</a>, FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OF CIRCUIT DESCRIBED HEREIN, NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

#### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

#### As used herein:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
- A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

#### ANTI-COUNTERFEITING POLICY

Fairchild Semiconductor Corporation's Anti-Counterfeiting Policy. Fairchild's Anti-Counterfeiting Policy is also stated on our external website, www.fairchildsemi.com, under Sales Support

Counterfeiting of semiconductor parts is a growing problem in the industry. All manufacturers of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed applications, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handling and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address any warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors.

#### PRODUCT STATUS DEFINITIONS

#### **Definition of Terms**

Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

Rev. 173

<sup>\*</sup> Trademarks of System General Corporation, used under license by Fairchild Semiconductor.