

Electra House, 32 Southtown Road Great Yarmouth, Norfolk NR31 0DU, England Telephone +44 (0)1493 602602 Fax +44 (0)1493 665111 Email:sales@midasdisplays.com www.midasdisplays.com

MCOT128128C1V-WM	128 x 128	White	OLED Module		
Specification					
Version: 1 Date: 16/05/2017			017		
	Revision				
0 29/06	/2016 Firs	st release			

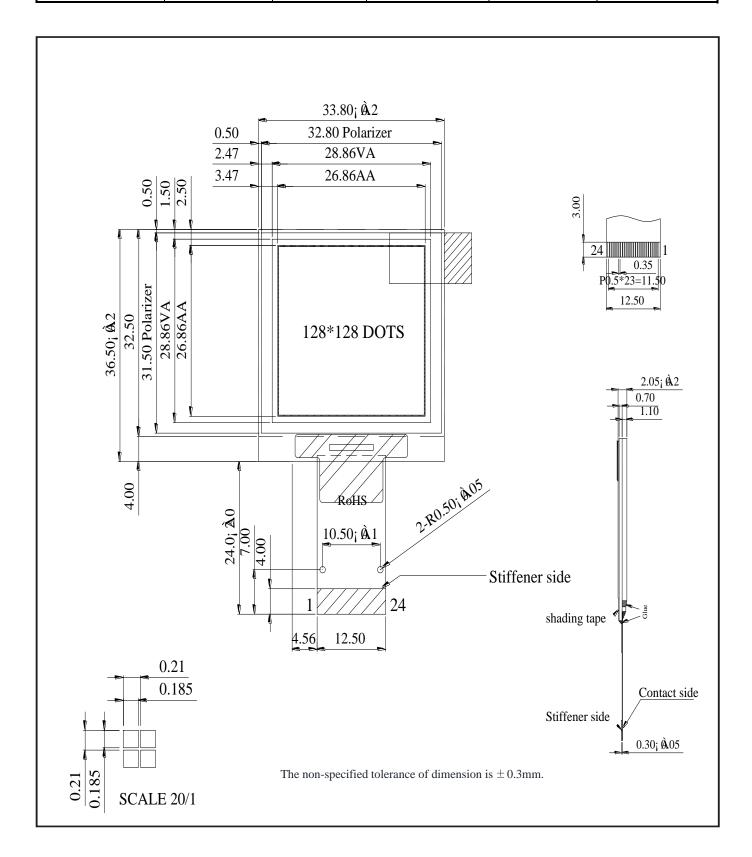
Display F				
Resolution	128 x 128			
Appearance	White on Black), HC	
Logic Voltage	3V		ЮПЭ	
Interface	Parallel / SPI / I2C	RoHS		
Module Size	33.80 x 36.50 x 2.05 mm		-	
Operating Temperature	-40°C ~ +80°C	Box Quantity	Weight / Display	
Construction	TAB			

* - For full design functionality, please use this specification in conjunction with the SSD1327ZB specification. (Provided Separately)

Display Accessories				
Part Number	Description			
MPBV7	30 Way FFC to cable and wires. Driven by any driver board that can be wired to a 1mm pitch SHDR-30V-S-B receptacle.			
MCIB-12	UC32 Breakout Board with SD card and LED back light driver. Used in conjunction with MPBV6.			

Optional Variants			
Appearance	Voltage		
Yellow on Black			

Mechanical Specifications						
Module Size	dule Size 36.98 x 41.23 x 2.05 (With Backlight) W x H x D mm					
Viewing Area	32.00 x 32.00	32.00 x 32.00 W x H mm Hole-to-Hole W				
Dot Size	0.185 x 0.185	0.185 x 0.185 W x H mm Dot Pitch 0.21 x 0.21				



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Pin layout					
Pin	Symbol	Description	Remarks		
1	VSS	Ground. Connect to external ground.			
2	VCC	Power Supply for driving voltage. Positive power voltage supply pin.			
3	VCOMH	COM signal deselected voltage level. Capacitor between here and VSS.			
4	VCI	Low Voltage power supply. Should match with MCU interface voltage level and must connect to external source. Must always be ≥ VDD.			
5	VDD	Power Supply pin for core logic operation.			
6	BS1	MCU bus interface selection pins. Select appropriate logic setting, as described below: (Note: "0" is connected to VSS and "1" is			
7	BS2	connected to VCI) I2C = BS1: 1 BS2: 0 4-wire SPI = BS1: 0 BS2: 0 8-bit 68XX = BS1: 0 BS2: 1 8-bit 80XX = BS1: 1 BS2: 1			
8	VSS	Ground Pin, must connect to external ground.			
9	IREF	Segment output current reference pin.			
10	CS#	Chip Select Input connecting to MCU. Chip is enabled for MCU communication when CS# is pulled Low.			
11	RES#	Reset Signal Input. Initialisation is executed when pulled Low. Keep pulled High during normal operation.			
12	D/C	Data / Command control pin connect to MCU. High= Data at D(7:0) interpreted as data. Low= Data at D(7:0) transferred to command register. I2C mode = SA0 for slave address selection. 3-Wire SPI = Connect to VSS			
13	W/R#	Read / Write input pin, connecting to MCU interface. 6800 Mode= R/W (R/W#) selection input, read mode carried out when pulled High, write mode when Low. 8080 Mode= WR (W/R#) input, data write initiated when pin is pulled Low and chip is selected.			
14	RD#	MCU Interface Input. 6800 Mode= Enable (E) signal pin, Read/Write initiated when pin is pulled High and chip is selected. 8080 Mode= Read (RD#) signal pin, read operation initiated when pin is pulled Low and chip is selected. I2C or SPI selected = Connect to VSS.			
15	D0				
16	D1				
17	D2	Bi-directional data bus connecting to MCU data bus. Unused pins			
18	D3	to tie low. SPI Mode= D0 will be Serial Clock input (SCLK). D1 will be the			
19	D4	Serial Data input (SDIN) and D2 should be kept NC.			
20	D5	I2C Mode= D2 and D1 should be tied together and serve as SDAout, SDAin in application and D0 is Serial Clock input (SCL).			
21	D6				
22	D7				
23	VCC	Power Supply for panel driving voltage. Supplied externally.			
24	VSS	Ground.			
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Absolute Maximums Ratings							
Item Symbol Minimum Typical Maximum Unit							
Supply Voltage for Display	VCC	-0.50		19.00	V		
Supply Voltage for Logic	VDD	-0.50		2.75	V		
Supply Voltage for Operation	VCI	-0.30		4.00	V		
Operating Temperature	TOP	-40		80	°C		
Storage Temperature	TSTG	-40		80	°C		

Electronic Characteristics						
Item	Symbol	Condition	Minimum	Typical	Maximum	Unit
Input High Voltage	VIH		0.80		VDD	V
Input Low Voltage	VIL		GND		0.20	V
Output High Voltage	VOH		0.90		VDD	V
Output Low Voltage	VOL		GND		0.10	V
Supply Voltage for Logic	VDD		2.80	3.00	3.30	V
Supply Voltage for Display	VCC		14.00	14.50	15.00	V
50% Checkboard Operating Current.	IDD	VDD=14.5V	23.00	24.00	26.00	mA

OLED Characteristics								
Item Symbol Condition Minimum Typical Maximum Unit								
Viouing Anglo	(V)θ		160			Deg		
Viewing Angle	(Η)φ		160			Deg		
Contrast Ratio	CR	Dark	2000:1					
Doonongo Timo	T Rise			10		μs		
Response Time	T Fall			10		μs		
Display with 50% Checkboard Brightness			60	80		cd/m ²		
CIEx(Blue)		(CIE1931)	0.26	0.28	0.30			
CIEy(Blu	ie)	(CIE1931)	0.30	0.32	0.34			

OLED Life Time					
Item Conditions Typical Remark					
Operating Life Time	Ta=25°C. Initial checkboard brightness, 50%.	20,000 Hours			

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