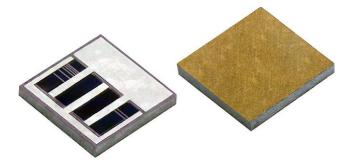
IGBR

SHAY. www.vishay.com

Vishay Electro-Films

Thin Film, High Power Back-Contact Resistor



The high power back-contact resistor (IGBR) series thin film chip resistor utilizes the excellent thermal properties of silicon to allow ultra high power rating with miniature case size for hybrid (chip and wire) assemblies.

FEATURES

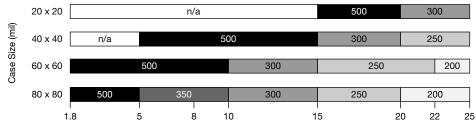
- Wire bondable
- Small size
- High power rating
- Single wire bond assembly
- Moisture resistant
- Case size 0202 to 0808
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- · Gate resistor for IGBT based power converters
- Current limiting for LED lighting applications
- High power applications
- Alternative energy
- Hybrid assemblies

TEMPERATURE COEFFICIENT OF RESISTANCE, VALUES, AND TOLERANCES				
PARAMETER	VALUE	UNIT		
Total Resistance Range	1.8 to 25	Ω		
Standard Tolerances	5, 10, 25	%		
TCR	± 500	ppm/°C		

TCR (ppm/°C) BY CASE SIZE AND VALUE





STANDARD ELECTRICAL SPECIFICATIONS

PARAMETER	VALUE	UNIT	
Operating Film Temperature Range	200 max.	°C	
Operating Temperature Range	-55 to +125	°C	
Working Voltage	75 max.	V	
Breakdown Voltage	400 max.	V	
Thermal Resistivity ⁽¹⁾	Down to 2	K/W	
DC Power Rating ⁽¹⁾⁽²⁾	Up to 4	W	
Load Life Stability, 1000 h, Film Temperature 200 °C	$\pm 1 \Delta R/R$	%	
Short Time Overload, 5 x Rated Power, 25 °C, 5 s	± 0.25 ∆R/R	%	
Thermal Shock, MIL-STD-202, Method 107 F	± 1 ∆R/R	%	
Moisture Resistance, MIL-STD-202, Method 106 (3)	± 0.25 ∆R/R	%	
High Temperature Exposure, 100 h, +150 °C	± 0.5 ∆R/R	%	
Low Temperature Operation, -65 °C, 45 min	± 0.5 ∆R/R	%	

Notes

⁽¹⁾ See table "Power Rating by Case Size"

(2) Power rating determined by application specific heat sink properties. Film temperature should not exceed 200 °C. See table "Power Rating by Case Size" for more details

⁽³⁾ Aluminum pads and aluminum wire bonds are sensitive to high moisture environments. Adequate application level packaging is required to protect the components and wire bonds from moisture related damage

Pb-free

RoHS COMPLIANT HALOGEN FREE GREEN (5-2008) www.vishay.com

Vishay Electro-Films

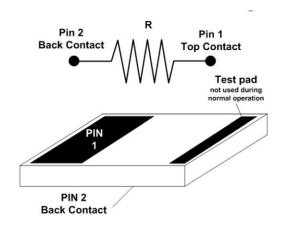
POWER RATING BY CASE SIZE							
CASE SIZE	CHIP SIZE mil (mm) ⁽²⁾	BOND PAD SIZE mil (mm)	DIE THICKNESS mil (mm) ⁽²⁾	TYPICAL <i>R</i> THERMAL ⁽¹⁾ K/W	R MIN. Ω	R MAX. Ω	
0202	20 x 20 (0.5 x 0.5)	10 x 16 (0.25 x 0.41)	10 (0.25)	10	15	25	
0404	40 x 40 (1 x 1)	15 x 36 (0.38 x 0.91)	10 (0.25)	7	5	25	
0606	60 x 60 (1.5 x 1.5)	20 x 56 (0.51 x 1.42)	10 (0.25)	5	1.8	25	
0808	80 x 80 (2 x 2)	27 x 76 (0.69 x 1.93)	10 (0.25)	2	1.8	25	

Notes

⁽¹⁾ Typical *R* thermal between film and back contact. Does not include die attach joint (epoxy or solder)

⁽²⁾ Dimension tolerances are ± 0.05 mm (± 2 mil)

SCHEMATIC



MATERIAL SPECIFICATIONS	
PARAMETER	
Chip Substrate Material	Oxidized silicon, 10 kÅ minimum SiO ₂
Film Material	Tantalum Nitride
Case Size	See table "Power Rating by Case Size"
Passivation	None
Number of Pads	1
Top Terminations Suitable for Heavy Gage Aluminum Wire-Bonding	Al (2.5 μm min.)
	P = TiW (500 Å to 1000 Å) Pd (2000 Å to 3000 Å) Au (3000 Å to 5000 Å)
Back Termination (for epoxy, lead (Pb)-free solder or silver compression assembly)	N = TiW (500 Å to 1000 Å) Ni (6000 Å to 7000 Å) Au (3000 Å to 5000 Å)
	T = TiW (500 Å to 1000 Å) Au (1000 Å to 3000 Å) Ni (40 μ" minimum) Au (40 μ" minimum)

2



Vishay Electro-Films

GLOBA	GLOBAL PART NUMBER INFORMATION							
Global Pa	Global Part Number: IGBRB3000CJOPCST							
Global Pa	Global Part Number Description: IGBR 1 mm 3 Ω 5 % 300 ppm/°C PD Commercial Tape							
Ι	I G B R B 3 0 0 0 C J 0 P C S T							
MODEL	SIZE	RESISTANCE (Ω)	RESISTANCE MULTILPLIER CODE	TOL. CODE (%)	TCR (ppm/°C)	BACKSIDE TERMINATION	VISUAL CLASS	PACKAGING CODE
IGBR High	$A = 20 \times 20$ $B = 40 \times 40$ $C = 60 \times 60$	First 4 digits are significant figures of	C = 0.001 B = 0.01 A = 0.1	J = 5 K = 10 M = 20	$J = \pm 500$ $W = \pm 350$ $O = \pm 300$	$\mathbf{P} = \text{TiW/Pd/Au}$ $\mathbf{N} = \text{TiW/Ni/Au}$ $\mathbf{T} = \text{TiW/Au/}$	C = commercial H = class H K = class K	WS = waffle pack 100 min., 1 mult
power back- contact resistor	D = 80 x 80	resistance		L = 25	M = ± 250	Ni/Au ⁽¹⁾		FW = full wafer (3") ST = diced on tape

Note

⁽¹⁾ See Material Specifications table for metal thickness



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