

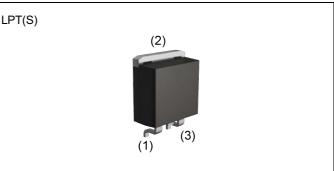
1) Fast reverse recovery time (trr)

4) Drive circuits can be simple

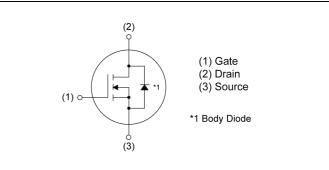
5) Pb-free plating ; RoHS compliant

V _{DSS}	600V
R _{DS(on)} (Max.)	1.43Ω
I _D	±4A
P _D	60W

●Outline



Inner circuit



Application

Features

2) Low on-resistance3) Fast switching speed

Switching

Packaging specifications

Packing	Embossed Tape
Packing code	TL
Marking	R6004JNJ
Quantity (pcs)	1000

• Absolute maximum ratings (T_a = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V _{DSS}	600	V
Continuous drain current $(T_c = 25^{\circ}C)$	I _D *1	±4	А
Pulsed drain current	I _{DP} *2	±12	А
Gate - Source voltage	V _{GSS}	±30	V
Avalanche current, single pulse	I _{AS} *3	1.0	А
Avalanche energy, single pulse	E _{AS} *3	52	mJ
Power dissipation $(T_c = 25^{\circ}C)$	P _D	60	W
Junction temperature	Tj	150	°C
Operating junction and storage temperature range	T _{stg}	-55 to +150	°C

•Thermal resistance

Deremeter	Cumph of	Values			Linit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC}	-	-	2.11	°C/W
Thermal resistance, junction - ambient	R _{thJA}	-	-	80	°C/W
Soldering temperature, wavesoldering for 10s	T _{sold}	-	-	265	°C

•Electrical characteristics (T_a = 25°C)

Deremeter	C) maked	Conditions	Values			Linit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 1mA	600	-	-	V	
Zero gate voltage drain current	I _{DSS}	$V_{DS} = 600V, V_{GS} = 0V$ $T_j = 25^{\circ}C$	-	-	100	μA	
Gate - Source leakage current	I _{GSS}	V_{GS} = ±30V, V_{DS} = 0V	-	-	±100	nA	
Gate threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 450 \mu A$	5.0	6.0	7.0	V	
Static drain - source on - state resistance	R _{DS(on)} *5	$V_{GS} = 15V, I_D = 2.0A$ $T_j = 25^{\circ}C$	-	1.10	1.43	Ω	
Gate resistance	R _G	f = 1MHz, open drain	-	3.6	-	Ω	



• Electrical characteristics (T_a = 25°C)

Deremeter	Cump of	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input capacitance	C _{iss}	V _{GS} = 0V	-	260	-	
Output capacitance	C _{oss}	V _{DS} = 100V	-	18	-	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	0.9	-	_
Effective output capacitance energy related	C _{o(er)} *6	V _{GS} = 0V	-	14	-	pF
Effective output capacitance time related	C _{o(tr)} *7	$V_{DS} = 0V$ to 480V	-	50	-	
Turn - on delay time	t _{d(on)} *5	$V_{DD} \simeq 300$ V, V_{GS} = 15V	-	13	-	
Rise time	t _r *5	I _D = 2.0A	-	11	-	20
Turn - off delay time	$t_{d(off)}^{*5}$	$R_L \simeq 150\Omega$	-	24	-	ns
Fall time	t _f *5	R _G = 10Ω	-	33	-	

• Gate charge characteristics ($T_a = 25^{\circ}C$)

Deremeter	Symbol	Conditions	Values			Unit
Parameter	Symbol Conditions		Min.	Тур.	Max.	Onit
Total gate charge	Q_g^{*5}	$V_{DD} \simeq 300 V$	-	10.5	-	
Gate - Source charge	Q_{gs}^{*5}	I _D = 4A	-	3.0	-	nC
Gate - Drain charge	Q_{gd}^{*5}	V _{GS} = 15V	-	3.5	-	
Gate plateau voltage	V _(plateau)	$V_{DD} \simeq 300$ V, I _D = 4A	-	9.5	-	V

*1 Limited only by maximum temperature allowed.

*2 Pw \leq 10µs, Duty cycle \leq 1%

*3 L \simeq 100mH, V_{DD} = 50V, R_G = 25 Ω , starting T_i = 25°C

*4 Tc=25°C

*5 Pulsed

- *6 Co(er) is a fixed capacitance that gives the same stored energy as Coss while V_{DS} is rising from 0 to 80% V_{DSS} .
- *7 Co(tr) is a fixed capacitance that gives the same charging time as Coss while V_{DS} is rising from 0 to 80% V_{DSS} .



•Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Source current	I _S *1		-	-	4	А	
Pulsed source current	I_{SP}^{*2}	T _C = 25°C	-	-	12	А	
Source-Drain voltage	V_{SD}^{*5}	V _{GS} = 0V, I _S = 4A	-	-	1.7	V	
Reverse recovery time	t rr ^{*5}		-	45	-	ns	
Reverse recovery charge	Q _{rr} *5	I _S = 4Α di/dt = 100Α/μs	-	100	-	nC	
Peak reverse recovery current	_{rr} *5		-	4.0	-	А	





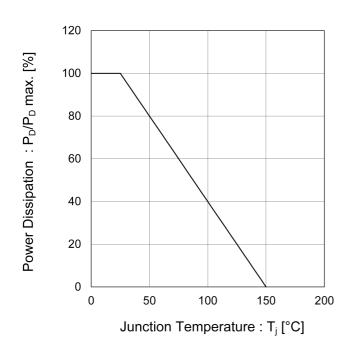


Fig.1 Power Dissipation Derating Curve

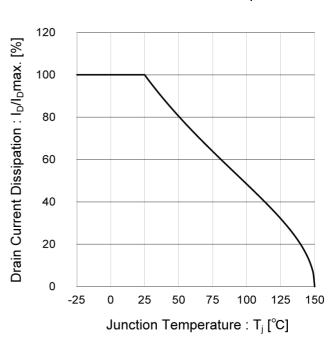


Fig.2 Drain Current Derating Curve vs. Junction Temperature

Fig.3 Normalized Transient Thermal
Resistance vs. Pulse Width

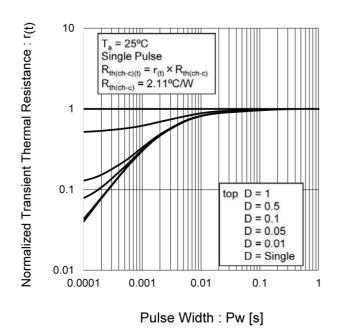
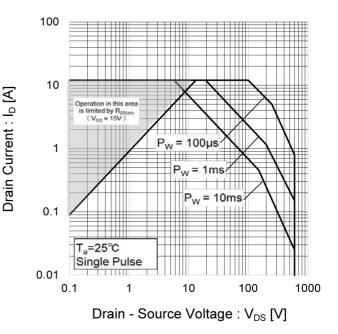


Fig.4 Maximum Safe Operating Area





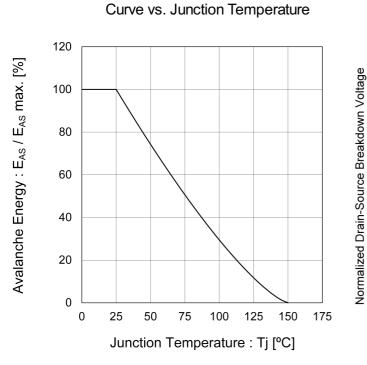


Fig.5 Avalanche Energy Derating



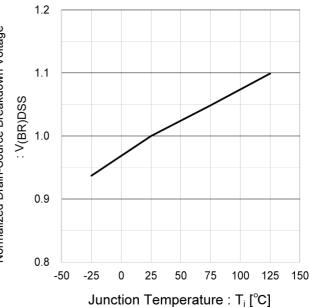
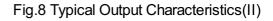
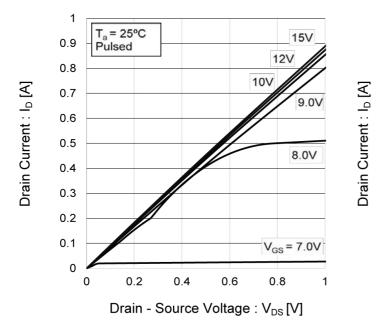
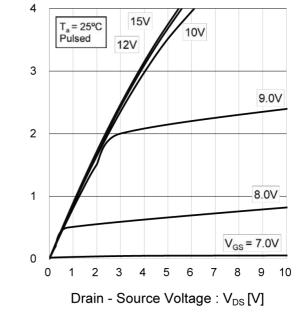


Fig.7 Typical Output Characteristics(I)









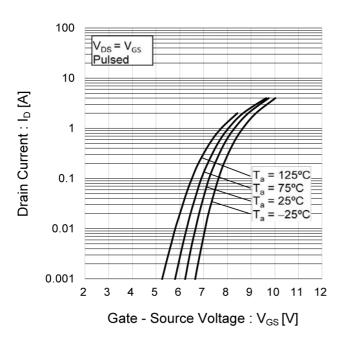


Fig.9 Typical Transfer Characteristics

Fig.10 Normalized Gate Threshold . Voltage vs Junction Temperature

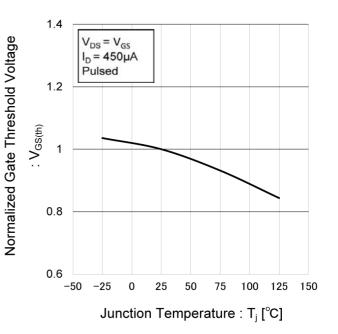
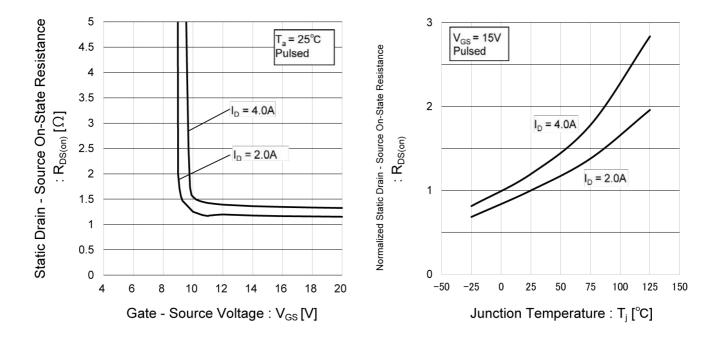


Fig.11 Static Drain - Source On - State Resistance vs. Gate Source Voltage

Fig.12 Normalized Static Drain - Source On - State Resistance vs. Junction Temperature





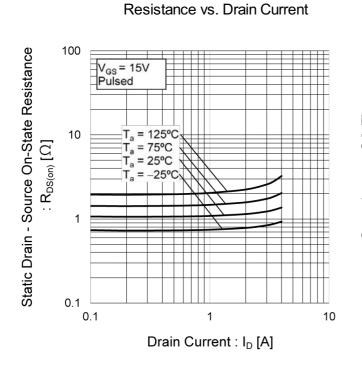


Fig.13 Static Drain - Source On - State

Fig.14 Typical Capacitance vs. Drain - Source Voltage

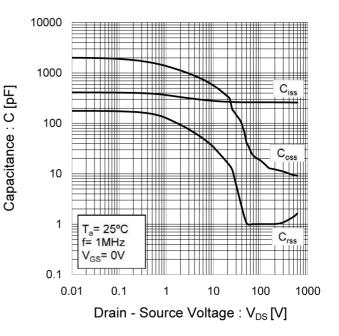


Fig.15 Typical Coss Stored Energy

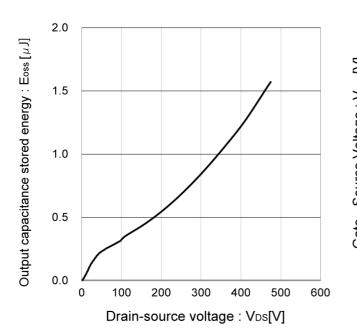
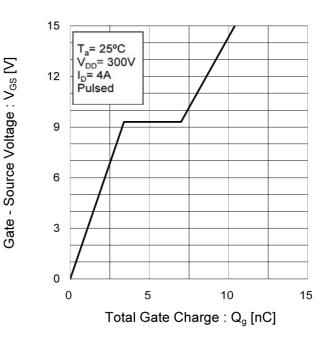


Fig.16 Typical Gate Charge



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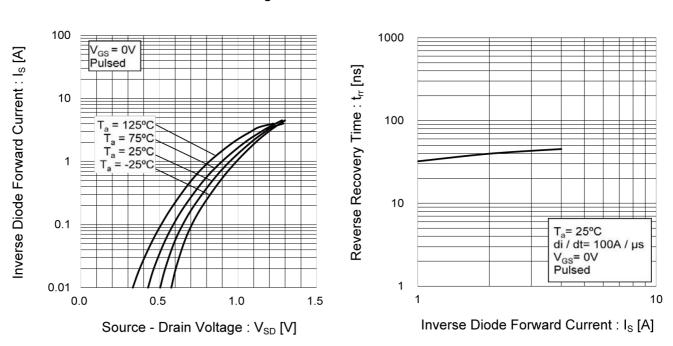
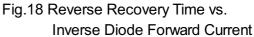


Fig.17 Inverse Diode Forward Current vs. Source - Drain Voltage







Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

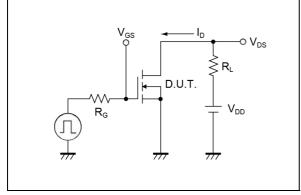


Fig.2-1 Gate Charge Measurement Circuit

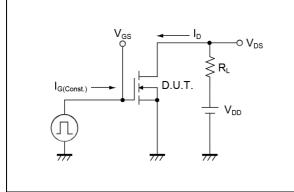


Fig.3-1 Avalanche Measurement Circuit

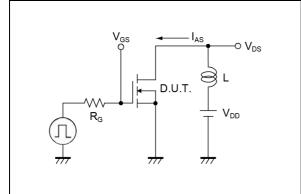


Fig.4-1 Diode Recovery Measurement Circuit

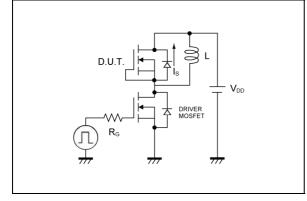


Fig.1-2 Switching Waveforms

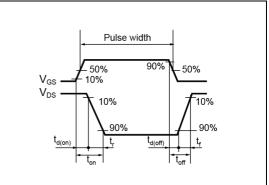


Fig.2-2 Gate Charge Waveform

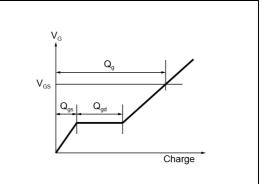


Fig.3-2 Avalanche Waveform

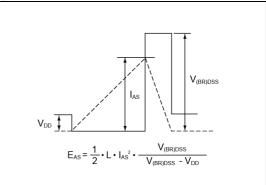
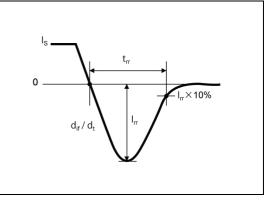
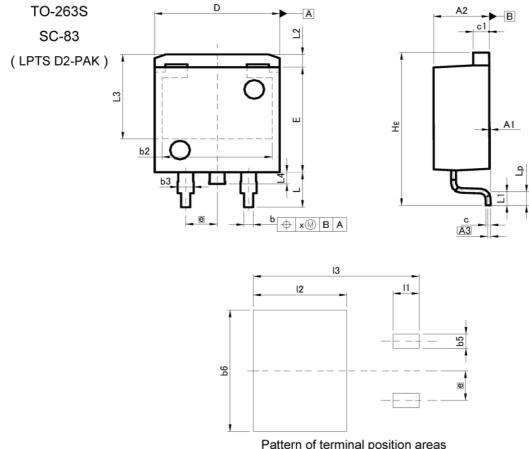


Fig.4-2 Diode Recovery Waveform





Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

		ETERS	INC	HES
DIM -	MIN	MAX	MIN	MAX
A1	0.00	0.30	0.000	0.012
A2	4.30	4.70	0.169	0.185
A3	0.	25	0.0	10
b	0.68	0.98	0.027	0.039
b2	8.	90		350
b3	1.14	1.44	0.045	0.057
C	0.30	0.60	0.012	0.024
c1	1.10	1.50	0.043	0.059
D	9.80	10.40	0.386	0.409
E	8.80	9.20	0.346	0.362
e	2.	54	0.1	00
HE	12.80	13.40	0.504	0.528
L	2.70	3.30	0.106	0.130
L1	1.	20	0.047	
L2	1.	10	0.043	
L3	7.	25	0.285	
L4	1.	1.00)39
Lp	0.90	1.50	0.035	0.059
x	A	0.25	-	0.010
	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
b5		1.23	-	0.049
b6	-	10.40	· · · · · · · · · · · · · · · · · · ·	0.409
11	<u>22</u> 8	2.10	<u>, 12</u>	0.083
12		7.55	1. 1979:	0.297
13	-	13.40	-	0.528

Dimension in mm/inches





Notice

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1. Our Products are designed and manufactured for application in ordinary electronic equipment (such as AV equipment, OA equipment, telecommunication equipment, home electronic appliances, amusement equipment, etc.). If you intend to use our Products in devices requiring extremely high reliability (such as medical equipment (^{Note 1)}, transport equipment, traffic equipment, aircraft/spacecraft, nuclear power controllers, fuel controllers, car equipment including car accessories, safety devices, etc.) and whose malfunction or failure may cause loss of human life, bodily injury or serious damage to property ("Specific Applications"), please consult with the ROHM sales representative in advance. Unless otherwise agreed in writing by ROHM in advance, ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of any ROHM's Products for Specific Applications.

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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSI	CLASS II b	CLASSII
CLASSⅣ	CLASSII	CLASSⅢ	CLASSI

- 2. ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
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 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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