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Product Change Notification - SYST-14SDBI473   **(Printer Friendly)**

Date:	15 Dec 2015
Affected CPNs:	📕 📗
Notification subject:	ERRATA - PIC18(L)F26/46K22 Rev. A2/A4 Silicon Errata and Data Sheet Clarification Errata Document Revision
Notification text:	

SYST-14SDBI473

Microchip has released a new DeviceDoc for the PIC18(L)F26/46K22 Rev. A2/A4 Silicon Errata and Data Sheet Clarification of devices. If you are using one of these devices please read the document located at PIC18(L)F26/46K22 Rev. A2/A4 Silicon Errata and Data Sheet Clarification.

Notification Status: Final

Description of Change: Data Sheet Clarifications: Added Module 1: ECCP, applying corrections to bits 6-4 in Register 14-5.

Pre Change: NA

Post Change: ECCPxASbits.CCPxAS options have been corrected 101 = FLT0 pin – low level will cause shutdown event or Comparator C1 (async_C1OUT) – output high will cause shutdown event 110 = FLT0 pin – low level will cause shutdown event or Comparator C2 (async_C2OUT) – output high will cause shutdown event 111 = FLT0 pin – low level will cause shutdown event or Comparators C1 or C2 – output high will cause shutdown event

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 15 Dec 2015

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachment(s):	PIC18(L)F26/46K22 Rev. A2/A4 Silicon Errata and Data Sheet Clarification

Please contact your local Microchip sales office with questions or concerns regarding this notification.

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