			151ERA008054
FARNELL	Date of Issue	:	December 12.2008
			■ New □ Changed

PRODUCT SPECIFICATION FOR APPROVAL

Product Description : Metal Film Chip Resistors (RoHS Compliance)

Product Part Number : $ERA6A \square \square xxxV$

Country of Origin : JAPAN

Applications : Standard electronic equipment

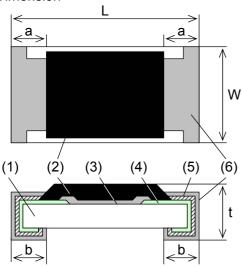
*If you approve	this specification, please fill in and sign the below and return 1 copy to us.
Approval No	:
Approval Date	
Executed by	:
	(signature)
Title	:
Dept.	:

Circuit Components Business Unit	Prepared by	:	Engineering Section
Panasonic Electronic Devices Co., Ltd.	Contact Person	:	
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Subject Metal Film Chip Resistor	PRODUCT SPECIFICATION FOR INFORMATION	Spec. No. 151-SRA-6A00FE
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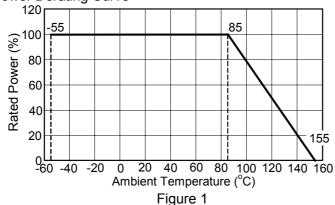
1. Dimension



- (1) Substrate: Alumina
- (2) Protective Coating: Epoxy Resin(3) Resistive Element: High Reliability Metal Alloy(4) Inner Termination: Special Termination
- (5) Middle Termination: Ni Plating
- (6) Outer Termination: Sn Plating

	L	W	а	b	t
Dimension (mm)	2.00±0.20	1.25±0.10	0.40±0.25	0.40±0.25	0.50±0.10

2. Power Derating Curve



Category temperature range :-55 °C to +155 °C

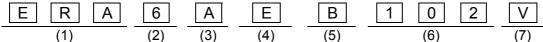
3. Ratings

<u>, i i tatii i go</u>		
Item	Rated value (Explanation)	
Power rating	0.125 W When used at ambient temperature above 85 °C, power rating shall be determined in accordance with Figure 1.	
Rated voltage	The rated voltage of each resistor shall be calculated from the equation	
&	below. In case the rated voltage exceeds the limiting element voltage,	
Rated continuous	the limiting element voltage shall be the rated voltage.	
working voltage	Rated voltage(V) = $\sqrt{\text{Power rating(W)} \times \text{Resistance value}(\Omega)}$	
(RCWV)	Max. RCWV ;100V	

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Item	Rated value (Explanation)			
Maximum overload voltage	Voltage shall be 2.5 times the rated voltage. In case voltage exc the maximum overload voltage, the value shown below shall be maximum overload voltage. Max. overload voltage: 200V			
Resistance range	Resistance tolerance ±0.5% ±0.1%, ±0.5% ± 0.1% ± 0.05%, ± 0.1% E-24 series and E-96 s	T.C.R. ±50×10 ⁻⁶ /°C ±25×10 ⁻⁶ /°C ±15×10 ⁻⁶ /°C ±10×10 ⁻⁶ /°C eries shall be standa	Resistance range $10\Omega \sim 46.4\Omega$ $47\Omega \sim 1M\Omega$ $470\Omega \sim 100k\Omega$ $1k\Omega \sim 100k\Omega$ ard.	

4.	Expl	lanation	of l	Part	Number
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- (1) Product code: Metal Film Chip Resistor
- (2) Size and power rating: 2.00 mm × 1.25 mm, 0.125W
- (3) Series

	Code	Series	
	Α	High reliability type	
Η	Towns and the Coefficient of Decistors (T.C.D.)		

(4) Temperature Coefficient of Resistance (T.C.R.)

Code	Resistance tolerance
R	± 10 × 10 ⁻⁶ / °C
Р	± 15 × 10 ⁻⁶ / °C
E	± 25 × 10 ⁻⁶ / °C
Н	± 50 × 10 ⁻⁶ / °C

(5) Resistance Tolerance

Code	e Resistance tolerance	
W	± 0.05%	
В	± 0.1%	
D	± 0.5%	

(6) Resistance value

<E-24 series>

3-digits : The first two digits are the significant figures of resistance value, and the last figure shows the number of zero following in ohm.

ex) 123
$$12 \times 10^3$$
 $12k\Omega$

<E-96 series>

4-digits: The first three digits are the significant figures of resistance value, and the last figure shows the number of zero following in ohm.

ex) 1212
$$121 \times 10^2$$
 $12.1k\Omega$

In case the resistance value is same as E-24 series, E-24 series part number shall be applied.

(7) Packaging configuration

Code Packaging		Packaging configuration	
	V	Punched carrier taping (5,000pcs/reel)	

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5. Appearance & Construction

Item	Explanation
Appearance & Construction	 The resistive element shall be covered with protective coating that does not fade easily. There shall not be significant unevenness, flaw, pinhole and discoloration on surface of coating. The electrode shall be formed uniformly, as shown in the dimensions. The plating shall not fade easily. There shall not be significant unevenness, flaw, pinhole, projection and discoloration. The electrode shall be connected electrically, mechanically to resistive element.

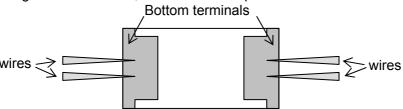
As far as there shall not designation especially, the following tests and measurement shall be operated under the following conditions.

Normal temperature: 15 °C to 35 °C Normal humidity: 25 %RH to 75 %RH

Normal atmospheric pressure: 86 k Pa to 106 k Pa

<Measuring method>

In measuring resistance value, 4 wires must be put on the bottom terminals as below.



6. Performance Specification

5. Performance Specification					
Item	Specification	Test method (JIS-C5201-1)			
DC resistance	DC resistance value shall be within the specified tolerance.	At 20 °C, 65%RH			
Temperature coefficient of resistance (TCR)	H:±50×10 ⁻⁶ /°C E:±25×10 ⁻⁶ /°C P:±15×10 ⁻⁶ /°C R:±10×10 ⁻⁶ /°C	Natural resistance change per temperature degree centigrade.			
Short time overload	Resistance tolerance \pm 0.5%: Δ R: \pm (0.5% +0.01 Ω) Resistance tolerance \pm 0.1%,0.05%: Δ R: \pm (0.1% +0.01 Ω)	Resistors shall be applied 2.5 times the rated voltage for 5 s. Max. overload voltage shall be 200V			
Dielectric withstanding	No evidence of flashover, mechanical damage, arcing or insulation breakdown	AC 200V between substrate and termination for 1 min. AC powersupply or Insulation resistance			
Insulation resistance	Min. 1,000 MΩ	Resistors shall be facing down. After applying DC 200V to the resistor, insulation resistance shall be measured.			

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Metal Fill Chip Resistor	TRODUCT SECULICATION FOR INFORMATION	101 010 C 0 1001 E
Part.No.		9 - 4
	ERA6A	9-4

7. Mechanical Characteristics

7.	Mechanical Characteristics				
	Item	Specifications	Test method (JIS-C5201-1)		
	Bending strength	Resistance tolerance \pm 0.5%: Δ R: \pm (0.5% +0.01 Ω) Resistance tolerance \pm 0.1%,0.05%: Δ R: \pm (0.1% +0.01 Ω) and no mechanical damage.	Substrate: Glass epoxy (t=1.6 mm) Span: 90mm Bending distance: 3mm (10 s) <test pattern=""> 1.4 1.2 1.4 (unit: mm)</test>		
	Solderability	Termination shall be covered uniformly with solder (Min. 95% coverage)	Resistors shall be dipped in the melted solder bath at 235 $^{\circ}$ C \pm 5 $^{\circ}$ C for 2 s \pm 0.5 s. Flux shall be removed from the surface of termination with clean organic solvent.		
	Resistance to soldering heat	Resistance tolerance \pm 0.5%: Δ R: \pm (0.5% +0.01 Ω) Resistance tolerance \pm 0.1%,0.05%: Δ R: \pm (0.1% +0.01 Ω)	Resistors shall be dipped in the melted solder bath at 270 $^{\circ}\text{C} \pm 3~^{\circ}\text{C}$ for 10 s \pm 1 s.		
	Resistance to solvent	Resistance tolerance \pm 0.5%: ΔR : \pm (0.5% +0.01 Ω) Resistance tolerance \pm 0.1%,0.05%: ΔR : \pm (0.1% +0.01 Ω) and without distinct deformation in appearance	Solvent solution: Isopropyl alcohol (1) Dipping 10 h ± 1 h, dry in room condition for 30 min ± 10 min. (2) Ultrasonic wave washing 5 min ± 1 min (0.3W/cm², 28kHz), dry in room condition for 30 min ± 10 min.		

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8. Environmental Test

u.	o. Environmental lest						
	Item	Specification	Test method (JIS-C5201-1)				
	High temperature exposure	Resistance tolerance \pm 0.5%: Δ R: \pm (0.5% +0.01 Ω) Resistance tolerance \pm 0.1%,0.05%: Δ R: \pm (0.1% +0.01 Ω)	Resistors shall be exposed at 155 $^{\circ}$ C \pm 3 with no load for 1000 h +48/-0 h.				
	Temperature cycling	Resistance tolerance \pm 0.5%: Δ R: \pm (0.5% +0.01 Ω) Resistance tolerance \pm 0.1%,0.05%: Δ R: \pm (0.1% +0.01 Ω)	Resistors shall be tested for 1000 cycles continuously in accordance with the following duty cycle. Judgement shall be made by unit component of the shall be made by unit c			nit component. Time (min.) 30 Max. 3 30	
	Humidity (Steady state) Resistance tolerance \pm 0.5%: Δ R: \pm (0.5% +0.01 Ω) Resistance tolerance \pm 0.1%,0.05%: Δ R: \pm (0.1% +0.01 Ω)		Resistors shall be exposed at 85 $^{\circ}$ C \pm 2 $^{\circ}$ C and 85% relative hummidity in a humidity test chamber for 1000 h +48/-0 h.				
$\begin{array}{c} \text{Resistance toleral} \\ \text{Load Life} \\ \end{array} \begin{array}{c} \text{Resistance toleral} \\ \text{Resistance toleral} \end{array}$		Resistance tolerance \pm 0.5%: Δ R: \pm (0.5% +0.01 Ω) Resistance tolerance \pm 0.1%,0.05%: Δ R: \pm (0.1% +0.01 Ω)	Resistors shall be operated at DC rated voltage (1.5 h "ON", 0.5 h "OFF") for 1000 h \pm 48/-0 h in a test chamber controlled at 85 °C \pm 2 °C.			F") for 1000 h	
Resistors shall be op voltage (1.5 h "ON", (Δ R: \pm (0.5% +0.01 Ω) Hhumidity Resistors shall be op voltage (1.5 h "ON", (\pm 48/-0 h in a test cha °C \pm 2 °C and at 85 %				.5 h "ON", 0.5 h "OF n a test chamber con and at 85 % relative R 100kΩ ∶Rated vo	F") for 1000 h ntrolled at 85 hummidity.		

9. Resistance value marking

<E-24 series>

Express resistance value on resin side with three digits.



The first two digits are the significant figures of resistance value, and the last figure shows the number of zero following in ohm.

ex) 123 12×10^3 $12k\Omega$

<E-96 series>

No-marking.

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Metal Film Chip Resistor	PRODUCT SPECIFICATION FOR INFORMATION	151-SRA-6A00FE
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10. Common Precautions in Handling Resistors

Notice for use

- (1) This specification shows the quality and performance of a unit component. Before adoption, be sure to evaluate and verify the product mounting it in your product.
- (2) We take no responsibility for troubles caused by the product usage that is not specified in this specification.
- (3) Use fail-safe design and ensure safety by carrying out the following items in cases where it is forecast that the failure of the product gives serious damage to something important like human life, for instant in traffic transportation equipment (trains, cars, traffic signal equipment, etc.), medical equipment, aerospace equipment, electric heating appliances, combustion and gas equipment, rotating equipment, disaster and crime preventive equipment.
 - * Ensure safety as the system by setting protective circuits and protective equipment.
 - * Ensure safety as the system by s etting such redundant circuits as do not cause danger by a single failure.
- (4) When a dogma shall be occurred about safety for this product, be sure to inform us rapidly, operate your technical examination.
- (5) The product is designed to use in general standard applications of general electric equipment (AV products, household electric appliances, office equipment, information and communication equipment, etc.); hence, it do not take the use under the following special environments into consideration. Accordingly, the use in the following special environments, and such environmental conditions may affect the performance of the product; prior to use, verify the performance, reliability, etc. thoroughly.
 - 1) Use in liquids such as water, oil, chemical, and organic solvent.
 - 2) Use under direct sunlight, in outdoor or in dusty atmospheres.
 - 3) Use in places full of corrosive gases such as sea breeze, Cl₂, H₂S, NH₃, SO₂, and NO_X.
 - 4) Use in environment with large static electricity or strong electromagnetic waves.
 - 5) Where the product is close to a heating component, and where an inflammable such as a polyvinyl chloride wire is arranged close to the product.
 - 6) Where the resistor is sealed or coated with resin, etc.
 - 7) Where water or a water-soluble detergent is used in cleaning free soldering and in flux cleaning af ter soldering (Pay particular attention to soluble flux.)
 - 8) Use in such a place where the product is wetted due to dwe condensation.
- (6) If transient load (heavy load in a short time) like pulse is expected to be applied, carry out evaluation and confirmation test with resistors actually mounted on your own board. When the load of more than rated power is applied under the load condition at steady state, it may impair performance and/or reliability of resistor. Never exceed the rated power. When the product shall be used under special condition, be sure to ask us in advance.
- (7) Halogen type (Chlorine type, Bromine type, etc.) or other high-activity flux is not recommended as the residue may affect performance or reliability of resistors.
- (8) When soldering with soldering iron, never touch the body of the chip resistor with a tip of the soldering iron. When using a soldering iron with a tip at high temperature, solder for a time as short as possible. (3 s or less up to 350 °C)
- (9) Avoid physical shock to the resistor and nipping of the resistor with hard tool (a pair of pliers or tweezers) as it may damage protective film or the body of resistor and may affect resistor's performance.
- (10) Avoid immersion of chip resistor in solvent for long time. Use solvent after the effect of immersion is confirmed.

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11. Storage Method

If the product is stored in the following environments and conditions, the performance and solderability may be badly affected, avoid the storage in the following environments.

- (1) Storage in places full of corrosive gases such as sea breeze, Cl_2 , H_2S , NH_3 , SO_2 , and NO_x .
- (2) Storage in places exposed to direct sunlight.
- (3) Storage in places outside the temperature range of 5 °C to 35 °C and humidity range of 45 %RH to 85 %RH.
- (4) The period of guarantee for performance such as solderability is 1 year after our delivery; and this condition applies only to the case where the storage method specified in item (1) to (3) has been followed.

12. Laws and Regulations

- (1) No ODCs or other ozone-depleting substances that are subject to regulation under the Montreal Protocol are used in our manufacturing processes, including in the manufacture of this product.
- (2) This product complies with the RoHS Directive (Restriction of the use of certain Hazardous substances in electrical and electronic equipment (DIRECTIVE 2002/95/EC)).
- (3) All materials used in this product are existing chemical substances recognized under "lows on examination of chemical substances and regulations of manufacturing and others."
- (4) None of the materials used in this product contain the designated incombustible bromic substances, PBBOs and PBBs.
- (5) Please contact us to obtain a notice as to whether this product has passed inspection under review criteria primarily based on Foreign Exchange and Foreign Trade Control Laws, and appended table in the Export Control Laws.

13. Production Site

Country: Japan

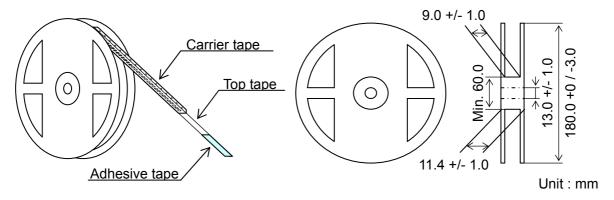
Plant: Panasonic Electronic Devices Japan Co., Ltd.

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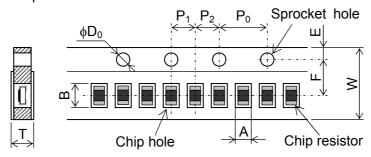
14. Taped and Reel Package

14-1. Physical Dimensions

14-1-1 Structure and dimensions of reel shall be as shown in the figure below. Inaccordance with EIAJ ET-7200.



14-1-2 Carrier Tape Dimension



	Α	В	W	F	E
Dimension (mm)	1.65±0.15	2.50±0.20	8.00±0.20	3.50±0.05	1.75±0.10

	P1	P2	P0	øD0	Т
Dimension (mm)	4.00±0.10	2.00±0.05	4.00±0.10	1.50 ^{+0.10}	0.70±0.05

14-2 Specifications

14-2-1 Taping

(1) Minimum Bending Radius

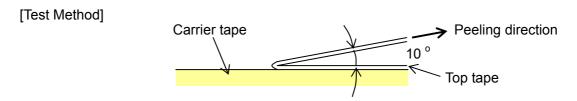
There shall be no defection of chip and no breakage of carrier tape in case carrier tape have been bent by minimum bending radius (15mm). Test shall be conducted for 1 time.

(2) Resistance to climate of top tape

The top tape shall not tear off after exposure at 60 °C, 90 %RH to 95 %RH for 120 h.

(3) Peeling strength

Peeling strength shall be within 0.049 N to 0.49 N. There shall be no burr or breakage after test. Test method is as follows:

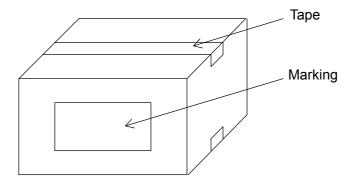


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- 14-2-2 Quantity in Taping: 5,000 pcs./reel
- 14-2-3 Tape packaging
- (1) Resistance side shall be facing upward.
- (2) Chip resistor shall not be sticking to top tape and bottom tape.
- (3) Chip resistor shall be easy to take out from carrier tape and chip hole or sprocket hole shall not have flash and break.

14-3 Outer Packaging

Quantity: 20 reels (Max. 100,000 pcs.)



- * When taping shall not reach Max. or quantity, the remaining empty space shall be buried with buffer material.
- * When the quantity shall be few, alternative packaging methods may be used. No problem must occur during the exportation of the product.

14-4 Marking

At least production country is displayed in English.

- (1) Side of reel (Marking shall be on one side)
 - 1)Part name, 2)Part number, 3)Quantity, 4)Lot number, 5)Maker name, 6) Poduction country
- (2)Packaging box
 - 1)Customer name, 2)Part name, 3)Part number, 4)Customer part number, 5)Quantity.
 - 6)Maker name, 7)Production country