Cypress Semiconductor Corporation, 198 Champion Court, San Jose, CA  95134. Tel: (408) 943-2600

# PRODUCT INFORMATION NOTIFICATION

**PIN:**  PIN163705                                              **Date:**   October 04, 2016

**Subject:**  Updates to Timing Parameters for ASO Mode Entry on 65nm GL-S NOR Flash Memory Devices

**To:**      General Inbox
            NEWARK
            Productchangenotices@newark.com

**Change Type:** Minor

**Product Information:**
Cypress is updating the 65nm GL-S family datasheet with the required timing for the command cycle to enter the ASO modes. (ASO=Address Space Overlay, mapping of non-array data into the address space, for e.g. CFI, DYB, or PPB.) This update clarifies the timing parameters thereby avoiding any incorrect data read from the device after exiting an ASO in the device.

This change does not affect the form, fit and function of the device. Functionality specifications remain unchanged. Refer to the attached datasheet page 84 for the updates. Below table summarizes the datasheet update.

| Page / Section | Parameters | From | To |
|---|---|---|---|
| 84, 10.4.2 | ASO Entry Timing | - | Addition of Table 10.9 and Figure 10.17 |

**Part Numbers Affected:**
This update is applicable to all MPNs as per this datasheet (Refer Section 13)

**Approximate Implementation Date:**
Revised datasheets are now live on www.cypress.com . Please visit the corresponding product page to download the latest copy.

**Anticipated Impact:**
None. There is no change to the product. Products manufactured with updated datasheet parameters are completely compatible with existing product from a functional, parametric, and quality performance perspective. The updated datasheet is a clarification on proper timing requirements for ASO Entry Mode.

Cypress also recommends that customers take this opportunity to review these changes against current application notes, system design considerations and customer environment conditions to assess impact (if any) to their application.

**Method of Identification:**
Cypress maintains traceability of product to wafer level, including wafer fabrication location, through the lot number marked on the package.

**Response Required:**
This is an information only announcement. No response is required

For additional information regarding this change, contact your local sales representative or contact the PCN Administrator at pcn_adm@cypress.com.


Sincerely,


Cypress PCN Administration