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Product Change Notification - SYST-08XRFN605 (Printer Friendly)

Date:

18 Nov 2016

Product Category:

8-bit Microcontrollers

Notification subject:

Data Sheet - 42721-AVR-ATtiny417-814-816-817 Data Sheet Complete

Notification text:

SYST-08XRFN605

Microchip has released a new DeviceDoc for the 42721-AVR-ATtiny417-814-816-817 Data Sheet Complete of devices. If you are using one of these devices please read the document located at 42721-AVR-ATtiny417-814-816-817 Data Sheet Complete.

Notification Status: Final

Description of Change: 1) Updated the document to add the sections conventions, peripheral sections, 16 bit registers updated and editorial updated throughout the document. 2) Updated and revised the following sections: Pinout, SYSCFG - System configuration, I/O Multiplexing and considerations, AVR CPU, CLKCTRL - Clock controller, FUSES - Configuration and User Fuses, NVMCTRL - Non Volatile Memory Controller, SLPCTRL - Sleep controller, CPUINT - CPU Interrupt controller, EVSYS - Event System, PORT - I/O Pin Controller, BOD - Brownout Detector, TCA - 16-bit Timer/Counter Type A, TCB - 16-bit Timer/Counter Type B, TCD - 12 bit Timer/Counter Type D, RTC - Real Time Counter, USART - Universal Synchronous and Asynchronous receiver and transmitter, TWI - Two Wire Interface, CRCCSAN - Cyclic Redundancy Check Memory, CCL - Configurable Custom Logic, ADC - Analog to Digital Converter, DAC - Digital to Analog Converter, AC - Analog Comparator, UPDI - Unified Program and Debug Interface, Electrical Characteristics and Typical Characteristics.

Impacts to Data Sheet: None

Reason for Change: To Improve Manufacturability

Change Implementation Status: Complete

Date Document Changes Effective: 18 Nov 2016

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachment(s):

42721-AVR-ATtiny417-814-816-817 Data Sheet Complete SYST-08XRFN605_Affected_CPN.pdf SYST-08XRFN605_Affected_CPN.xlsx

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