AX3-0006-T

Request Samples 🕥



L x W x H mm RoHS/RoHS II Compliant MSL Level = 1

Features

- Exceptionally Low RMS Jitter: < 95fs Typ (120fs Max)
- ±20ppm stability over industrial operating temperature (0°C to +105°C)
- 1.8V supply voltage
- HCSL differential output
- Industry standard 3.2 x 2.5 x 1.0 mm footprint
- Available in Abracon's Global Distribution Network

Applications

PCI Express

Electrical Specifications

Parar	neters	Min.	Typ.	Max.	Units	Notes	
Output Frequency		100.000		MHz			
Supply Voltage (Vdd)		1.71	1.8	1.89	V		
Supply Current (Idd)			32	40	mA		
Operating Temperature Range		0		105	°C		
Storage Temperature		-55		150	°C		
Frequency Accuracy (Initial Set-Tolerance)		-10		10	ppm	Relative to the carrier at time of shipment (Pre-Reflow) @ +25°C	
Frequency Stability over Operating Temperature Range		-20		20	ppm	0°C to +105°C [Note 1]	
Aging over 20 Year Product Life		-15		15	ppm	[Note 2]	
All-Inclusive Frequency Accuracy (Total Stability) over 20 Year Product Life		-45		45	ppm	[Note 2, 3]	
Output Logic		HCSL					
Rise (Tr) / Fall (Tf) Time			0.2	0.6	ns	20% to 80% of Waveform	
Duty Cycle		45		55	%	Measured at 50% of Waveform $RL=50\Omega$ to Ground	
Output Voltage	V _{OH}	0.55		1.0	V	RL=50Ω to Ground See Recommended Test Circuit	
	Vol	-0.15	0	+0.15	V		
Output Voltage Swing (Vopp)		0.400			V		
Start-up Time			< 2	5	ms	[Note 1]	
RMS Phase Jitter			0.095	0.120	pSec	12kHz -20MHz BW	
Period Jitter: Cycle to Cycle				350	fSec		
Period Jitter: Long Term (10k cycles min)				750	fSec		
Output Enable & Disable Control		0.7*(Vdd)			V	Output Enable; or No Connect	
				0.3*(Vdd)		Output Disable; High Impedance	
Output Enable Time			< 2	5.0	ms		
Output Disable Time				0.2	μs		
Output Disable Current Consumption				30	μA	$OE \le 0.3V$	

Note 1: Relative to initial measured frequency @ +25°C

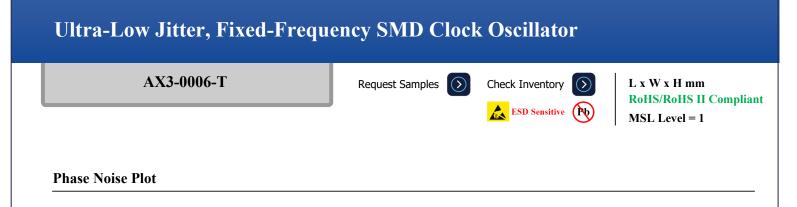
Note 2: Relative to post-reflow frequency @ +25°C

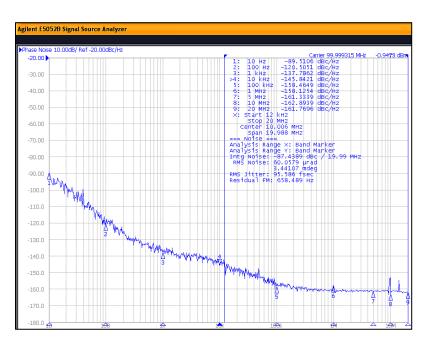
Note 3: Includes temperature stability, initial frequency accuracy, load pulling, power supply variation, and 20 year aging



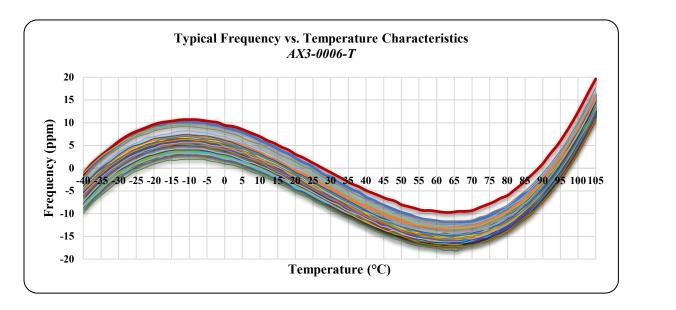
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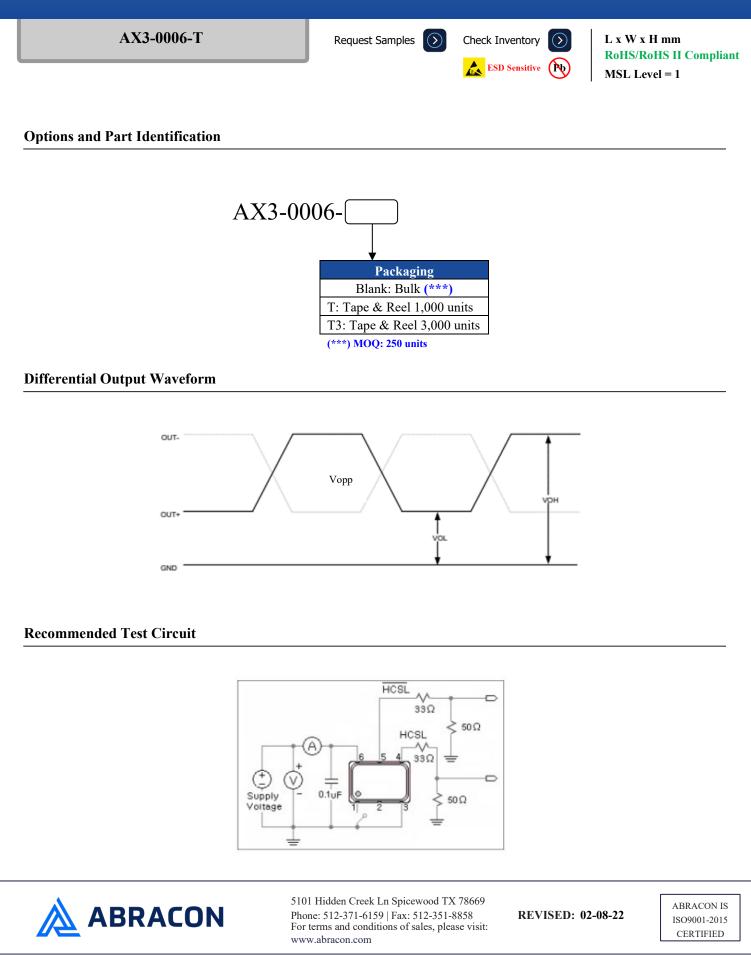
Typical Frequency vs. Temperature Characteristics

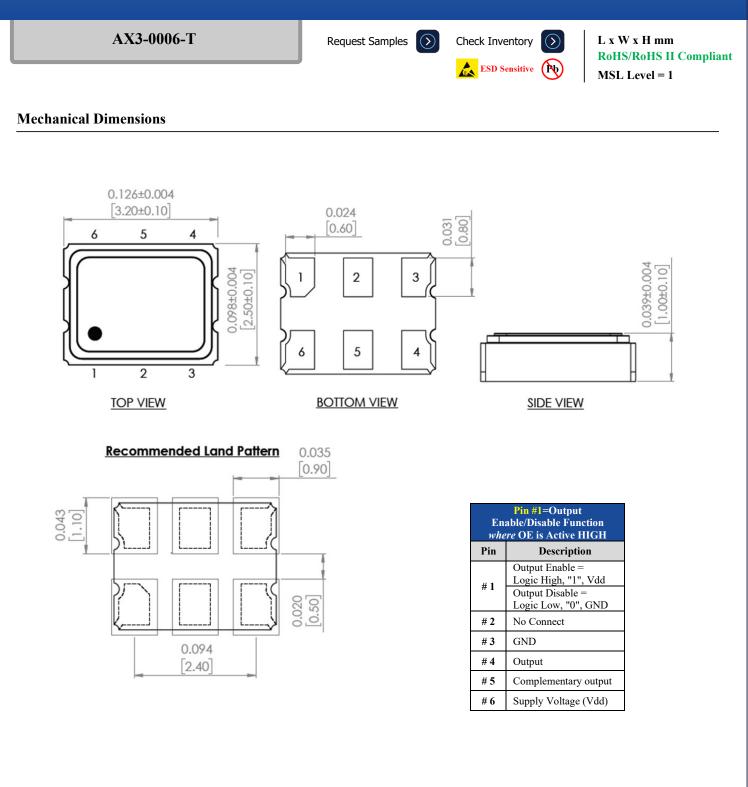


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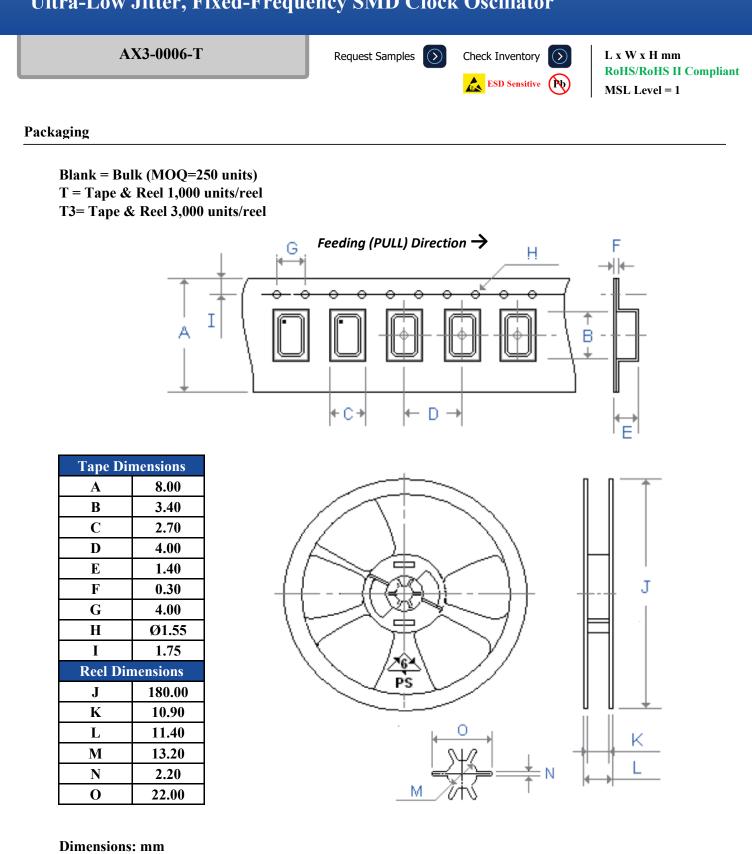


Dimensions: inches [mm]



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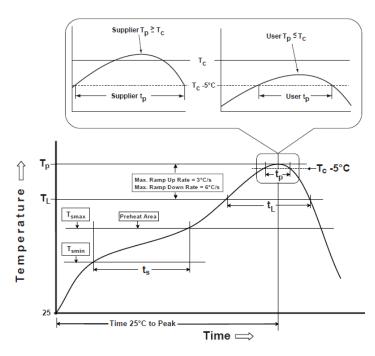
Check Inventory

(Pb)

ESD Sensitive

L x W x H mm RoHS/RoHS II Compliant MSL Level = 1

Reflow Profile [JEDEC J-STD-020]



SnPb Eutectic Process Classification Temperatures (Tc)						
Package Thickness	Volume mm ³ <350	Volume mm ³ <u>></u> 350				
<2.5 mm	235 °C	220 °C				
>2.5 mm	220 °C	220 °C				

Table 2

Pb-Free Process Classification Temperatures (T_c) Volume mm Package Volume mm³ Volume mm³ Thickness <350 350-2000 >2000 <1.6 mm 260 °C 260 °C 260 °C 1.6 mm - 2.5 mm 260 °C 250 °C 245 °C >2.5 mm 250 °C 245 °C 245 °C

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat / soak		
Temperature minimum (T _{smin})	100°C	150°C
Temperature maximum (T _{smax})	150°C	200°C
Time (T _{smin} to T _{smax}) (t _s)	60 - 120 sec.	60 - 120 sec.
Average ramp-up rate (T _{smax} to T _P)	3°C/sec. max	3°C/sec. max
Liquidous temperature (T _L)	183°C	217°C
Time at liquidous (t _L)	60 - 150 sec.	60 - 150 sec.
Peak package body temperature (T _P)*	see Table 1	see Table 2
Time (t _p)** within 5°C of the specified classification temperature (T _C)	20 sec.	30 sec.
Ramp-down rate (T _p to T _{smax})	6°C/sec. max	6°C/sec. max
Time 25°C to peak temperature	6 min. max	8 min. max
Reflow cycles	2 max	2 max

*Tolerance for peak profile temperature (T_P) is defined as a supplier minimum and a user maximum.

**Tolerance for time at peak profile temperature (t_p) is defined as supplier minimum and a user maximum.

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