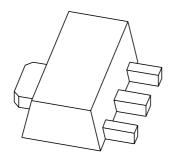
DISCRETE SEMICONDUCTORS

DATA SHEET



BST39; BST40 NPN high-voltage transistors

Product data sheet Supersedes data of 2000 Jul 03 2004 Dec 14



NPN high-voltage transistors

BST39; **BST40**

FEATURES

• Low current (max. 100 mA)

• High voltage (max. 350 V).

APPLICATIONS

• General purpose switching and amplification.

DESCRIPTION

NPN high-voltage transistor in a SOT89 plastic package. PNP complements: BST15 and BST16.

MARKING

TYPE NUMBER	MARKING CODE
BST39	AT1
BST40	AT2

PINNING

PIN	DESCRIPTION
1	emitter
2	collector
3	base

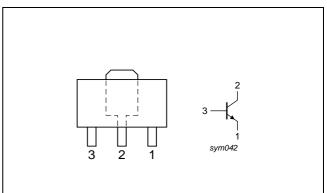


Fig.1 Simplified outline (SOT89) and symbol.

ORDERING INFORMATION

TYPE NUMBER		PACKAGE	
TIPE NOMBER	NAME	DESCRIPTION	VERSION
BST39	SC-62	plastic surface mounted package; collector pad for good heat	SOT89
BST40		transfer; 3 leads	

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NPN high-voltage transistors

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LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter			
	BST39		_	400	V
	BST40		_	300	V
V _{CEO}	collector-emitter voltage	open base			
	BST39		_	350	V
	BST40		_	250	V
V _{EBO}	emitter-base voltage	open collector	_	5	V
I _C	collector current (DC)		_	100	mA
I _{CM}	peak collector current		_	200	mA
I _{BM}	peak base current		_	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	1.3	W
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	ambient temperature		-65	+150	°C

Note

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th(j-a)}	thermal resistance from junction to ambient	note 1	96	K/W
R _{th(j-s)}	thermal resistance from junction to soldering point		16	K/W

Note

CHARACTERISTICS

T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector-base cut-off current	I _E = 0 A; V _{CB} = 300 V	_	20	nA
I _{EBO}	emitter-base cut-off current	$I_C = 0 A; V_{EB} = 5 V$	_	100	nA
h _{FE}	DC current gain	$I_C = 20 \text{ mA}; V_{CE} = 10 \text{ V}$	40	-	
V _{CEsat}	collector-emitter saturation voltage	$I_C = 50 \text{ mA}; I_B = 4 \text{ mA}$	_	500	mV
C _c	collector capacitance	$I_E = i_e = 0 \text{ A}; V_{CB} = 10 \text{ V}; f = 1 \text{ MHz}$	_	2	pF
f _T	transition frequency	$I_C = 10 \text{ mA}; V_{CE} = 10 \text{ V}; f = 100 \text{ MHz}$	70	_	MHz

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^{1.} Device mounted on a printed-circuit board, single-sided copper, tin-plated, mounting pad for collector 6 cm². For other mounting conditions, see "Thermal considerations for SOT89 in the General Part of associated Handbook".

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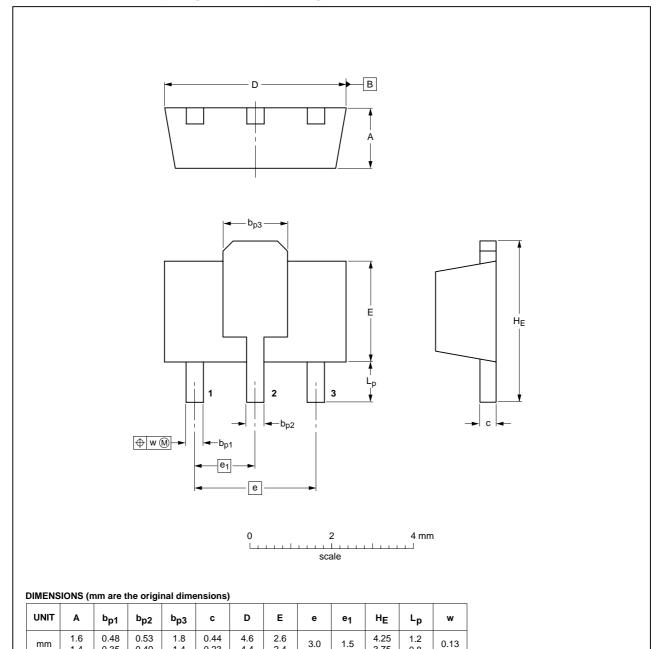
NPN high-voltage transistors

BST39; **BST40**

PACKAGE OUTLINE

Plastic surface-mounted package; collector pad for good heat transfer; 3 leads

SOT89



OUTLINE	OUTLINE REFERENCES			EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	JEITA	PROJECTION	ISSUE DATE	
SOT89		TO-243	SC-62		04-08-03 06-03-16	

3.75

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0.35

0.40

0.23

NPN high-voltage transistors

BST39; **BST40**

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

- 1. Please consult the most recently issued document before initiating or completing a design.
- The product status of device(s) described in this document may have changed since this document was published
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Customer notification

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

Contact information

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