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# **High Performance Schottky Rectifier, 18 A**

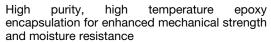


D <sup>2</sup> PAK	TO 0	2024	•
D-PAK	(10-2	203AI	3

PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	18 A			
$V_{R}$	35 V, 40 V, 45 V			
V <sub>F</sub> at I <sub>F</sub>	0.53 V			
I <sub>RM</sub>	25 mA at 125 °C			
T <sub>J</sub> max.	175 °C			
E <sub>AS</sub>	24 mJ			
Package	D <sup>2</sup> PAK (TO-263AB)			
Circuit configuration	Single			

#### **FEATURES**

- 175 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation





- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **DESCRIPTION**

The VS-18TQ... Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UI				
I <sub>F(AV)</sub>	Rectangular waveform	18	А			
$V_{RRM}$	Range	35 to 45	V			
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	1800	А			
V <sub>F</sub>	18 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.53	V			
TJ	Range	-55 to +175	°C			

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-18TQ035S-M3	VS-18TQ040S-M3	VS-18TQ045S-M3	UNITS
Maximum DC reverse voltage	$V_R$	35	40	45	\/
Maximum working peak reverse voltage	$V_{RWM}$	33	40	45	V

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDI	TIONS	VALUES	UNITS		
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 149 °C	18	А			
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated	1800	А		
non-repetitive surge current See fig. 7	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	load condition and with rated V <sub>RRM</sub> applied	390			
Non-repetitive avalanche energy	E <sub>AS</sub>	$T_J = 25  ^{\circ}\text{C},  I_{AS} = 3.6  \text{A},  L = 3.7  \text{mH}$		24	mJ		
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		3.6	Α		

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS			
		18 A	- T <sub>.I</sub> = 25 °C	0.60	V	
Maximum forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	36 A	1j = 25 C	0.72		
See fig. 1	V FM (1)	18 A	- T <sub>.I</sub> = 125 °C	0.53	V	
		36 A	1j = 125 C	0.67		
Maximum reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>B</sub> = Rated V <sub>B</sub>	2.5	mA	
See fig. 2	IRM (')	T <sub>J</sub> = 125 °C	v <sub>R</sub> = nateu v <sub>R</sub>	25	] IIIA	
Maximum junction capacitance	C <sub>T</sub>	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		1400	pF	
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		8.0	nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs	

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300 µs, duty cycle < 2 %

PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature ra		T <sub>J</sub> , T <sub>Stg</sub>		-55 to 175	°C	
Maximum thermal resistance, junction to case		R <sub>thJC</sub>	DC operation See fig. 4	1.50		
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth, and greased	0.50	°C/W	
Annewigate weight				2	g	
Approximate weight				0.07	OZ.	
Manustinantaum	minimum			6 (5)	kgf · cm	
Mounting torque n	maximum			12 (10)	(lbf · in)	
Marking device				18TQ035S		
			Case style D <sup>2</sup> PAK (TO-263AB)	18TQ	040S	
				18TQ	045S	

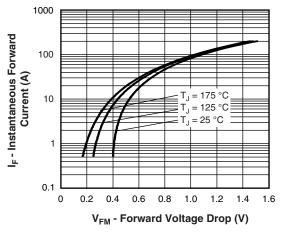


Fig. 1 - Maximum Forward Voltage Drop Characteristics

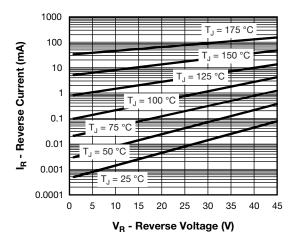


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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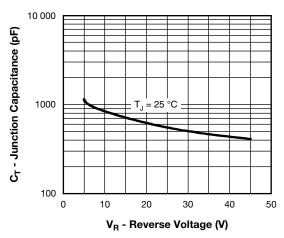


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

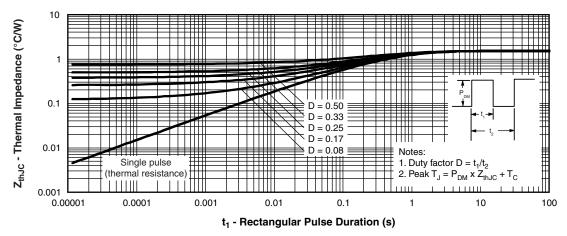


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

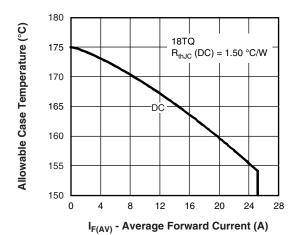


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

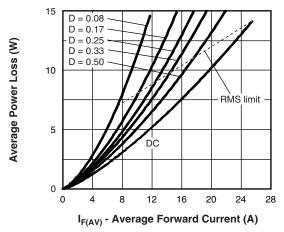
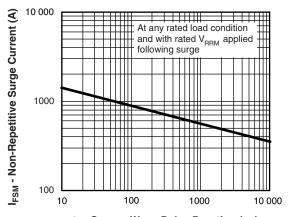


Fig. 6 - Forward Power Loss Characteristics

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 $t_{\text{p}}$  - Square Wave Pulse Duration (µs)

Fig. 7 - Maximum Non-Repetitive Surge Current

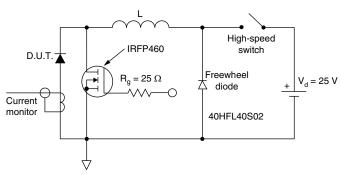


Fig. 8 - Unclamped Inductive Test Circuit

#### **ORDERING INFORMATION TABLE**

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**Device code** VS-18 T Q 045 S **TRL** -M3 2 (3) 4 5 6 (8) Vishay Semiconductors product Current rating (18 A) Circuit configuration: T = TO-220 Schottky "Q" series 035 = 35 V040 = 40 VVoltage ratings 045 = 45 V $S = D^2PAK (TO-263AB)$ • None = tube • TRL = tape and reel (left oriented) • TRR = tape and reel (right oriented)

Revision: 27-Oct-17 4 Document Number: 94928

-M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free



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ORDERING INFORMATION							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-18TQ035S-M3	50	1000	Antistatic plastic tubes				
VS-18TQ035STRR-M3	800	800	13" diameter reel				
VS-18TQ035STRL-M3	800	800	13" diameter reel				
VS-18TQ040S-M3	50	1000	Antistatic plastic tubes				
VS-18TQ040STRR-M3	800	800	13" diameter reel				
VS-18TQ040STRL-M3	800	800	13" diameter reel				
VS-18TQ045S-M3	50	1000	Antistatic plastic tubes				
VS-18TQ045STRR-M3	800	800	13" diameter reel				
VS-18TQ045STRL-M3	800	800	13" diameter reel				

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?96164			
Part marking information	www.vishay.com/doc?95444			
Packaging information	www.vishay.com/doc?96424			
SPICE model	www.vishay.com/doc?96209			



# Vishay Semiconductors

# D<sup>2</sup>PAK

#### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIMETERS		INC	HES	NOTES	SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOIES	STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190		D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010		Е	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039		E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4	е	2.54	BSC	0.100	) BSC	
b2	1.14	1.78	0.045	0.070		Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4	L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029		L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4	L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065		L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2	L4	4.78	5.28	0.188	0.208	

#### Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch
- (7) Outline conforms to JEDEC® outline TO-263AB

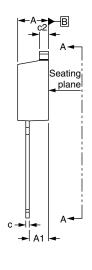


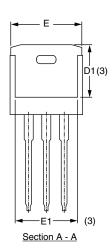
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## **TO-262**

#### **DIMENSIONS** in millimeters and inches

# Modified JEDEC outline TO-262 (Datum A) (2) (3) (3) L1 D D D C C C C A (2) A (2) A (3) L2 B B B B C C C A (2)



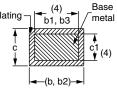


**⊕** 0.010**⋒**|A**⋒**|B

#### Lead assignments



<u>Diodes</u>
1. - Anode (two die)/open (one die)
2., 4. - Cathode
3. - Anode



Section B - B and C - C Scale: None

CYMPOL	MILLIN	METERS	INCH	IES	NOTES
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190	
A1	2.03	3.02	0.080	0.119	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54	BSC	0.100	BSC	
L	13.46	14.10	0.530	0.555	
L1	-	1.65	-	0.065	3
L2	3.56	3.71	0.140	0.146	

#### Notes

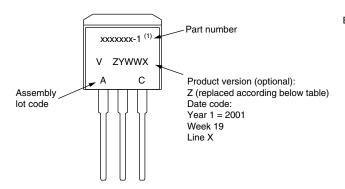
- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- $^{(3)}$  Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline



# **Part Marking Information**

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## **TO-262**



Example: This is a xxxxxxx-1 <sup>(1)</sup> with assembly lot code AC, assembled on WW 19, 2001

in the assembly line "X"

#### Note

(1) If part number contain "H" as last digit, product is AEC-Q101 qualified

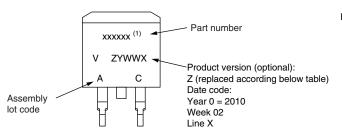
ENVIRONMENTAL NAMING CODE (Z)	PRODUCT DEFINITION		
A	Termination lead (Pb)-free		
В	Totally lead (Pb)-free		
E	RoHS-compliant and termination lead (Pb)-free		
F	RoHS-compliant and totally lead (Pb)-free		
М	Halogen-free, RoHS-compliant and termination lead (Pb)-free		
N	Halogen-free, RoHS-compliant and totally lead (Pb)-free		
G	Green		



# **Part Marking Information**

Vishay Semiconductors

## D<sup>2</sup>PAK



Example: This is a xxxxxx <sup>(1)</sup> with assembly lot code AC, assembled on WW 02, 2010

#### Note

(1) If part number contain "H" as last digit, product is AEC-Q101 qualified

ENVIRONMENTAL NAMING CODE (Z)	PRODUCT DEFINITION
А	Termination lead (Pb)-free
В	Totally lead (Pb)-free
E	RoHS-compliant and termination lead (Pb)-free
F	RoHS-compliant and totally lead (Pb)-free
M	Halogen-free, RoHS-compliant, and termination lead (Pb)-free
N	Halogen-free, RoHS-compliant, and totally lead (Pb)-free
G	Green



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