



Product Change Notification / SYST-23DAMK194

Date:

27-Apr-2021

Product Category:

8-bit Microcontrollers, Power Management - System Supervisors/Voltage Detectors

PCN Type:

Document Change

Notification Subject:

Data Sheet - MCP131X/2X Voltage Supervisor Datasheet Document Revision

Affected CPNs:

[SYST-23DAMK194_Affected_CPN_04272021.pdf](#)

[SYST-23DAMK194_Affected_CPN_04272021.csv](#)

Notification Text:

SYST-23DAMK194

Microchip has released a new Product Documents for the MCP131X/2X Voltage Supervisor Datasheet of devices. If you are using one of these devices please read the document located at [MCP131X/ 2X Voltage Supervisor Datasheet](#).

Notification Status: Final

Description of Change:

- 1) Added information about the automotive qualification status of the device in the Features section.
- 2) Updated the Product Identification System section with automotive qualified devices.

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 27 Apr 2021

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

[MCP131X/ 2X Voltage Supervisor Datasheet](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

Terms and Conditions:

If you wish to receive Microchip PCNs via email please register for our PCN email service at our [PCN home page](#) select register then fill in the required fields. You will find instructions about registering for Microchips PCN email service in the [PCN FAQ](#) section.

If you wish to change your PCN profile, including opt out, please go to the [PCN home page](#) select login and sign into your myMicrochip account. Select a profile option from the left navigation bar and make the applicable selections.

Affected Catalog Part Numbers (CPN)

MCP1316T-20LI/OT
MCP1316T-23LI/OT
MCP1316T-24MI/OT
MCP1316MT-20LI/OT
MCP1316MT-20RI/OT
MCP1316MT-23LI/OT
MCP1319T-31QI/OT
MCP1319MT-24QI/OT
MCP1316T-44JE/OT
MCP1316T-44LE/OT
MCP1316T-44NE/OT
MCP1316T-46BE/OT
MCP1316T-46CE/OT
MCP1316T-46FE/OT
MCP1316T-46KE/OT
MCP1316T-46LE/OT
MCP1316T-47FE/OT
MCP1316T-25LE/OT
MCP1316T-26JE/OT
MCP1316T-26LE/OT
MCP1316T-27LE/OT
MCP1316T-29AE/OT
MCP1316T-29EE/OT
MCP1316T-29LE/OT
MCP1316T-29NE/OT
MCP1316T-30CE/OT
MCP1316T-30FE/OT
MCP1316T-31LE/OT
MCP1316T-31RE/OT
MCP1316T-32LE/OT
MCP1316MT-45GE/OT
MCP1316MT-46FE/OT
MCP1316MT-46GE/OT
MCP1316MT-46LE/OT
MCP1316MT-26CE/OT
MCP1316MT-29GE/OT
MCP1316MT-29KE/OT
MCP1316MT-29LE/OT
MCP1316MT-29ME/OT
MCP1316MT-30HE/OT
MCP1316MT-31LE/OT
MCP1316MT-33LE/OT
MCP1317T-46LE/OT
MCP1317T-25LE/OT
MCP1317T-29LE/OT
MCP1317T-31AE/OT

MCP1317T-31LE/OT
MCP1317T-31RE/OT
MCP1317T-33LE/OT
MCP1318T-45LE/OT
MCP1318T-46LE/OT
MCP1318T-46ME/OT
MCP1318T-29EE/OT
MCP1318T-29HE/OT
MCP1318T-29LE/OT
MCP1318MT-46BE/OT
MCP1318MT-46LE/OT
MCP1318MT-29BE/OT
MCP1318MT-29EE/OT
MCP1318MT-29LE/OT
MCP1319T-44LE/OT
MCP1319T-46LE/OT
MCP1319T-47QE/OT
MCP1319T-28LE/OT
MCP1319T-29CE/OT
MCP1319T-29LE/OT
MCP1319T-42LE/OT
MCP1319MT-46LE/OT
MCP1319MT-47QE/OT
MCP1319MT-26AE/OT
MCP1319MT-26EE/OT
MCP1319MT-28LE/OT
MCP1319MT-29LE/OT
MCP1319MT-30GE/OT
MCP1319MT-33AE/OT
MCP1316T-46FE/OTV02
MCP1316T-30FE/OTV04
MCP1318T-29LE/OTV09
MCP1316T-46FE/OTV13
MCP1316MT-29KE/OTV16
MCP1316T-30CE/OTV18
MCP1316MT-46LE/OTV19
MCP1316MT-29JE/OTV20
MCP1316MT-45GE/OTV21
MCP1316MT-29LE/OTV22
MCP1318T-46FE/OTV24
MCP1316MT-29LE/OTV27
MCP1319T-42LE/OTV31
MCP1316MT-29GE/OTV33
MCP1319MT-26EE/OTV34
MCP1316T-29LE/OTV35
MCP1316MT-29LE/OTV36
MCP1316MT-46FE/OTVAO
MCP1316T-46LE/OTVAO
MCP1316T-29AE/OTVAO

MCP1316T-46FE/OTVAO
MCP1316T-26LE/OTVAO
MCP1316MT-46LE/OTVAO
MCP1316MT-29LE/OTVAO
MCP1316MT-26CE/OTVAO
MCP1318T-29LE/OTVAO
MCP1319T-42LE/OTVAO
MCP1319MT-26AE/OTVAO
MCP1319MT-26EE/OTVAO
MCP1319MT-29LE/OTVAO
MCP1316T-29EE/OTVAO
MCP1316MT-29GE/OTVAO
MCP1316T-30CE/OTVAO
MCP1321T-20LI/OT713
MCP1321T-20LI/OT
MCP1321T-20QI/OT
MCP1321T-43BE/OT
MCP1321T-43BE/OTV30
MCP1320T-46BE/OT
MCP1320T-46GE/OT
MCP1320T-46KE/OT
MCP1320T-46LE/OT
MCP1320T-46FE/OT
MCP1320T-25LE/OT
MCP1320T-27ME/OT
MCP1320T-29BE/OT
MCP1320T-29FE/OT
MCP1320T-29GE/OT
MCP1320T-29LE/OT
MCP1320T-29ME/OT
MCP1320T-29RE/OT
MCP1320T-30BE/OT
MCP1320T-30FE/OT
MCP1320T-30LE/OT
MCP1320T-19HI/OT
MCP1320T-31KE/OT
MCP1320T-31LE/OT
MCP1320T-41LE/OT
MCP1320T-42EE/OT
MCP1320T-42JE/OT
MCP1321T-46CE/OT
MCP1321T-46FE/OT
MCP1321T-46GE/OT
MCP1321T-46LE/OT
MCP1321T-26GE/OT
MCP1321T-29AE/OT
MCP1321T-29CE/OT
MCP1321T-29DE/OT
MCP1321T-29FE/OT

MCP1321T-29KE/OT
MCP1321T-29LE/OT
MCP1321T-29ME/OT
MCP1321T-30LE/OT
MCP1321T-31LE/OT
MCP1322T-44LE/OT
MCP1322T-46EE/OT
MCP1322T-46LE/OT
MCP1322T-27LE/OT
MCP1322T-29CE/OT
MCP1322T-29LE/OT
MCP1322T-31GE/OT
MCP1322T-31LE/OT
MCP1322T-32AE/OT
MCP1320T-30FE/OTV07
MCP1321T-29LE/OTV10
MCP1321T-29CE/OTV15
MCP1320T-30BE/OTV17
MCP1322T-46LE/OTV23
MCP1322T-29LE/OTV25
MCP1322T-29CE/OTV26
MCP1322T-29CE/OTV28
MCP1321T-46LE/OTV32
MCP1320T-30BE/OTV18
MCP1320T-42EE/OTVAO
MCP1320T-29BE/OTVAO
MCP1320T-46BE/OTVAO
MCP1320T-29FE/OTVAO
MCP1320T-46FE/OTVAO
MCP1320T-30BE/OTVAO
MCP1320T-29LE/OTVAO
MCP1320T-42JE/OTVAO
MCP1321T-46LE/OTVAO
MCP1321T-29LE/OTVAO
MCP1322T-46LE/OTVAO
MCP1322T-29LE/OTVAO

Affected Catalog Part Numbers(CPN)

MCP1316T-20LI/OT
MCP1316T-23LI/OT
MCP1316T-24MI/OT
MCP1316MT-20LI/OT
MCP1316MT-20RI/OT
MCP1316MT-23LI/OT
MCP1319T-31QI/OT
MCP1319MT-24QI/OT
MCP1316T-44JE/OT
MCP1316T-44LE/OT
MCP1316T-44NE/OT
MCP1316T-46BE/OT
MCP1316T-46CE/OT
MCP1316T-46FE/OT
MCP1316T-46KE/OT
MCP1316T-46LE/OT
MCP1316T-47FE/OT
MCP1316T-25LE/OT
MCP1316T-26JE/OT
MCP1316T-26LE/OT
MCP1316T-27LE/OT
MCP1316T-29AE/OT
MCP1316T-29EE/OT
MCP1316T-29LE/OT
MCP1316T-29NE/OT
MCP1316T-30CE/OT
MCP1316T-30FE/OT
MCP1316T-31LE/OT
MCP1316T-31RE/OT
MCP1316T-32LE/OT
MCP1316MT-45GE/OT
MCP1316MT-46FE/OT
MCP1316MT-46GE/OT
MCP1316MT-46LE/OT
MCP1316MT-26CE/OT
MCP1316MT-29GE/OT
MCP1316MT-29KE/OT
MCP1316MT-29LE/OT
MCP1316MT-29ME/OT
MCP1316MT-30HE/OT
MCP1316MT-31LE/OT
MCP1316MT-33LE/OT

MCP1317T-46LE/OT
MCP1317T-25LE/OT
MCP1317T-29LE/OT
MCP1317T-31AE/OT
MCP1317T-31LE/OT
MCP1317T-31RE/OT
MCP1317T-33LE/OT
MCP1318T-45LE/OT
MCP1318T-46LE/OT
MCP1318T-46ME/OT
MCP1318T-29EE/OT
MCP1318T-29HE/OT
MCP1318T-29LE/OT
MCP1318MT-46BE/OT
MCP1318MT-46LE/OT
MCP1318MT-29BE/OT
MCP1318MT-29EE/OT
MCP1318MT-29LE/OT
MCP1319T-44LE/OT
MCP1319T-46LE/OT
MCP1319T-47QE/OT
MCP1319T-28LE/OT
MCP1319T-29CE/OT
MCP1319T-29LE/OT
MCP1319T-42LE/OT
MCP1319MT-46LE/OT
MCP1319MT-47QE/OT
MCP1319MT-26AE/OT
MCP1319MT-26EE/OT
MCP1319MT-28LE/OT
MCP1319MT-29LE/OT
MCP1319MT-30GE/OT
MCP1319MT-33AE/OT
MCP1316T-46FE/OTV02
MCP1316T-30FE/OTV04
MCP1318T-29LE/OTV09
MCP1316T-46FE/OTV13
MCP1316MT-29KE/OTV16
MCP1316T-30CE/OTV18
MCP1316MT-46LE/OTV19
MCP1316MT-29JE/OTV20
MCP1316MT-45GE/OTV21
MCP1316MT-29LE/OTV22
MCP1318T-46FE/OTV24
MCP1316MT-29LE/OTV27
MCP1319T-42LE/OTV31
MCP1316MT-29GE/OTV33

MCP1319MT-26EE/OTV34
MCP1316T-29LE/OTV35
MCP1316MT-29LE/OTV36
MCP1316MT-46FE/OTVAO
MCP1316T-46LE/OTVAO
MCP1316T-29AE/OTVAO
MCP1316T-46FE/OTVAO
MCP1316T-26LE/OTVAO
MCP1316MT-46LE/OTVAO
MCP1316MT-29LE/OTVAO
MCP1316MT-26CE/OTVAO
MCP1318T-29LE/OTVAO
MCP1319T-42LE/OTVAO
MCP1319MT-26AE/OTVAO
MCP1319MT-26EE/OTVAO
MCP1319MT-29LE/OTVAO
MCP1316T-29EE/OTVAO
MCP1316MT-29GE/OTVAO
MCP1316T-30CE/OTVAO
MCP1321T-20LI/OT713
MCP1321T-20LI/OT
MCP1321T-20QI/OT
MCP1321T-43BE/OT
MCP1321T-43BE/OTV30
MCP1320T-46BE/OT
MCP1320T-46GE/OT
MCP1320T-46KE/OT
MCP1320T-46LE/OT
MCP1320T-46FE/OT
MCP1320T-25LE/OT
MCP1320T-27ME/OT
MCP1320T-29BE/OT
MCP1320T-29FE/OT
MCP1320T-29GE/OT
MCP1320T-29LE/OT
MCP1320T-29ME/OT
MCP1320T-29RE/OT
MCP1320T-30BE/OT
MCP1320T-30FE/OT
MCP1320T-30LE/OT
MCP1320T-19HI/OT
MCP1320T-31KE/OT
MCP1320T-31LE/OT
MCP1320T-41LE/OT
MCP1320T-42EE/OT
MCP1320T-42JE/OT
MCP1321T-46CE/OT

MCP1321T-46FE/OT
MCP1321T-46GE/OT
MCP1321T-46LE/OT
MCP1321T-26GE/OT
MCP1321T-29AE/OT
MCP1321T-29CE/OT
MCP1321T-29DE/OT
MCP1321T-29FE/OT
MCP1321T-29KE/OT
MCP1321T-29LE/OT
MCP1321T-29ME/OT
MCP1321T-30LE/OT
MCP1321T-31LE/OT
MCP1322T-44LE/OT
MCP1322T-46EE/OT
MCP1322T-46LE/OT
MCP1322T-27LE/OT
MCP1322T-29CE/OT
MCP1322T-29LE/OT
MCP1322T-31GE/OT
MCP1322T-31LE/OT
MCP1322T-32AE/OT
MCP1320T-30FE/OTV07
MCP1321T-29LE/OTV10
MCP1321T-29CE/OTV15
MCP1320T-30BE/OTV17
MCP1322T-46LE/OTV23
MCP1322T-29LE/OTV25
MCP1322T-29CE/OTV26
MCP1322T-29CE/OTV28
MCP1321T-46LE/OTV32
MCP1320T-30BE/OTV18
MCP1320T-42EE/OTVAO
MCP1320T-29BE/OTVAO
MCP1320T-46BE/OTVAO
MCP1320T-29FE/OTVAO
MCP1320T-46FE/OTVAO
MCP1320T-30BE/OTVAO
MCP1320T-29LE/OTVAO
MCP1320T-42JE/OTVAO
MCP1321T-46LE/OTVAO
MCP1321T-29LE/OTVAO
MCP1322T-46LE/OTVAO
MCP1322T-29LE/OTVAO

Voltage Supervisor

Features

- Low Supply Current: 1 μ A (Typical), 10 μ A (Max.)
- Precision Monitoring Trip Point Options:
 - 2.9V and 4.6V (Standard Offerings)
 - 2.0V to 4.7V in 100 mV Increments, (Contact the local Microchip Sales Office)
- Resets Microcontroller in a Power-loss Event
- Reset Delay Time-Out Option:
 - 1.4 ms, 30 ms, 200 ms, or 1.6s (Typical)
- Watchdog Timer Input Time-Out Options:
 - 6.3 ms, 102 ms, 1.6s, or 25.6s (Typical)
- Manual Reset ($\overline{\text{MR}}$) Input (Active-low)
- Single and Complementary Reset Output(s)
- Reset Output Options:
 - Push-Pull (Active-high or Active-low)
 - Open-Drain (Internal or External Pull-up)
- Temperature Range:
 - -40°C to $+85^{\circ}\text{C}$ for Trip Points 2.0 to 2.4V and,
 - -40°C to $+125^{\circ}\text{C}$ for Trip Points $> 2.5\text{V}$
- Voltage Range: 1.0V to 5.5V
- Lead Free Packaging
- The MCP1316(M)/1318/1319(M)/1320/1321/1322 pass Automotive AEC-Q100 Reliability Testing

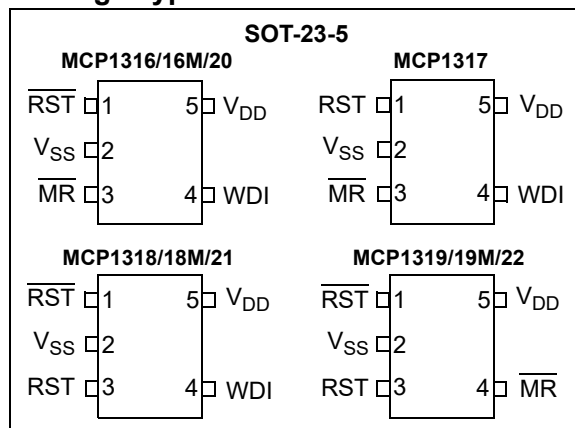
Description

The MCP131X/2X are voltage supervisor devices designed to keep a microcontroller in Reset until the system voltage has reached and stabilized at the proper level for reliable system operation. The table below shows the available features for these devices.

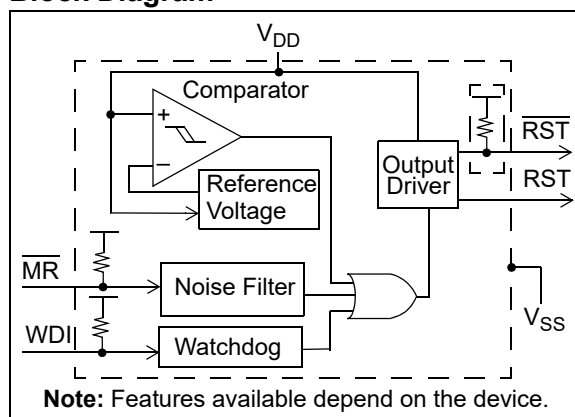
Device Features

Device	Reset Output A			Reset Output B			WDI Input	$\overline{\text{MR}}$ Input
	Type	Pull-up Resistor	Active Level	Type	Pull-up Resistor	Active Level		
MCP1316	Push-Pull	—	Low	—	—	—	Yes	Yes
MCP1316M	Open-Drain	Internal	Low	—	—	—	Yes	Yes
MCP1317	Push-Pull	—	High	—	—	—	Yes	Yes
MCP1318	Push-Pull	—	Low	Push-Pull	—	High	Yes	No
MCP1318M	Open-Drain	Internal	Low	Push-Pull	—	High	Yes	No
MCP1319	Push-Pull	—	Low	Push-Pull	—	High	No	Yes
MCP1319M	Open-Drain	Internal	Low	Push-Pull	—	High	No	Yes
MCP1320	Open-Drain	External	Low	—	—	—	Yes	Yes
MCP1321	Open-Drain	External	Low	Push-Pull	—	High	Yes	No
MCP1322	Open-Drain	External	Low	Push-Pull	—	High	No	Yes

Package Types



Block Diagram



MCP131X/2X

NOTES:

1.0 ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings†

Supply Voltage (V_{DD} to V_{SS})	7.0V
Input current (V_{DD})	10 mA
Output current (\overline{RST})	10 mA
Voltage on all inputs and outputs, except Open-Drain \overline{RST} (with no internal pull-up resistor), w.r.t. V_{SS}	-0.6V to ($V_{DD} + 1.0V$)
Voltage on Open-Drain \overline{RST} (with no internal pull-up resistor) w.r.t. V_{SS}	-0.6V to 13.5V
Storage temperature	-65°C to +150°C
Ambient temp. with power applied	-40°C to +125°C
Maximum Junction temp. with power applied	+150°C
Power Dissipation ($T_A \leq +70^\circ\text{C}$):	
5-Pin SOT-23A	240 mW
ESD protection on all pins	≥ 4 kV

† **Notice:** Stresses above those listed under “Maximum Ratings” may cause permanent damage to the device. This is a stress rating only and functional operation of the device at those or any other conditions above those indicated in the operational listings of this specification is not implied. Exposure to maximum rating conditions for extended periods may affect device reliability.

DC CHARACTERISTICS

Electrical Specifications: Unless otherwise indicated, all limits are specified for $V_{DD} = 1V$ to $5.5V$, $R_{PU} = 100\text{ k}\Omega$ (only **MCP1320**, **MCP1321** and **MCP1322**), $T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$.

Parameters	Sym	Min	Typ	Max	Units	Conditions
Operating Voltage Range	V_{DD}	1.0	—	5.5	V	
Specified V_{DD} Value to V_{OUT} Low	V_{DD}	1.0	—	—	V	$I_{\overline{RST}} = 10\ \mu\text{A}$, $V_{\overline{RST}} < 0.3V$
Operating Current:	I_{DD}	—	5	10	μA	Watchdog Timer Active
		—	1	2	μA	Watchdog Timer Inactive
		—	1	2	μA	$V_{DD} < V_{TRIP}$
		—	5	10	μA	Reset Delay Timer Active

- Note 1:** Trip point is $\pm 1.5\%$ from typical value.
2: Trip point is $\pm 2.5\%$ from typical value.
3: Hysteresis is minimum = 1%, maximum = 6% at $+25^\circ\text{C}$.
4: This specification allows this device to be used in PIC[®] microcontroller applications that require the In-Circuit Serial Programming[™] (ICSP[™]) feature (see device-specific programming specifications for voltage requirements). The total time that the \overline{RST} pin can be above the maximum device operational voltage (5.5V) is 100s. Current into the \overline{RST} pin should be limited to 2 mA. It is recommended that the device operational temperature be maintained between 0°C to $+70^\circ\text{C}$ ($+25^\circ\text{C}$ preferred). For additional information, refer to [Figure 2-35](#).
5: This parameter is established by characterization and is not 100% tested.
6: Custom ordered voltage trip point; minimum order volume requirement. Information available upon request.

MCP131X/2X

DC CHARACTERISTICS (CONTINUED)

Electrical Specifications: Unless otherwise indicated, all limits are specified for $V_{DD} = 1V$ to $5.5V$, $R_{PU} = 100\text{ k}\Omega$ (only **MCP1320**, **MCP1321** and **MCP1322**), $T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$.

Parameters		Sym	Min	Typ	Max	Units	Conditions
V _{DD} Trip Point	MCP13XX-20 (Note 6)	V _{TRIP}	1.970	2.00	2.030	V	T _A = +25°C (Note 1)
			1.950	2.00	2.050	V	T _A = -40°C to +85°C (Note 2)
	MCP13XX-21 (Note 6)		2.069	2.10	2.132	V	T _A = +25°C (Note 1)
			2.048	2.10	2.153	V	T _A = -40°C to +85°C (Note 2)
	MCP13XX-22 (Note 6)		2.167	2.20	2.233	V	T _A = +25°C (Note 1)
			2.145	2.20	2.255	V	T _A = -40°C to +85°C (Note 2)
	MCP13XX-23 (Note 6)		2.266	2.30	2.335	V	T _A = +25°C (Note 1)
			2.243	2.30	2.358	V	T _A = -40°C to +85°C (Note 2)
	MCP13XX-24 (Note 6)		2.364	2.40	2.436	V	T _A = +25°C (Note 1)
			2.340	2.40	2.460	V	T _A = -40°C to +85°C (Note 2)
	MCP13XX-25 (Note 6)		2.463	2.50	2.538	V	T _A = +25°C (Note 1)
			2.438	2.50	2.563	V	T _A = -40°C to +125°C (Note 2)
	MCP13XX-26 (Note 6)		2.561	2.60	2.639	V	T _A = +25°C (Note 1)
			2.535	2.60	2.665	V	T _A = -40°C to +125°C (Note 2)
	MCP13XX-27 (Note 6)		2.660	2.70	2.741	V	T _A = +25°C (Note 1)
			2.633	2.70	2.768	V	T _A = -40°C to +125°C (Note 2)
	MCP13XX-28 (Note 6)		2.758	2.80	2.842	V	T _A = +25°C (Note 1)
			2.730	2.80	2.870	V	T _A = -40°C to +125°C (Note 2)
	MCP13XX-29		2.857	2.90	2.944	V	T _A = +25°C (Note 1)
			2.828	2.90	2.973	V	T _A = -40°C to +125°C (Note 2)
	MCP13XX-30 (Note 6)		2.955	3.00	3.045	V	T _A = +25°C (Note 1)
			2.925	3.00	3.075	V	T _A = -40°C to +125°C (Note 2)
	MCP13XX-31 (Note 6)		3.054	3.10	3.147	V	T _A = +25°C (Note 1)
			3.023	3.10	3.178	V	T _A = -40°C to +125°C (Note 2)
	MCP13XX-32 (Note 6)		3.152	3.20	3.248	V	T _A = +25°C (Note 1)
			3.120	3.20	3.280	V	T _A = -40°C to +125°C (Note 2)
	MCP13XX-33 (Note 6)		3.251	3.30	3.350	V	T _A = +25°C (Note 1)
			3.218	3.30	3.383	V	T _A = -40°C to +125°C (Note 2)

- Note 1:** Trip point is $\pm 1.5\%$ from typical value.
Note 2: Trip point is $\pm 2.5\%$ from typical value.
Note 3: Hysteresis is minimum = 1%, maximum = 6% at +25°C.
Note 4: This specification allows this device to be used in PIC[®] microcontroller applications that require the In-Circuit Serial Programming™ (ICSP™) feature (see device-specific programming specifications for voltage requirements). The total time that the RST pin can be above the maximum device operational voltage (5.5V) is 100s. Current into the RST pin should be limited to 2 mA. It is recommended that the device operational temperature be maintained between 0°C to +70°C (+25°C preferred). For additional information, refer to [Figure 2-35](#).
Note 5: This parameter is established by characterization and is not 100% tested.
Note 6: Custom ordered voltage trip point; minimum order volume requirement. Information available upon request.

DC CHARACTERISTICS (CONTINUED)

Electrical Specifications: Unless otherwise indicated, all limits are specified for $V_{DD} = 1V$ to $5.5V$, $R_{PU} = 100\text{ k}\Omega$ (only **MCP1320**, **MCP1321** and **MCP1322**), $T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$.

Parameters	Sym	Min	Typ	Max	Units	Conditions	
V_{DD} Trip Point (Con't)	MCP13XX-34 (Note 6)	V_{TRIP}	3.349	3.40	3.451	V	$T_A = +25^\circ\text{C}$ (Note 1)
			3.315	3.40	3.385	V	$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$ (Note 2)
	MCP13XX-35 (Note 6)		3.448	3.50	3.553	V	$T_A = +25^\circ\text{C}$ (Note 1)
			3.413	3.50	3.588	V	$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$ (Note 2)
	MCP13XX-36 (Note 6)		3.546	3.60	3.654	V	$T_A = +25^\circ\text{C}$ (Note 1)
			3.510	3.60	3.690	V	$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$ (Note 2)
	MCP13XX-37 (Note 6)		3.645	3.70	3.756	V	$T_A = +25^\circ\text{C}$ (Note 1)
			3.608	3.70	3.793	V	$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$ (Note 2)
	MCP13XX-38 (Note 6)		3.743	3.80	3.857	V	$T_A = +25^\circ\text{C}$ (Note 1)
			3.705	3.80	3.895	V	$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$ (Note 2)
	MCP13XX-39 (Note 6)		3.842	3.90	3.959	V	$T_A = +25^\circ\text{C}$ (Note 1)
			3.803	3.90	3.998	V	$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$ (Note 2)
	MCP13XX-40 (Note 6)		3.940	4.00	4.060	V	$T_A = +25^\circ\text{C}$ (Note 1)
			3.900	4.00	4.100	V	$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$ (Note 2)
	MCP13XX-41 (Note 6)		4.039	4.10	4.162	V	$T_A = +25^\circ\text{C}$ (Note 1)
			3.998	4.10	4.203	V	$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$ (Note 2)
	MCP13XX-42 (Note 6)		4.137	4.20	4.263	V	$T_A = +25^\circ\text{C}$ (Note 1)
			4.095	4.20	4.305	V	$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$ (Note 2)
	MCP13XX-43 (Note 6)		4.236	4.30	4.365	V	$T_A = +25^\circ\text{C}$ (Note 1)
			4.193	4.30	4.408	V	$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$ (Note 2)
	MCP13XX-44 (Note 6)		4.334	4.40	4.466	V	$T_A = +25^\circ\text{C}$ (Note 1)
			4.290	4.40	4.510	V	$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$ (Note 2)
	MCP13XX-45 (Note 6)		4.433	4.50	4.568	V	$T_A = +25^\circ\text{C}$ (Note 1)
			4.388	4.50	4.613	V	$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$ (Note 2)
MCP13XX-46		4.531	4.60	4.669	V	$T_A = +25^\circ\text{C}$ (Note 1)	
		4.485	4.60	4.715	V	$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$ (Note 2)	
MCP13XX-47 (Note 6)		4.630	4.70	4.771	V	$T_A = +25^\circ\text{C}$ (Note 1)	
		4.583	4.70	4.818	V	$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$ (Note 2)	
V_{DD} Trip Point Tempco	T_{TPCO}	—	± 40	—	ppm/ $^\circ\text{C}$		

- Note 1:** Trip point is $\pm 1.5\%$ from typical value.
Note 2: Trip point is $\pm 2.5\%$ from typical value.
Note 3: Hysteresis is minimum = 1%, maximum = 6% at $+25^\circ\text{C}$.
Note 4: This specification allows this device to be used in PIC[®] microcontroller applications that require the In-Circuit Serial Programming™ (ICSP™) feature (see device-specific programming specifications for voltage requirements). The total time that the RST pin can be above the maximum device operational voltage (5.5V) is 100s. Current into the RST pin should be limited to 2 mA. It is recommended that the device operational temperature be maintained between 0°C to $+70^\circ\text{C}$ ($+25^\circ\text{C}$ preferred). For additional information, refer to [Figure 2-35](#).
Note 5: This parameter is established by characterization and is not 100% tested.
Note 6: Custom ordered voltage trip point; minimum order volume requirement. Information available upon request.

MCP131X/2X

DC CHARACTERISTICS (CONTINUED)

Electrical Specifications: Unless otherwise indicated, all limits are specified for $V_{DD} = 1V$ to $5.5V$, $R_{PU} = 100\text{ k}\Omega$ (only **MCP1320**, **MCP1321** and **MCP1322**), $T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$.

Parameters		Sym	Min	Typ	Max	Units	Conditions
Threshold Hysteresis (Note 3)	MCP13XX-20 (Note 6)	V_{HYS}	0.020	—	0.120	V	$T_A = +25^\circ\text{C}$ (Note 3)
			(Note 6)		V	$T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$	
	MCP13XX-21 (Note 6)	V_{HYS}	0.021	—	0.126	V	$T_A = +25^\circ\text{C}$ (Note 3)
			(Note 6)		V	$T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$	
	MCP13XX-22 (Note 6)	V_{HYS}	0.022	—	0.132	V	$T_A = +25^\circ\text{C}$ (Note 3)
			(Note 6)		V	$T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$	
	MCP13XX-23 (Note 6)	V_{HYS}	0.023	—	0.138	V	$T_A = +25^\circ\text{C}$ (Note 3)
			(Note 6)		V	$T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$	
	MCP13XX-24 (Note 6)	V_{HYS}	0.024	—	0.144	V	$T_A = +25^\circ\text{C}$ (Note 3)
			(Note 6)		V	$T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$	
	MCP13XX-25 (Note 6)	V_{HYS}	0.025	—	0.150	V	$T_A = +25^\circ\text{C}$ (Note 3)
			(Note 6)		V	$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$	
	MCP13XX-26 (Note 6)	V_{HYS}	0.026	—	0.156	V	$T_A = +25^\circ\text{C}$ (Note 3)
			(Note 6)		V	$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$	
	MCP13XX-27 (Note 6)	V_{HYS}	0.027	—	0.162	V	$T_A = +25^\circ\text{C}$ (Note 3)
			(Note 6)		V	$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$	
	MCP13XX-28 (Note 6)	V_{HYS}	0.028	—	0.168	V	$T_A = +25^\circ\text{C}$ (Note 3)
			(Note 6)		V	$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$	
	MCP13XX-29	V_{HYS}	0.029	—	0.174	V	$T_A = +25^\circ\text{C}$ (Note 3)
			(Note 6)		V	$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$	
MCP13XX-30 (Note 6)	V_{HYS}	0.030	—	0.180	V	$T_A = +25^\circ\text{C}$ (Note 3)	
		(Note 6)		V	$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$		
MCP13XX-31 (Note 6)	V_{HYS}	0.031	—	0.186	V	$T_A = +25^\circ\text{C}$ (Note 3)	
		(Note 6)		V	$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$		
MCP13XX-32 (Note 6)	V_{HYS}	0.032	—	0.192	V	$T_A = +25^\circ\text{C}$ (Note 3)	
		(Note 6)		V	$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$		
MCP13XX-33 (Note 6)	V_{HYS}	0.033	—	0.198	V	$T_A = +25^\circ\text{C}$ (Note 3)	
		(Note 6)		V	$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$		

- Note 1:** Trip point is $\pm 1.5\%$ from typical value.
Note 2: Trip point is $\pm 2.5\%$ from typical value.
Note 3: Hysteresis is minimum = 1%, maximum = 6% at $+25^\circ\text{C}$.
Note 4: This specification allows this device to be used in PIC[®] microcontroller applications that require the In-Circuit Serial Programming[™] (ICSP[™]) feature (see device-specific programming specifications for voltage requirements). The total time that the RST pin can be above the maximum device operational voltage (5.5V) is 100s. Current into the RST pin should be limited to 2 mA. It is recommended that the device operational temperature be maintained between 0°C to $+70^\circ\text{C}$ ($+25^\circ\text{C}$ preferred). For additional information, refer to [Figure 2-35](#).
Note 5: This parameter is established by characterization and is not 100% tested.
Note 6: Custom ordered voltage trip point; minimum order volume requirement. Information available upon request.

DC CHARACTERISTICS (CONTINUED)

Electrical Specifications: Unless otherwise indicated, all limits are specified for $V_{DD} = 1V$ to $5.5V$, $R_{PU} = 100\text{ k}\Omega$ (only **MCP1320**, **MCP1321** and **MCP1322**), $T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$.

Parameters	Sym	Min	Typ	Max	Units	Conditions
Threshold Hysteresis (Continued) (Note 3)	MCP13XX-34 (Note 6)	0.034	—	0.204	V	$T_A = +25^\circ\text{C}$ (Note 3)
	MCP13XX-35 (Note 6)					(Note 6)
		MCP13XX-36 (Note 6)	0.035	—	0.210	V
	MCP13XX-37 (Note 6)					
		MCP13XX-38 (Note 6)	0.036	—	0.216	V
	MCP13XX-39 (Note 6)					
		MCP13XX-40 (Note 6)	0.037	—	0.222	V
	MCP13XX-41 (Note 6)					
		MCP13XX-42 (Note 6)	0.038	—	0.228	V
	MCP13XX-43 (Note 6)					
		MCP13XX-44 (Note 6)	0.039	—	0.234	V
	MCP13XX-45 (Note 6)					
		MCP13XX-46 (Note 6)	0.040	—	0.240	V
	MCP13XX-47 (Note 6)					
		MCP13XX-48 (Note 6)	0.041	—	0.246	V
	MCP13XX-49 (Note 6)					
		MCP13XX-50 (Note 6)	0.042	—	0.252	V
	MCP13XX-51 (Note 6)					
		MCP13XX-52 (Note 6)	0.043	—	0.258	V
	MCP13XX-53 (Note 6)					
MCP13XX-54 (Note 6)		0.044	—	0.264	V	$T_A = +25^\circ\text{C}$ (Note 3)
	MCP13XX-55 (Note 6)					(Note 6)
MCP13XX-56 (Note 6)		0.045	—	0.270	V	$T_A = +25^\circ\text{C}$ (Note 3)
	MCP13XX-57 (Note 6)					(Note 6)
MCP13XX-58 (Note 6)		0.046	—	0.276	V	$T_A = +25^\circ\text{C}$ (Note 3)
	MCP13XX-59 (Note 6)					(Note 6)
MCP13XX-60 (Note 6)		0.047	—	0.282	V	$T_A = +25^\circ\text{C}$ (Note 3)
	MCP13XX-61 (Note 6)					(Note 6)

- Note 1:** Trip point is $\pm 1.5\%$ from typical value.
Note 2: Trip point is $\pm 2.5\%$ from typical value.
Note 3: Hysteresis is minimum = 1%, maximum = 6% at $+25^\circ\text{C}$.
Note 4: This specification allows this device to be used in PIC[®] microcontroller applications that require the In-Circuit Serial Programming™ (ICSP™) feature (see device-specific programming specifications for voltage requirements). The total time that the $\overline{\text{RST}}$ pin can be above the maximum device operational voltage (5.5V) is 100s. Current into the $\overline{\text{RST}}$ pin should be limited to 2 mA. It is recommended that the device operational temperature be maintained between 0°C to $+70^\circ\text{C}$ ($+25^\circ\text{C}$ preferred). For additional information, refer to [Figure 2-35](#).
Note 5: This parameter is established by characterization and is not 100% tested.
Note 6: Custom ordered voltage trip point; minimum order volume requirement. Information available upon request.

MCP131X/2X

DC CHARACTERISTICS (CONTINUED)

Electrical Specifications: Unless otherwise indicated, all limits are specified for $V_{DD} = 1V$ to $5.5V$, $R_{PU} = 100\text{ k}\Omega$ (only MCP1320 , MCP1321 and MCP1322), $T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$.							
Parameters	Sym	Min	Typ	Max	Units	Conditions	
RST/ $\overline{\text{RST}}$ Low-Level Output Voltage	V_{OL}	—	—	0.3	V	$I_{OL} = 50\ \mu\text{A}$, $1.0V \leq V_{DD} \leq 1.5V$	
		—	—	0.3	V	$I_{OL} = 100\ \mu\text{A}$, $1.5V < V_{DD} \leq 2.5V$	
		—	—	0.3	V	$I_{OL} = 2\ \text{mA}$, $2.5V < V_{DD} \leq 4.5V$	
		—	—	0.3	V	$I_{OL} = 4\ \text{mA}$, $V_{DD} > 4.5V$	
RST/ $\overline{\text{RST}}$ High-Level Output Voltage (Push-Pull Outputs only)	V_{OH}	$V_{DD} - 0.7$	—	—	V	$I_{OH} = 2.5\ \text{mA}$, $V_{DD} \geq 2.5V$	
		$V_{DD} - 0.7$	—	—	V	$I_{OH} = 500\ \mu\text{A}$, $V_{DD} \geq 1.5V$	
Input Low Voltage ($\overline{\text{MR}}$ and WDI pins)	V_{IL}	V_{SS}	—	$0.3V_{DD}$	V		
Input High Voltage ($\overline{\text{MR}}$ and WDI pins)	V_{IH}	$0.7V_{DD}$	—	V_{DD}	V		
Open-Drain High Voltage on Output (Note 4)	V_{ODH}	—	—	$13.5^{(4)}$	V	Open-Drain Output pin only, $V_{DD} = 3.0V$, Time voltage $> 5.5V$ applied $\leq 100\ \text{s}$, current into pin limited to $2\ \text{mA}$, $+25^\circ\text{C}$ operation recommended (Note 4 , Note 5)	
Input Leakage Current ($\overline{\text{MR}}$ and WDI)	I_{IL}	—	—	± 1	μA	$V_{SS} \leq V_{PIN} \leq V_{DD}$	
Open-Drain Output Leakage Current (MCP1316M , MCP1318M , MCP1319M , MCP1320 , MCP1321 , and MCP1322 only)	I_{OD}	—	0.003	1.0	μA		
Pull-up Resistance	$\overline{\text{MR}}$ pin	R_{PU}	—	52	—	$\text{k}\Omega$	$V_{DD} = 5.5V$
	WDI pin		—	52	—	$\text{k}\Omega$	$V_{DD} = 5.5V$
	$\overline{\text{RST}}$ pin		—	4.7	—	$\text{k}\Omega$	$V_{DD} = 5.5V$, MCP131XM devices only
Input Pin Capacitance ($\overline{\text{MR}}$ and WDI)	C_I	—	100	—	pF		
Output Pin Capacitive Loading (RST and $\overline{\text{RST}}$)	C_O	—	—	50	pF	This is the tester loading to meet the AC timing specifications.	

- Note 1:** Trip point is $\pm 1.5\%$ from typical value.
- Note 2:** Trip point is $\pm 2.5\%$ from typical value.
- Note 3:** Hysteresis is minimum = 1% , maximum = 6% at $+25^\circ\text{C}$.
- Note 4:** This specification allows this device to be used in PIC[®] microcontroller applications that require the In-Circuit Serial Programming™ (ICSP™) feature (see device-specific programming specifications for voltage requirements). The total time that the $\overline{\text{RST}}$ pin can be above the maximum device operational voltage ($5.5V$) is 100s . Current into the $\overline{\text{RST}}$ pin should be limited to $2\ \text{mA}$. It is recommended that the device operational temperature be maintained between 0°C to $+70^\circ\text{C}$ ($+25^\circ\text{C}$ preferred). For additional information, refer to [Figure 2-35](#).
- Note 5:** This parameter is established by characterization and is not 100% tested.
- Note 6:** Custom ordered voltage trip point; minimum order volume requirement. Information available upon request.

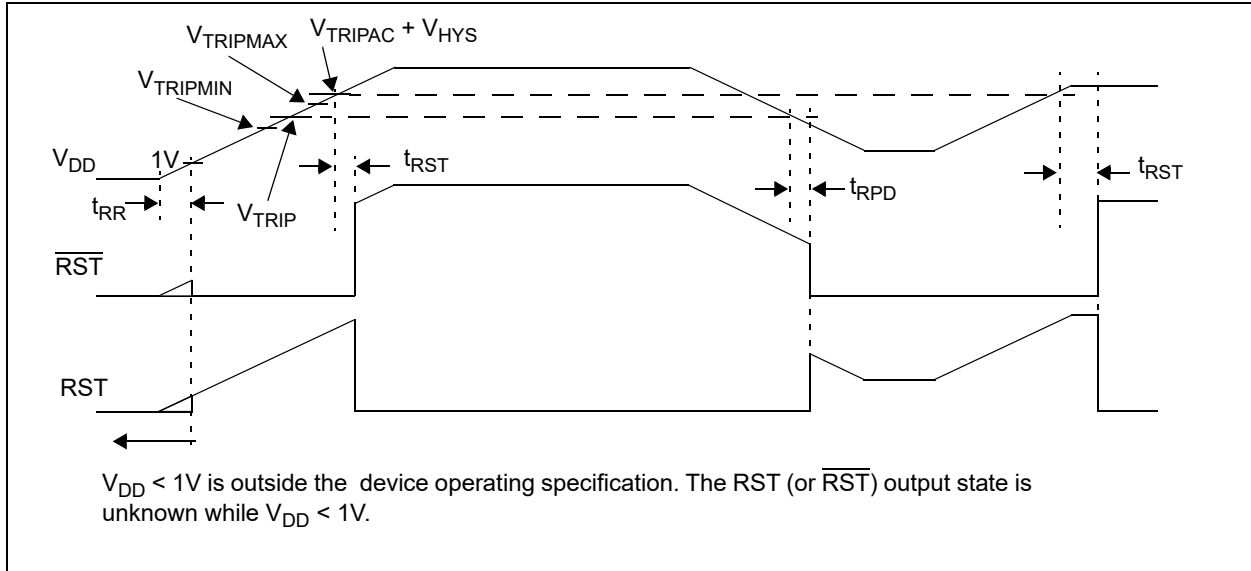


FIGURE 1-1: Device Voltage and Reset Pin Waveforms.

TABLE 1-1: DEVICE VOLTAGE AND RESET PIN TIMINGS

Electrical Specifications: Unless otherwise indicated, all limits are specified for $V_{DD} = 1V$ to $5.5V$, $R_{PU} = 100\text{ k}\Omega$ (only MCP1320, MCP1321, and MCP1322), $T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$.

Parameters	Sym	Min	Typ	Max	Units	Conditions
Falling V_{DD} Trip Point Detected to RST or \overline{RST} Active	t_{RPD}	—	650	—	μs	V_{DD} ramped from $V_{TRIPMAX} + 250\text{ mV}$ down to $V_{TRIPMIN} - 200\text{ mV}$, V_{DD} falling @ $5\text{ mV}/\mu\text{s}$, $C_L = 50\text{ pF}$ (Note 1)
V_{DD} Rise Rate	t_{RR}	Note 3				
Reset active time (MR Rising Edge, POR/BOR Inactive, or WDT time out) to RST/ \overline{RST} Inactive	t_{RST}	1.0	1.4	2.0	ms	Note 2
		20	30	40	ms	Note 2
		140	200	280	ms	Standard Time Out
		1120	1600	2240	ms	Note 2
RST Rise Time after RST Active (Push-Pull Outputs only)	t_{RT}	—	5	—	μs	For RST 10% to 90% of V_{DD} , $C_L = 50\text{ pF}$ (Note 1)
RST Rise Time after \overline{RST} Inactive (Push-Pull Outputs only)		—	5	—	μs	For \overline{RST} 10% to 90% of V_{DD} , $C_L = 50\text{ pF}$ (Note 1)
RST Fall Time after RST Inactive	t_{FT}	—	5	—	μs	For RST 90% to 10% of V_{DD} , $C_L = 50\text{ pF}$ (Note 1)
RST Fall Time after \overline{RST} Active		—	5	—	μs	For \overline{RST} 90% to 10% of V_{DD} , $C_L = 50\text{ pF}$ (Note 1)

Note 1: These parameters are for design guidance only and are not 100% tested.

2: Custom ordered Reset active time; minimum order volume requirement.

3: Designed to be independent of V_{DD} rise rate. Device characterization was done with a rise rate as slow as 0.1 V/s (@ $+25^\circ\text{C}$).

MCP131X/2X

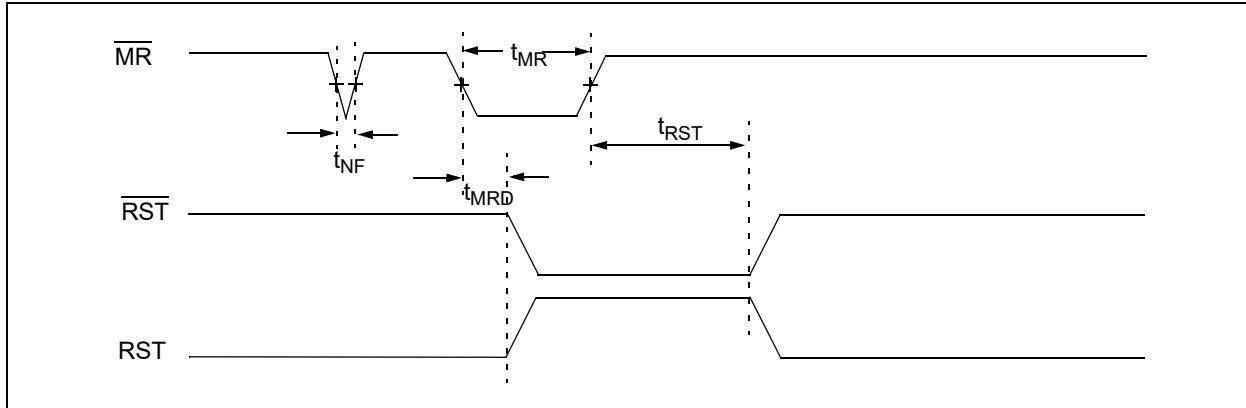


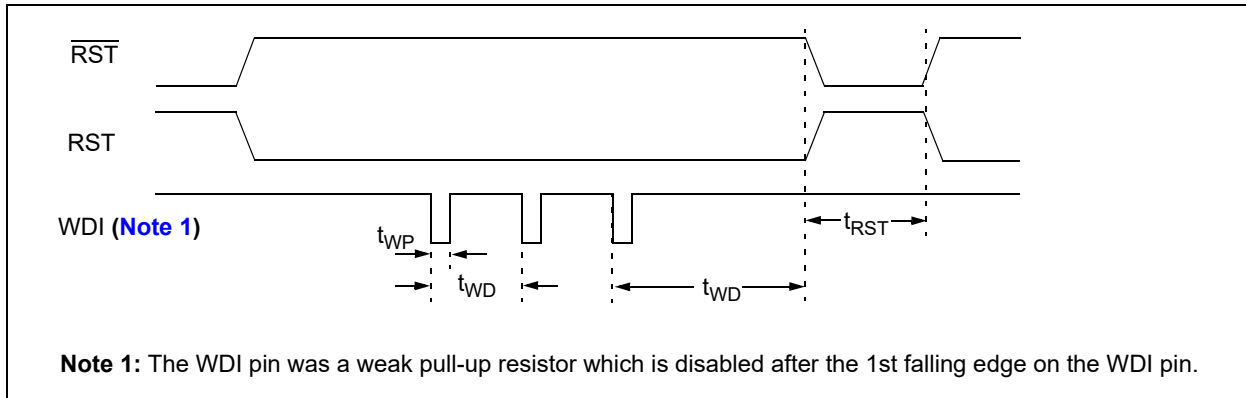
FIGURE 1-2: $\overline{\text{MR}}$ and Reset Pin Waveforms.

TABLE 1-2: $\overline{\text{MR}}$ AND RESET PIN TIMINGS

Electrical Specifications: Unless otherwise indicated, all limits are specified for $V_{DD} = 1\text{V to }5.5\text{V}$, $R_{PU} = 100\text{ k}\Omega$ (only **MCP1320**, **MCP1321**, and **MCP1322**), $T_A = -40^\circ\text{C to }+125^\circ\text{C}$.

Parameters	Sym	Min	Typ	Max	Units	Conditions
MR Pulse Width	t_{MR}	1	—	—	μs	
$\overline{\text{MR}}$ Active to RST/ $\overline{\text{RST}}$ Active	t_{MRD}	—	235	—	ns	$V_{DD} = 5.0\text{V}$
$\overline{\text{MR}}$ Input Noise filter	t_{NF}	—	150	—	ns	$V_{DD} = 5.0\text{V}$

Note 1: These parameters are for design guidance only and are not 100% tested.



Note 1: The WDI pin was a weak pull-up resistor which is disabled after the 1st falling edge on the WDI pin.

FIGURE 1-3: WDI and Reset Pin Waveforms.

TABLE 1-3: WDI AND RESET PIN TIMINGS

Electrical Specifications: Unless otherwise indicated, all limits are specified for $V_{DD} = 1\text{V to }5.5\text{V}$, $R_{PU} = 100\text{ k}\Omega$ (only **MCP1320**, **MCP1321**, and **MCP1322**), $T_A = -40^\circ\text{C to }+125^\circ\text{C}$.

Parameters	Sym	Min	Typ	Max	Units	Conditions
WDI Pulse Width	t_{WP}	50	—	—	ns	
Watchdog Time-Out Period	t_{WD}	4.3	6.3	9.3	ms	Note 1
		71	102	153	ms	Note 1
		1.12	1.6	2.4	sec	Standard Time Out
		17.9	25.6	38.4	sec	Note 1

Note 1: Custom ordered WatchDog Timer time out; minimum order volume requirement.

TEMPERATURE CHARACTERISTICS

Electrical Specifications: Unless otherwise noted, all limits are specified for $V_{DD} = 1V$ to $5.5V$, $R_{PU} = 100\text{ k}\Omega$ (only **MCP1316**), $T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$.

Parameters	Sym	Min	Typ	Max	Units	Conditions
Temperature Ranges						
Specified Temperature Range	T_A	-40	—	+85	$^\circ\text{C}$	MCP13XX-25 (or below)
Specified Temperature Range	T_A	-40	—	+125	$^\circ\text{C}$	Except MCP13XX-25 (or below)
Maximum Junction Temperature	T_J	—	—	+150	$^\circ\text{C}$	
Storage Temperature Range	T_A	-65	—	+150	$^\circ\text{C}$	
Package Thermal Resistances						
Thermal Resistance, 5L-SOT-23	θ_{JA}	—	220.7	—	$^\circ\text{C}/\text{W}$	

MCP131X/2X

NOTES:

2.0 TYPICAL PERFORMANCE CURVES

Note: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only. The performance characteristics listed herein are not tested or guaranteed. In some graphs or tables, the data presented may be outside the specified operating range (e.g., outside specified power supply range) and therefore outside the warranted range.

Note: Unless otherwise indicated, all limits are specified for $V_{DD} = 1V$ to $5.5V$, $R_{PU} = 100\text{ k}\Omega$ (only **MCP1316**; see [Figure 4-1](#)), $T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$.

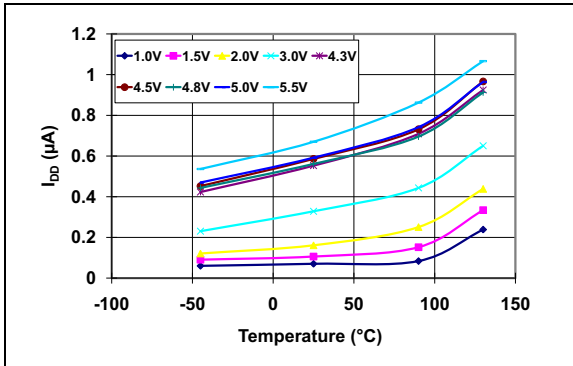


FIGURE 2-1: I_{DD} vs. Temperature (Reset Power-up Timer Inactive and Watchdog Timer Inactive) (MCP1318M-4.6).

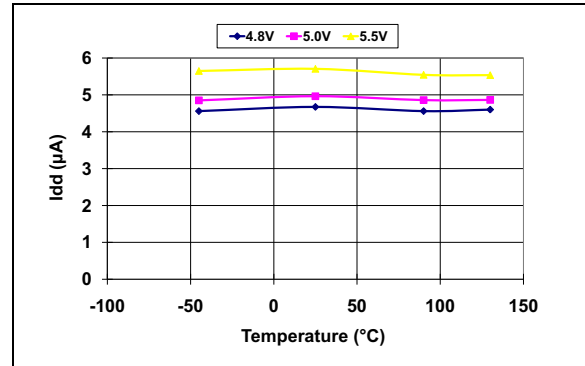


FIGURE 2-4: I_{DD} vs. Temperature (Reset Power-up Timer Active) (MCP1318M-4.6).

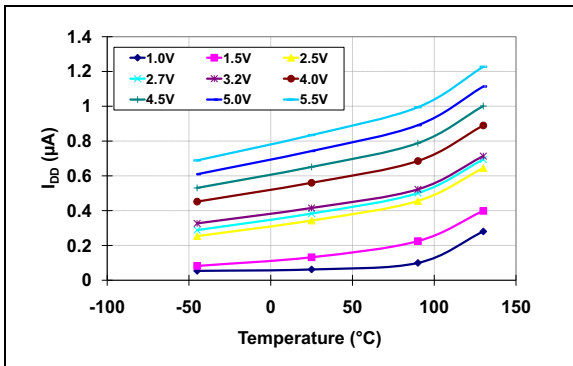


FIGURE 2-2: I_{DD} vs. Temperature (Reset Power-up Timer Inactive and Watchdog Timer Inactive) (MCP1319-2.9).

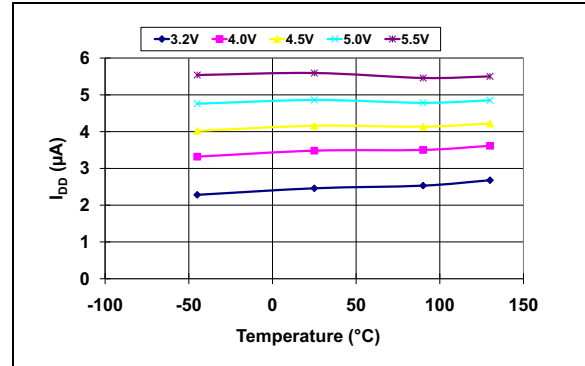


FIGURE 2-5: I_{DD} vs. Temperature (Reset Power-up Timer Active) (MCP1319-2.9).

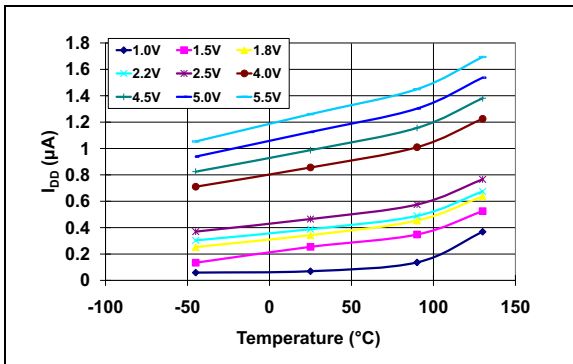


FIGURE 2-3: I_{DD} vs. Temperature (Reset Power-up Timer Inactive and Watchdog Timer Inactive) (MCP1316-2.0).

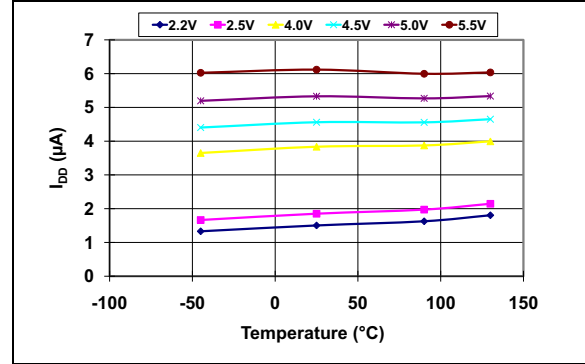


FIGURE 2-6: I_{DD} vs. Temperature (Reset Power-up Timer Active) (MCP1316-2.0).

MCP131X/2X

Note: Unless otherwise indicated, all limits are specified for $V_{DD} = 1V$ to $5.5V$, $R_{PU} = 100\text{ k}\Omega$ (only **MCP1316**; see **Figure 4-1**), $T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$.

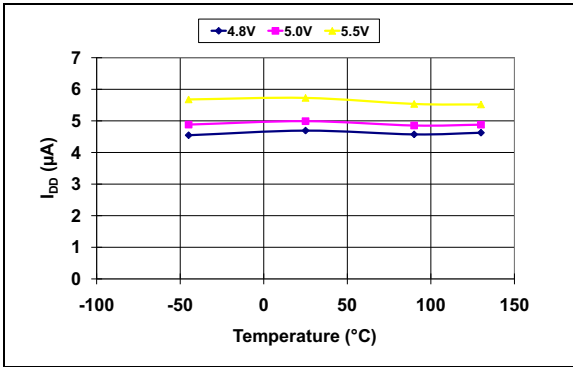


FIGURE 2-7: I_{DD} vs. Temperature (Watchdog Timer Active) (**MCP1318M-4.6**).

MCP1319 does not have a Watchdog Timer

FIGURE 2-8: I_{DD} vs. Temperature (Watchdog Timer Active) (**MCP1319-2.9**).

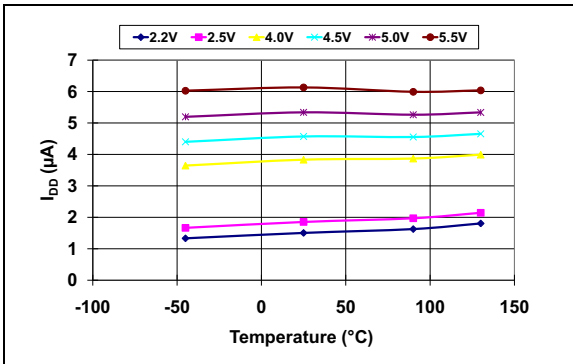


FIGURE 2-9: I_{DD} vs. Temperature (Watchdog Timer Active) (**MCP1316-2.0**).

Note: Unless otherwise indicated, all limits are specified for $V_{DD} = 1V$ to $5.5V$, $R_{PU} = 100\text{ k}\Omega$ (only **MCP1316**; see [Figure 4-1](#)), $T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$.

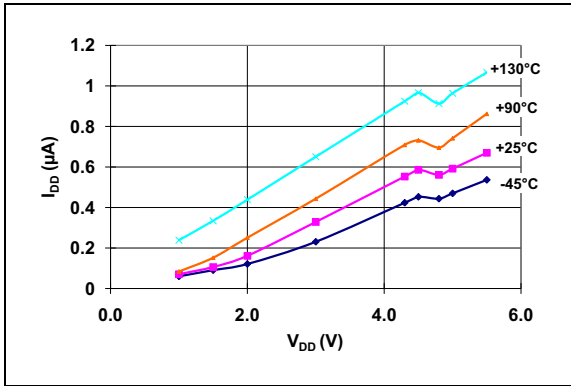


FIGURE 2-10: I_{DD} vs. V_{DD} (Reset Power-up Timer Inactive and Watchdog Timer Inactive) (MCP1318M-4.6).

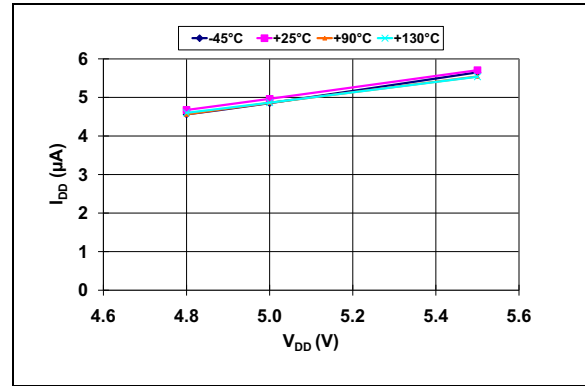


FIGURE 2-13: I_{DD} vs. V_{DD} (Reset Power-up Timer Active or Watchdog Timer Active) (MCP1318M-4.6).

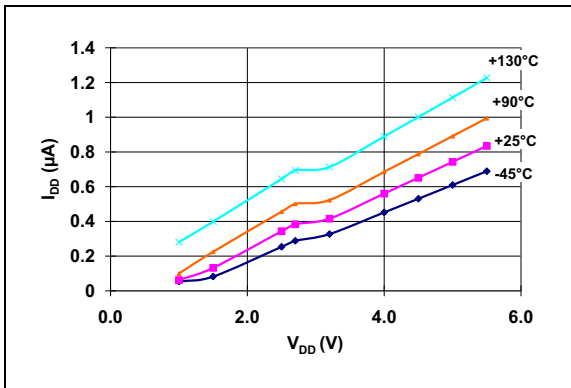


FIGURE 2-11: I_{DD} vs. V_{DD} (Reset Power-up Timer Inactive and Watchdog Timer Inactive) (MCP1319-2.9).

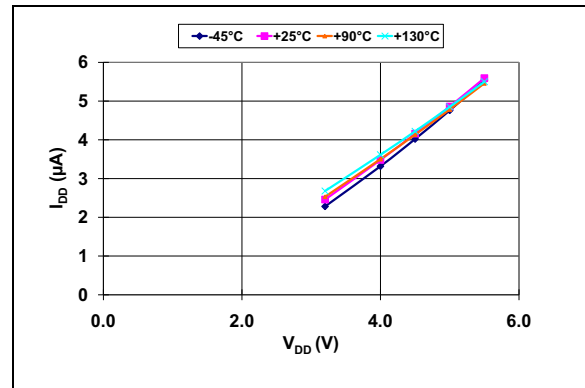


FIGURE 2-14: I_{DD} vs. V_{DD} (Reset Power-up Timer Active or Watchdog Timer Active) (MCP1319-2.9).

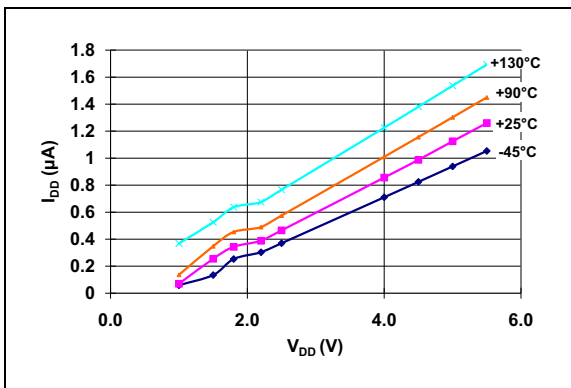


FIGURE 2-12: I_{DD} vs. V_{DD} (Reset Power-up Timer Inactive and Watchdog Timer Inactive) (MCP1316-2.0).

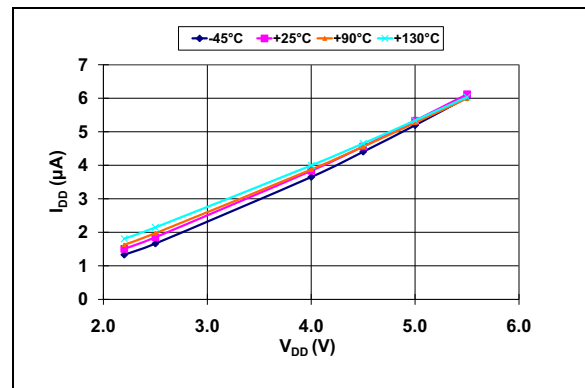


FIGURE 2-15: I_{DD} vs. V_{DD} (Reset Power-up Timer Active or Watchdog Timer Active) (MCP1316-2.0).

MCP131X/2X

Note: Unless otherwise indicated, all limits are specified for $V_{DD} = 1V$ to $5.5V$, $R_{PU} = 100\text{ k}\Omega$ (only MCP1316; see Figure 4-1), $T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$.

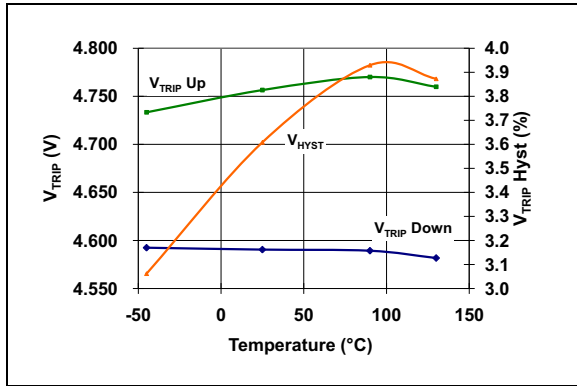


FIGURE 2-16: V_{TRIP} and V_{HYST} vs. Temperature (MCP1318M-4.6).

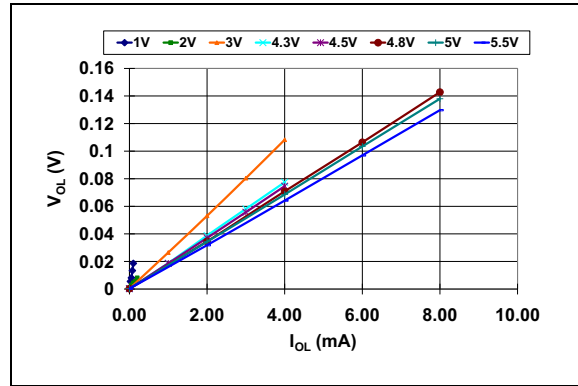


FIGURE 2-19: V_{OL} vs. I_{OL} (MCP1318M-4.6).

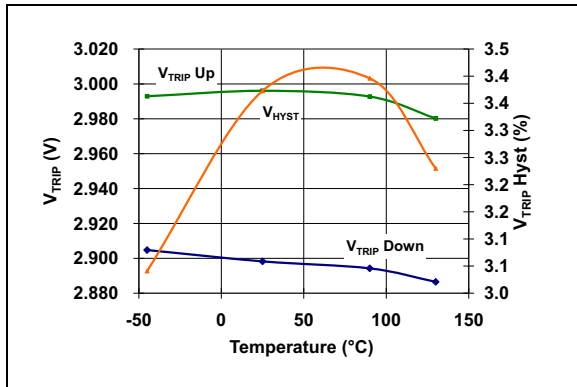


FIGURE 2-17: V_{TRIP} and V_{HYST} vs. Temperature (MCP1319-2.9).

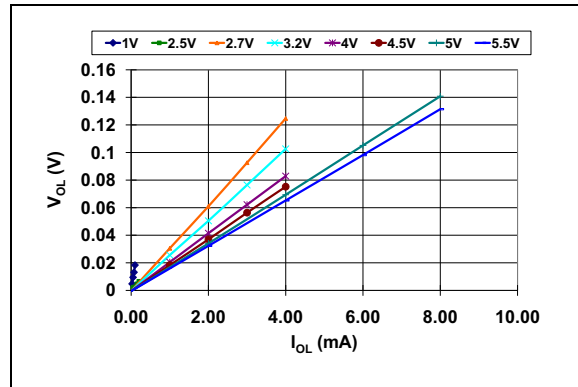


FIGURE 2-20: V_{OL} vs. I_{OL} (MCP1319-2.9).

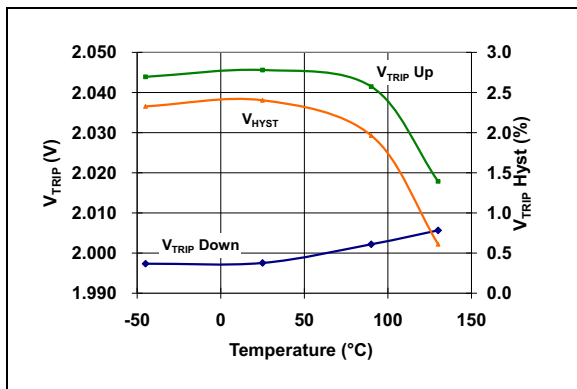


FIGURE 2-18: V_{TRIP} and V_{HYST} vs. Temperature (MCP1316-2.0).

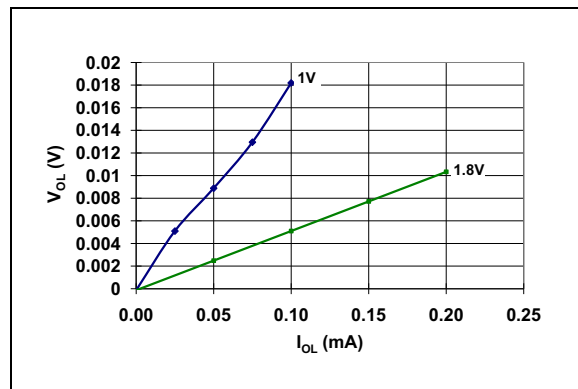


FIGURE 2-21: V_{OL} vs. I_{OL} (MCP1316-2.0).

Note: Unless otherwise indicated, all limits are specified for $V_{DD} = 1V$ to $5.5V$, $R_{PU} = 100\text{ k}\Omega$ (only MCP1316; see Figure 4-1), $T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$.

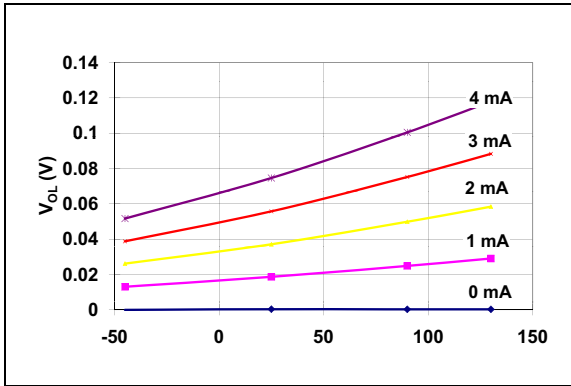


FIGURE 2-22: V_{OL} vs. Temperature (MCP1318M-4.6 @ $V_{DD} = 4.5V$).

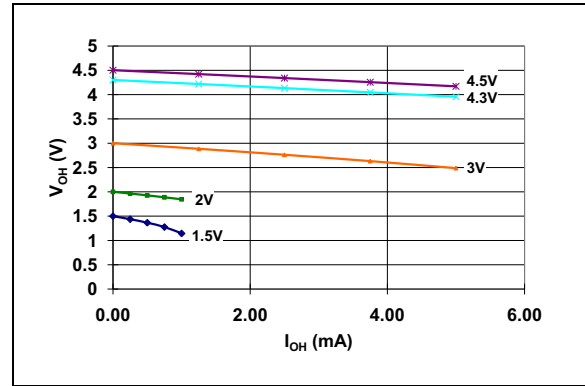


FIGURE 2-25: V_{OH} vs. I_{OH} (MCP1318M-4.6 @ $+25^\circ\text{C}$).

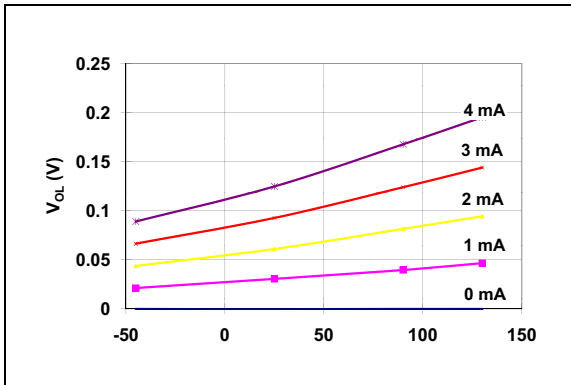


FIGURE 2-23: V_{OL} vs. Temperature (MCP1319-2.9 @ $V_{DD} = 2.7V$).

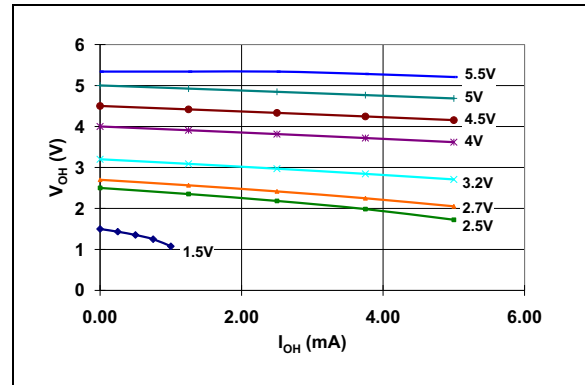


FIGURE 2-26: V_{OH} vs. I_{OH} (MCP1319-2.9 @ $+25^\circ\text{C}$).

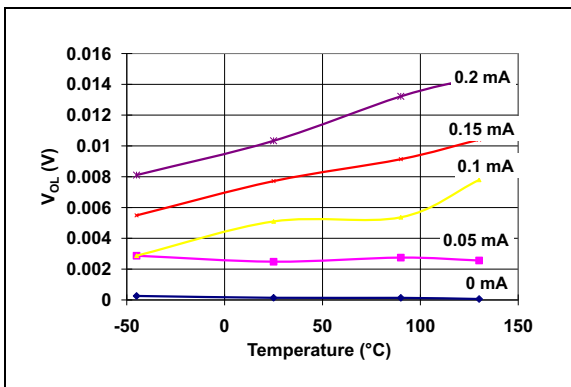


FIGURE 2-24: V_{OL} vs. Temperature (MCP1316-2.0 @ $V_{DD} = 1.8V$).

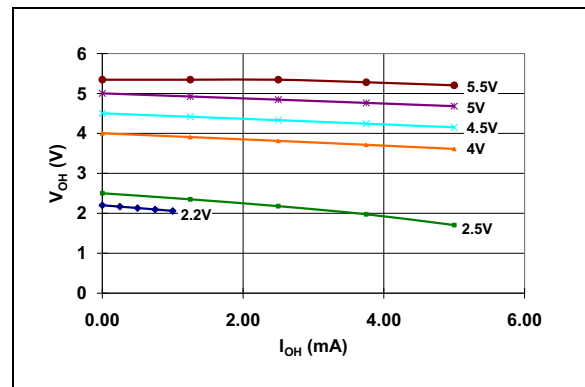


FIGURE 2-27: V_{OH} vs. I_{OH} (MCP1316-2.0 @ $+25^\circ\text{C}$).

MCP131X/2X

Note: Unless otherwise indicated, all limits are specified for $V_{DD} = 1V$ to $5.5V$, $R_{PU} = 100\text{ k}\Omega$ (only MCP1316; see Figure 4-1), $T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$.

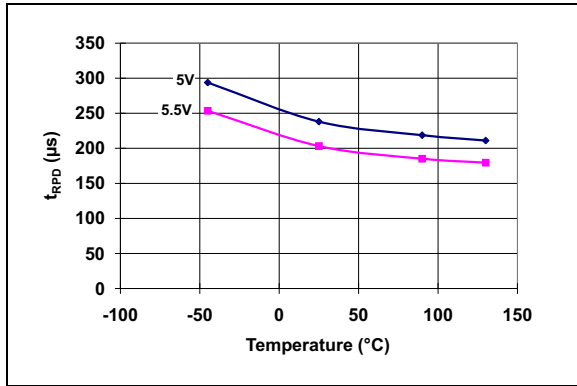


FIGURE 2-28: t_{RPD} vs. Temperature (MCP1318M-4.6).

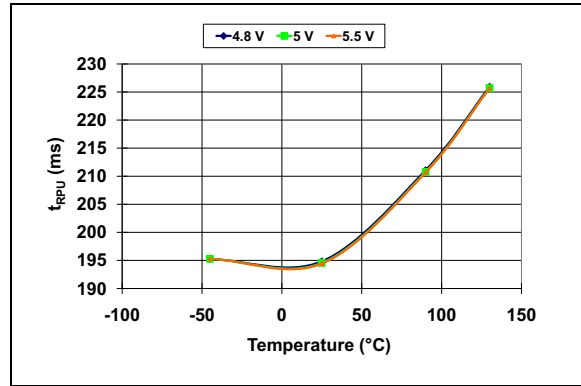


FIGURE 2-31: t_{RPU} vs. Temperature (MCP1318M-4.6).

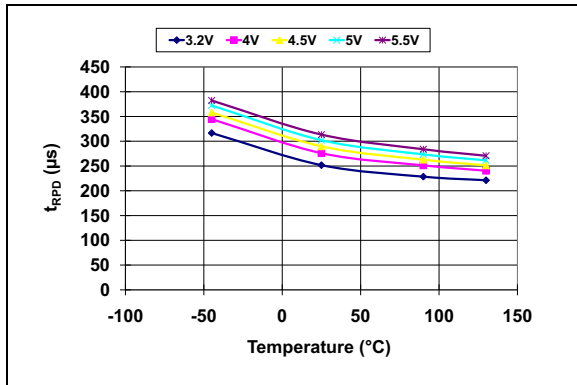


FIGURE 2-29: t_{RPD} vs. Temperature (MCP1319-2.9).

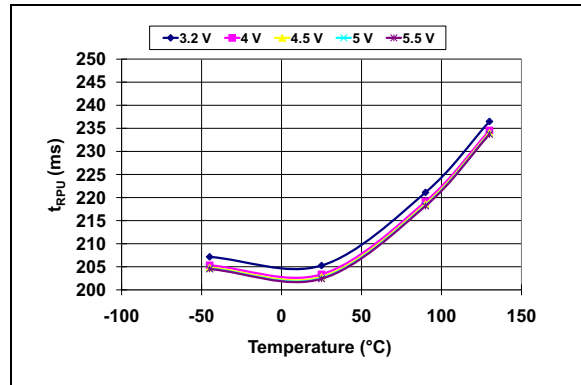


FIGURE 2-32: t_{RPU} vs. Temperature (MCP1319-2.9).

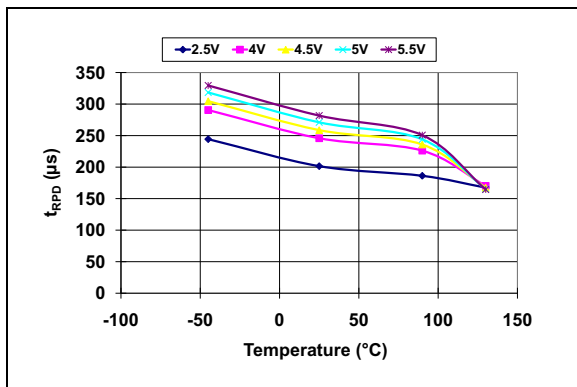


FIGURE 2-30: t_{RPD} vs. Temperature (MCP1316-2.0).

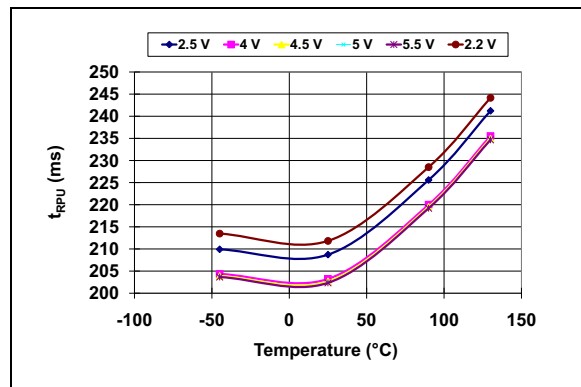


FIGURE 2-33: t_{RPU} vs. Temperature (MCP1316-2.0).

Note: Unless otherwise indicated, all limits are specified for $V_{DD} = 1V$ to $5.5V$, $R_{PU} = 100\text{ k}\Omega$ (only MCP1316; see [Figure 4-1](#)), $T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$.

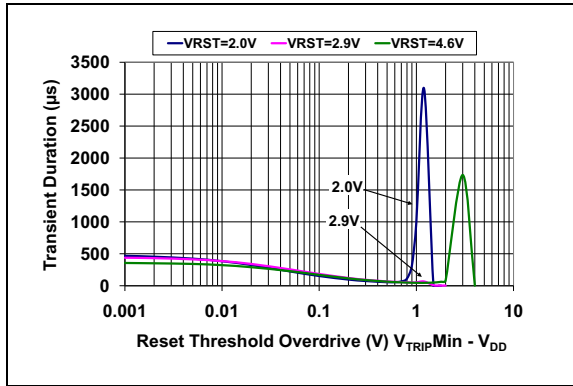


FIGURE 2-34: Transient Duration vs. $V_{TRIP}(\min) - V_{DD}$.

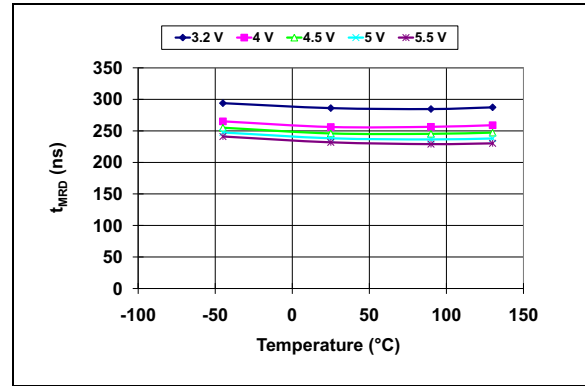


FIGURE 2-37: \overline{MR} Low to Reset Propagation Delay (MCP1319-2.9).

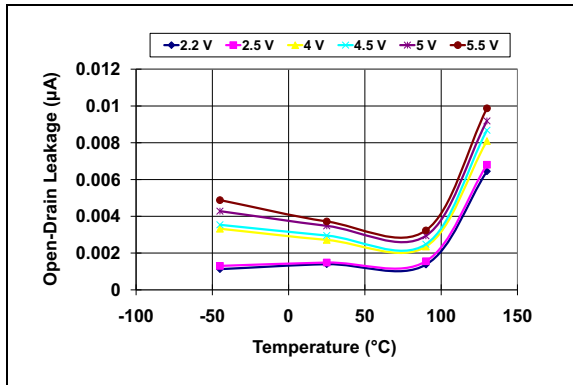


FIGURE 2-35: Open-Drain Leakage Current vs. Temperature (MCP1320-2.0).

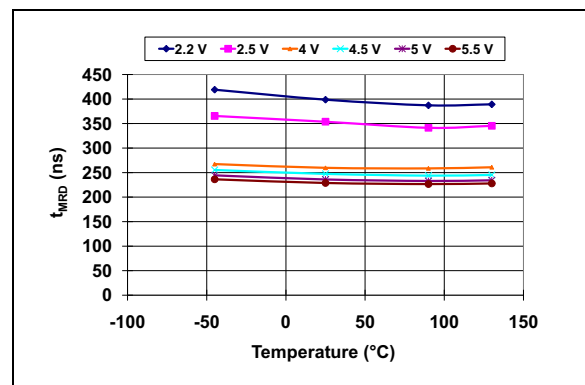


FIGURE 2-38: \overline{MR} Low to Reset Propagation Delay (MCP1316-2.0).

MCP1318M does not have an \overline{MR} pin

FIGURE 2-36: \overline{MR} Low to Reset Propagation Delay (MCP1318M-4.6).

MCP131X/2X

Note: Unless otherwise indicated, all limits are specified for $V_{DD} = 1V$ to $5.5V$, $R_{PU} = 100\text{ k}\Omega$ (only MCP1316; see Figure 4-1), $T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$.

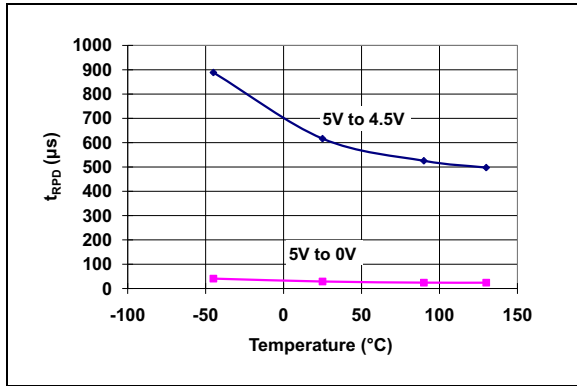


FIGURE 2-39: V_{DD} Falling to Reset Propagation Delay vs. Temperature (MCP1318M-4.6).

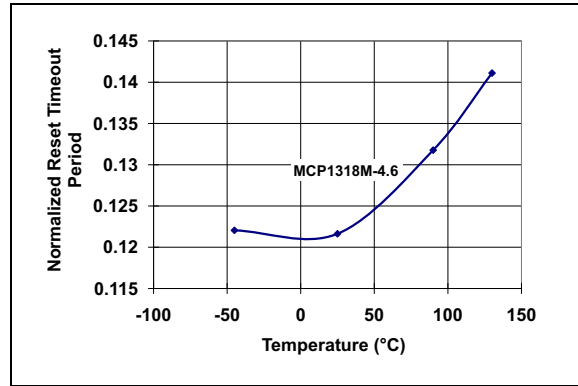


FIGURE 2-42: Normalized Reset Time-Out Period vs. Temperature (MCP1318M-4.6).

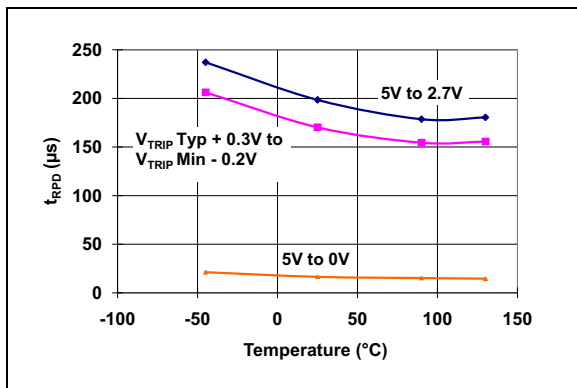


FIGURE 2-40: V_{DD} Falling to Reset Propagation Delay vs. Temperature (MCP1319-2.9).

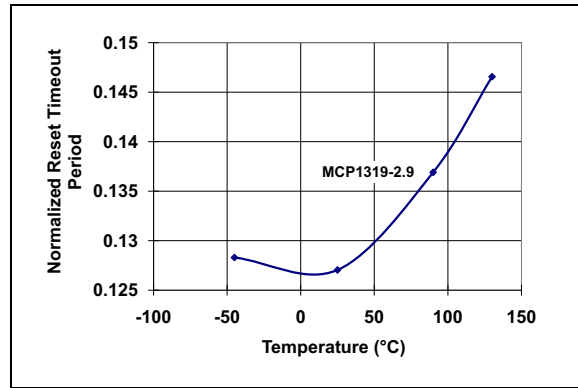


FIGURE 2-43: Normalized Reset Time-Out Period vs. Temperature (MCP1319-2.9).

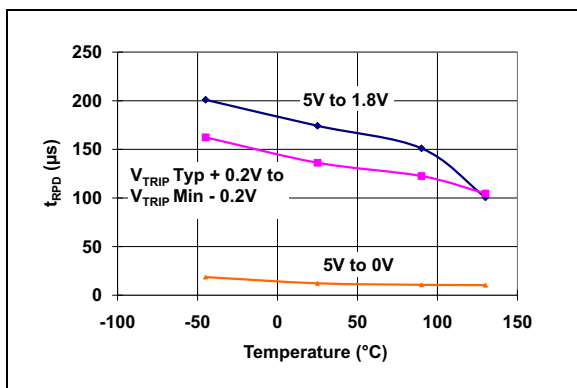


FIGURE 2-41: V_{DD} Falling to Reset Propagation Delay vs. Temperature (MCP1316-2.0).

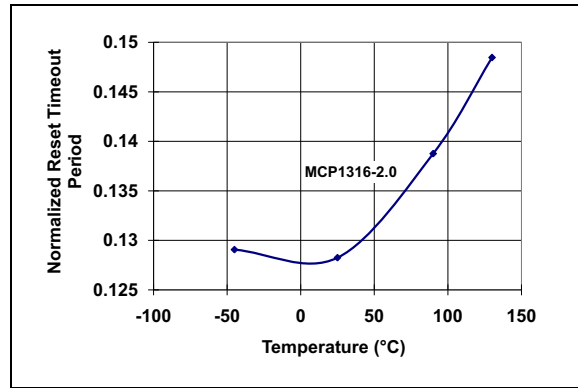


FIGURE 2-44: Normalized Reset Time-Out Period vs. Temperature (MCP1316-2.0).

Note: Unless otherwise indicated, all limits are specified for $V_{DD} = 1V$ to $5.5V$, $R_{PU} = 100\text{ k}\Omega$ (only MCP1316; see Figure 4-1), $T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$.

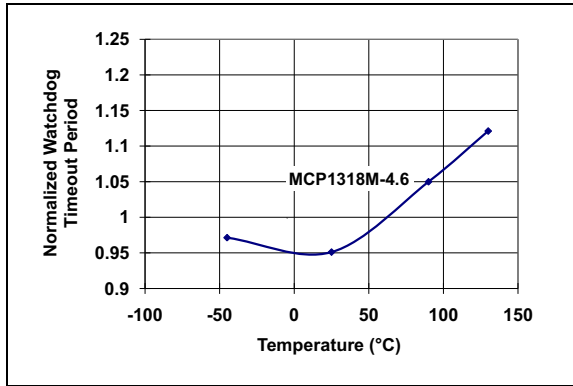


FIGURE 2-45: Normalized Watchdog Time-Out Period vs. Temperature (MCP1318M-4.6).

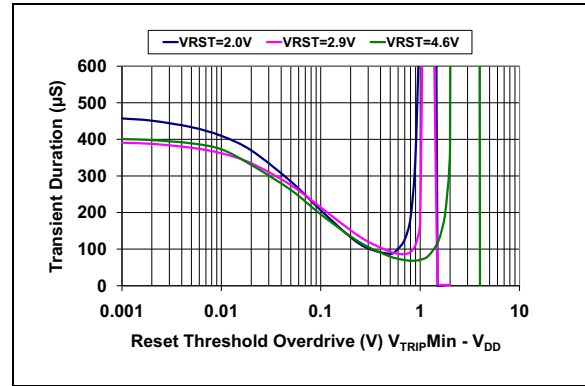


FIGURE 2-48: Max V_{DD} Transient Duration vs. Reset Threshold Overdrive.

**MCP1319 does not
have a Watchdog Timer**

FIGURE 2-46: Normalized Watchdog Time-Out Period vs. Temperature (MCP1319-2.9).

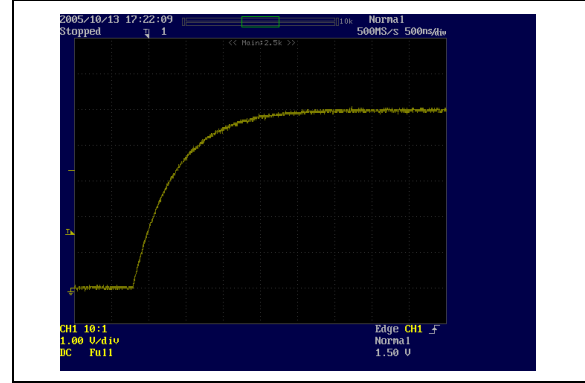


FIGURE 2-49: “M” Part Number Pull-up Characteristics (MCP1318M-4.6).

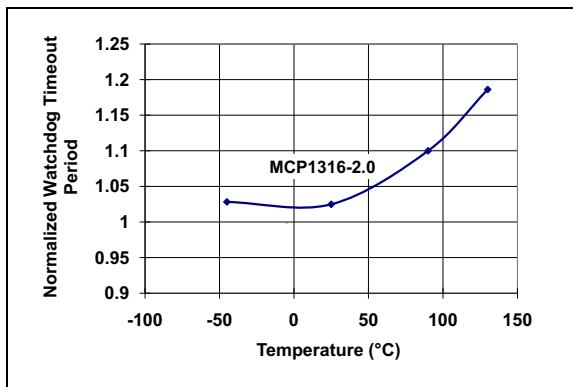


FIGURE 2-47: Normalized Watchdog Time-Out Period vs. Temperature (MCP1316-2.0).

MCP131X/2X

NOTES:

3.0 PIN DESCRIPTION

The descriptions of the pins are listed in [Table 3-1](#).

TABLE 3-1: PIN FUNCTION TABLE

Pin No.	Device	Symbol	Pin Type	Buffer/Driver Type	Function
1	MCP1316M ⁽¹⁾ , MCP1318M ⁽¹⁾ , MCP1319M ⁽¹⁾ , MCP1320, MCP1321, MCP1322	$\overline{\text{RST}}$	O	Open-Drain	<p>Reset Output (active-low)</p> <p>Goes active (Low) if one of these conditions occurs:</p> <ol style="list-style-type: none"> 1. If V_{DD} falls below the selected Reset voltage threshold. 2. If the $\overline{\text{MR}}$ pin is forced low. 3. If the WDI pin does not detect an edge transition within the minimum selected time-out period. 4. During power-up. <p>V_{DD} Falling: Open-Drain = $V_{DD} > V_{TRIP}$ L = $V_{DD} < V_{TRIP}$</p> <p>V_{DD} Rising: Open-Drain = $V_{DD} > V_{TRIP} + V_{HYS}$ L = $V_{DD} < V_{TRIP} + V_{HYS}$</p>
	MCP1316, MCP1318, MCP1319		O	Push-Pull	<p>V_{DD} Falling: H = $V_{DD} > V_{TRIP}$ L = $V_{DD} < V_{TRIP}$</p> <p>V_{DD} Rising: H = $V_{DD} > V_{TRIP} + V_{HYS}$ L = $V_{DD} < V_{TRIP} + V_{HYS}$</p>
	MCP1317	RST	O	Push-Pull	<p>Reset Output (active-high)</p> <p>Goes active (High) if one of these conditions occurs:</p> <ol style="list-style-type: none"> 1. If V_{DD} falls below the selected Reset voltage threshold. 2. If the $\overline{\text{MR}}$ pin is forced low. 3. If the WDI pin does not detect an edge transition within the minimum selected time-out period. 4. During power-up. <p>V_{DD} Falling: H = $V_{DD} < V_{TRIP}$ L = $V_{DD} > V_{TRIP}$</p> <p>V_{DD} Rising: H = $V_{DD} < V_{TRIP} + V_{HYS}$ L = $V_{DD} > V_{TRIP} + V_{HYS}$</p>
2	All	V_{SS}	—	P	The ground reference for the device.

Note 1: Open-Drain output with internal pull-up resistor.

MCP131X/2X

TABLE 3-1: PIN FUNCTION TABLE (CONTINUED)

Pin No.	Device	Symbol	Pin Type	Buffer/Driver Type	Function
SOT23-5					
3	MCP1316, MCP1316M, MCP1317, MCP1320	$\overline{\text{MR}}$	I	ST	Manual Reset input for a Reset switch. This input allows a push button switch to be directly connected to the MCP131X/2X $\overline{\text{MR}}$ pin, which can then be used to force a system Reset. This input filters (ignores) noise pulses that occur on the $\overline{\text{MR}}$ pin. L = Switch is depressed (shorted to ground). This forces the RST/RST pins Active. H = Switch is open (internal pull-up resistor pulls signal high). State of the RST/RST pins determined by other system conditions.
	MCP1318, MCP1318M, MCP1319, MCP1319M, MCP1321, MCP1322	RST	O	Push-Pull	Reset Output (active-high) Goes active (High) if one of these conditions occurs: 1. If V_{DD} falls below the selected Reset voltage threshold. 2. If the $\overline{\text{MR}}$ pin is forced low. 3. If the WDI pin does not detect an edge transition within the minimum selected time-out period. 4. During power-up. V_{DD} Falling: H = $V_{\text{DD}} < V_{\text{TRIP}}$ L = $V_{\text{DD}} > V_{\text{TRIP}}$ V_{DD} Rising: H = $V_{\text{DD}} < V_{\text{TRIP}} + V_{\text{HYS}}$ L = $V_{\text{DD}} > V_{\text{TRIP}} + V_{\text{HYS}}$
4	MCP1316, MCP1316M, MCP1317, MCP1318, MCP1318M, MCP1320, MCP1321	WDI	I	ST	Watchdog Timer Input The WDT period is specified at the time of device order. The Standard WDT period is 1.6s typical. An edge transition on the WDI pin resets the Watchdog Timer counter (no time out). A Falling Edge is required to start the WDT Timer.
	MCP1319, MCP1319M, MCP1322	$\overline{\text{MR}}$	I	ST	Manual Reset input for a Reset switch. This input allows a push button switch to be directly connected to the MCP131X/2X $\overline{\text{MR}}$ pin, which can then be used to force a system Reset. This input filters (ignores) noise pulses that occur on the $\overline{\text{MR}}$ pin. L = Switch is depressed (shorted to ground). This forces the RST/RST pins Active. H = Switch is open (internal pull-up resistor pulls signal high). State of the RST/RST pins determined by other system conditions.
5	All	V_{DD}	—	P	The positive supply for the device.

Note 1: Open-Drain output with internal pull-up resistor.

3.1 Ground Terminal (V_{SS})

V_{SS} provides the negative reference for the analog input voltage. Typically, the circuit ground is used.

3.2 Supply Voltage (V_{DD})

V_{DD} can be used for power supply monitoring or a voltage level that requires monitoring.

3.3 Reset Output (RST and $\overline{\text{RST}}$)

There are four types of Reset output pins. These are:

1. Open-Drain active-low Reset, External pull-up resistor required
2. Open-Drain active-low Reset, Internal pull-up resistor
3. Push-Pull active-low Reset
4. Push-Pull active-high Reset

Some devices have both an active-low and active-high Reset output.

3.3.1 ACTIVE-LOW ($\overline{\text{RST}}$) – OPEN-DRAIN, EXTERNAL PULL-UP RESISTOR

The $\overline{\text{RST}}$ open-drain output remains low while V_{DD} is below the Reset voltage threshold (V_{TRIP}). Once the device voltage (V_{DD}) returns to a high level ($V_{TRIP} + V_{HYS}$), the device will remain in Reset for the Reset delay timer (T_{RST}). After that time expires, the $\overline{\text{RST}}$ pin will float, and an external pull-up resistor is required to bring the output to the high state.

3.3.2 ACTIVE-LOW ($\overline{\text{RST}}$) – OPEN-DRAIN, INTERNAL PULL-UP RESISTOR

The $\overline{\text{RST}}$ open-drain output remains low while V_{DD} is below the Reset voltage threshold (V_{TRIP}). Once the device voltage (V_{DD}) returns to a high level ($V_{TRIP} + V_{HYS}$), the device will remain in Reset for the Reset delay timer (T_{RST}). After that time expires, the $\overline{\text{RST}}$ pin will be pulled high by an internal pull-up resistor (typically 4.7 k Ω).

3.3.3 ACTIVE-LOW ($\overline{\text{RST}}$) – PUSH-PULL

The $\overline{\text{RST}}$ push-pull output remains low while V_{DD} is below the Reset voltage threshold (V_{TRIP}). Once the device voltage (V_{DD}) returns to a high level ($V_{TRIP} + V_{HYS}$), the device will remain in Reset for the Reset delay timer (T_{RST}). After that time expires, the $\overline{\text{RST}}$ pin will be driven to the high state.

3.3.4 ACTIVE-HIGH (RST) – PUSH-PULL

The RST push-pull output remains high while V_{DD} is below the Reset voltage threshold (V_{TRIP}). Once the device voltage (V_{DD}) returns to a high level ($V_{TRIP} + V_{HYS}$), the device will remain in Reset for the Reset delay timer (T_{RST}). After that time expires, the RST pin will be driven to the low state.

3.4 Manual Reset Input ($\overline{\text{MR}}$)

The Manual Reset ($\overline{\text{MR}}$) input pin allows a push button switch to easily be connected to the system. When the push button is depressed, it forces a system Reset. This pin has circuitry that filters noise that may be present on the MR signal.

The $\overline{\text{MR}}$ pin is active-low and has an internal pull-up resistor.

3.5 Watchdog Input

In some systems, it is desirable to have an external Watchdog Timer to monitor the operation of the system. This is done by requiring the embedded controller to “pet” the Watchdog Timer within a predetermined time frame (T_{WD}). If the MCP131X/2X is not “petted” within this time frame, the MCP131X/2X will force the Reset pin(s) active.

The embedded controller “pets” the MCP131X/2X by forcing an edge transition on the WDI pin. The WDT Timer is activated by the first falling edge on the WDI pin.

The standard offering devices have a typical Watchdog Timer period (T_{WD}) of 1.6 s. [Table 1-3](#) shows the available Watchdog Timer periods.

MCP131X/2X

NOTES:

4.0 OPERATIONAL DESCRIPTION

For many of today's microcontroller applications, care must be taken to prevent low-power conditions that can cause many different system problems. The most common causes are brown-out conditions, where the system supply drops below the operating level momentarily. The second most common cause is when a slowly decaying power supply causes the microcontroller to begin executing instructions without sufficient voltage to sustain volatile memory (RAM), thus producing indeterminate results. Figure 4-1 shows a typical application circuit.

The MCP131X/2X family of voltage supervisor devices are designed to keep a microcontroller in Reset, until the system voltage has reached and stabilized at the proper level for reliable system operation. These devices also operate as protection from brown-out conditions when the system supply voltage drops below a safe operating level.

Some MCP131X/2X family members include a Watchdog Timer feature that after being enabled (by a falling edge on the WDI pin), monitors the WDI pin for falling or rising edges. If an edge transition is not detected within the expected time frame, the MCP131X/2X devices will force the Reset pin active. This is useful to ensure that the embedded system's Host Controller program is operating as expected.

Some MCP131X/2X family members include a Manual Reset feature that allow a push button switch to be directly connected to the MCP131X/2X devices (on the MR pin). This allows the system to easily be reset from the external control of the push button switch.

A superset block diagram is shown in Figure 4-2, with device specific block diagrams shown in Figure 4-3 through Figure 4-12.

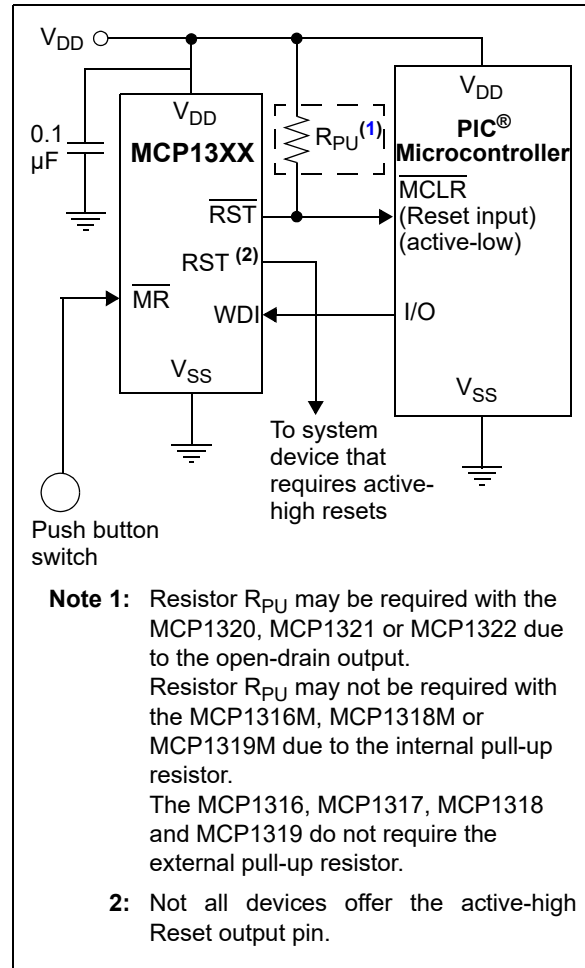


FIGURE 4-1: Typical Application Circuit.

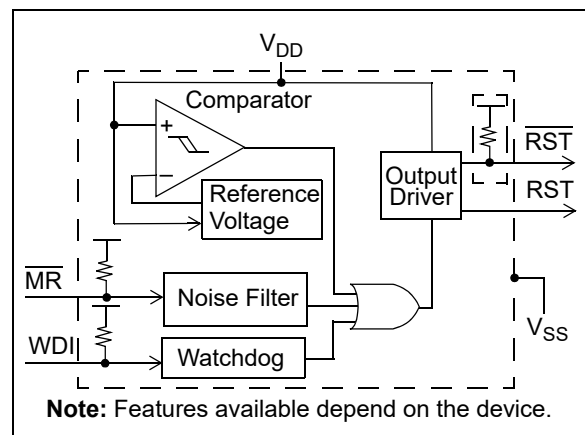


FIGURE 4-2: Family Block Diagram.

MCP131X/2X

4.0.1 DEVICE SPECIFIC BLOCK DIAGRAMS

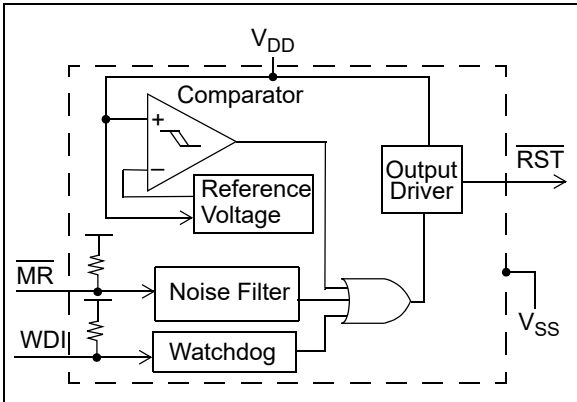


FIGURE 4-3: MCP1316 Block Diagram.

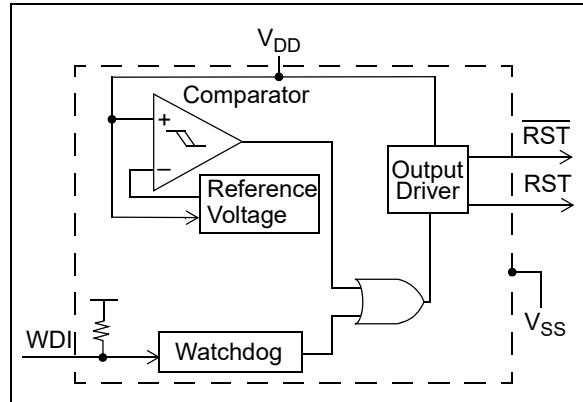


FIGURE 4-6: MCP1318 Block Diagram.

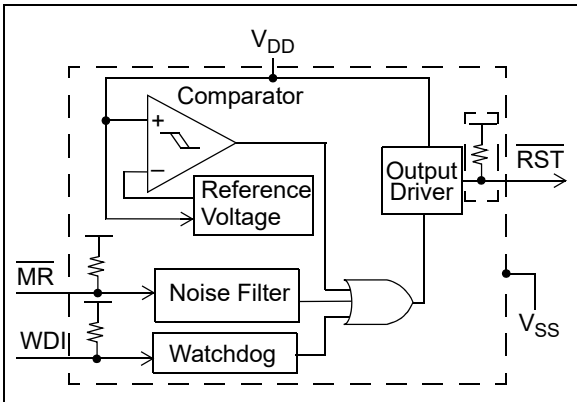


FIGURE 4-4: MCP1316M Block Diagram.

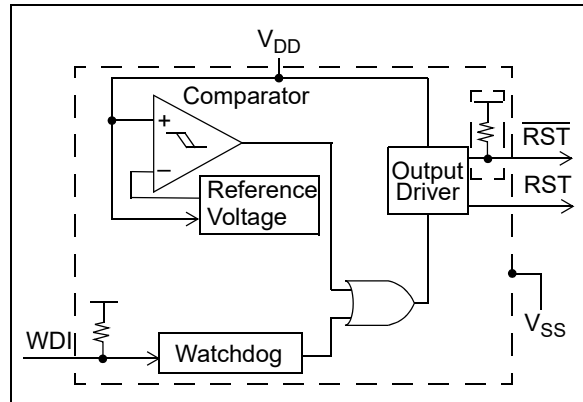


FIGURE 4-7: MCP1318M Block Diagram.

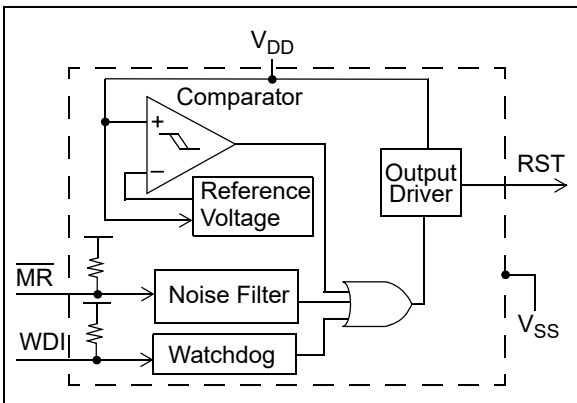


FIGURE 4-5: MCP1317 Block Diagram.

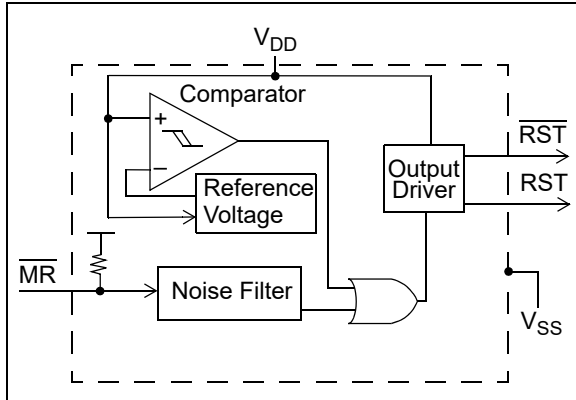


FIGURE 4-8: MCP1319 Block Diagram.

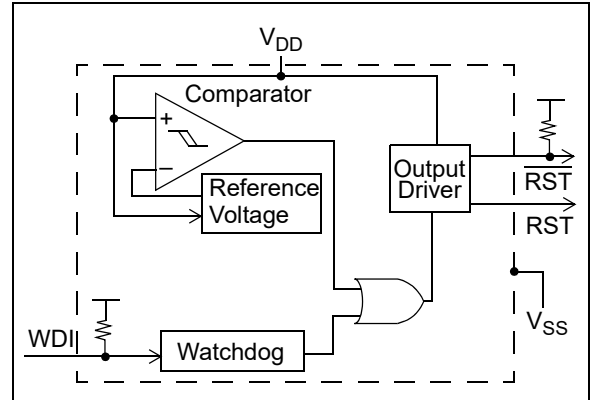


FIGURE 4-11: MCP1321 Block Diagram.

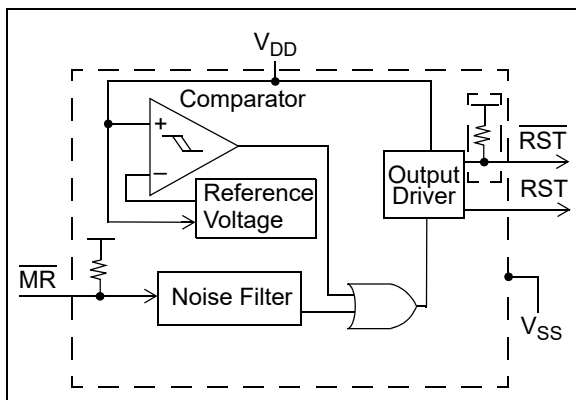


FIGURE 4-9: MCP1319M Block Diagram.

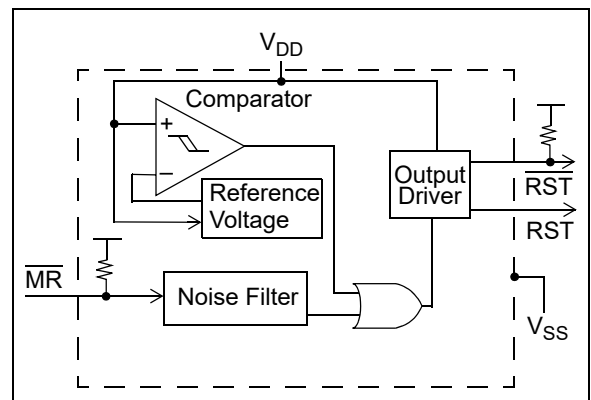


FIGURE 4-12: MCP1322 Block Diagram.

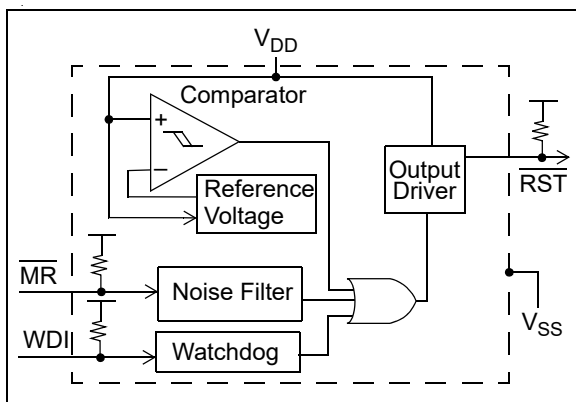


FIGURE 4-10: MCP1320 Block Diagram.

MCP131X/2X

4.1 Reset Voltage Trip Point (V_{TRIP}) Operation

The device's Reset voltage trip point (V_{TRIP}) is selected when the device is ordered. As the voltage on the device's V_{DD} pin is above or below this selected trip point, the output of the Reset pin (RST/\overline{RST}) will be forced to either the inactive or active state.

For the voltage trip point, there is a minimum trip voltage ($V_{TRIPMIN}$) and a maximum trip voltage ($V_{TRIPMAX}$). The voltage that the device "actually" trips at will be referred to as V_{TRIP} . The trip voltage is specified for the falling of the device V_{DD} .

There is also a hysteresis (V_{HYS}) on the trip point. This is so that noise on the device voltage (V_{DD}) does not cause the Reset pin (RST/\overline{RST}) to "jitter" (change between driving an active and inactive state).

The Reset pin (RST or \overline{RST}) will be forced active if any of the following occur:

- The Manual Reset input (\overline{MR}) goes low
- The Watchdog Timer times out
- V_{DD} goes below the threshold
- During device power-up

After the device exits the Reset condition, the delay circuitry will hold the RST and \overline{RST} pins active until the appropriate Reset delay time (t_{RST}) has elapsed.

TABLE 4-1: RESET PIN STATES

Device	State of \overline{RST} Pin when:		State of RST ⁽³⁾ Pin when:		Output Driver
	$V_{DD} < V_{TRIP}$	$V_{DD} > V_{TRIP} + V_{HYS}$	$V_{DD} < V_{TRIP}$	$V_{DD} > V_{TRIP} + V_{HYS}$	
MCP1316	L	H	—	—	Push-pull
MCP1316M	L	H ⁽²⁾	—	—	Open-drain ⁽²⁾
MCP1317	—	—	H	L	Push-pull
MCP1318	L	H	H	L	Push-pull
MCP1318M	L	H ⁽²⁾	H	L	Open-drain ⁽²⁾
MCP1319	L	H	H	L	Push-pull
MCP1319M	L	H ⁽²⁾	H	L	Open-drain ⁽²⁾
MCP1320	L	H ⁽¹⁾	—	—	Open-drain ⁽¹⁾
MCP1321	L	H ⁽¹⁾	H	L	Open-drain ⁽¹⁾
MCP1322	L	H ⁽¹⁾	H	L	Open-drain ⁽¹⁾

Note 1: Requires External Pull-up resistor.

2: Has Internal Pull-up resistor.

3: The RST pin output is always push-pull.

4.1.1 POWER-UP/RISING V_{DD}

As the device V_{DD} rises, the device's Reset circuit will remain active until the voltage rises above the "actual" trip point (V_{TRIP}) plus the hysteresis (V_{HYS}).

Figure 4-13 shows a power-up sequence and the waveform of the RST and \overline{RST} pins.

As the device powers up, the voltage will start below the valid operating voltage of the device. At this voltage, the Reset output value is not valid. Once the voltage is above the minimum operating voltage (1V) and below the selected V_{TRIP} , the Reset output will be active.

Once the device voltage rises above the "actual" trip point (V_{TRIP}) plus the hysteresis (V_{HYS}), the Reset delay timer (t_{RST}) starts. When the Reset delay timer times out, the Reset output (RST/ \overline{RST}) is driven inactive.

Note: While the Reset delay timer (t_{RST}) is active, additional system current is consumed.

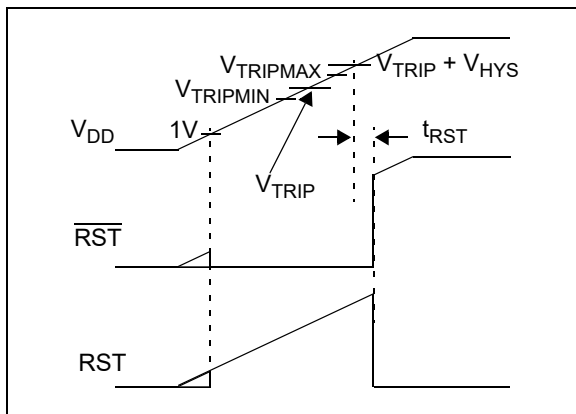


FIGURE 4-13: Reset Pin Operation on a Power-up.

4.1.2 POWER-DOWN/BROWN-OUTS

As the device powers-down/brown-outs, the voltage (V_{DD}) falls from a voltage above the device's trip point (V_{TRIP}). The device's "actual" trip point voltage (V_{TRIP}) will be between the minimum trip point ($V_{TRIPMIN}$) and the maximum trip point ($V_{TRIPMAX}$). Once the device voltage (V_{DD}) goes below this voltage, the Reset pin(s) will be forced to the active state. There is a hysteresis on this trip point. This is so noise on the device voltage (V_{DD}) does not cause the Reset pin (RST/ \overline{RST}) to "jitter" (change between driving an active and inactive).

Figure 4-14 shows the waveform of the \overline{RST} pin as determined by the V_{DD} voltage, while Table 4-1 shows the state of the \overline{RST} pin.

4.1.2.1 Operation of \overline{RST} pin with Internal Pull-Up Resistor

Note: Only the MCP1316M, MCP1318M and MCP1319M devices have an open-drain \overline{RST} output pin with an internal pull-up resistor.

The internal pull-up resistor has a typical value of 4.7 k Ω . The internal pull-up eliminates the need for an external resistor.

To reduce the current consumption of the device, when the \overline{RST} pin is driving low, the resistor is disconnected.

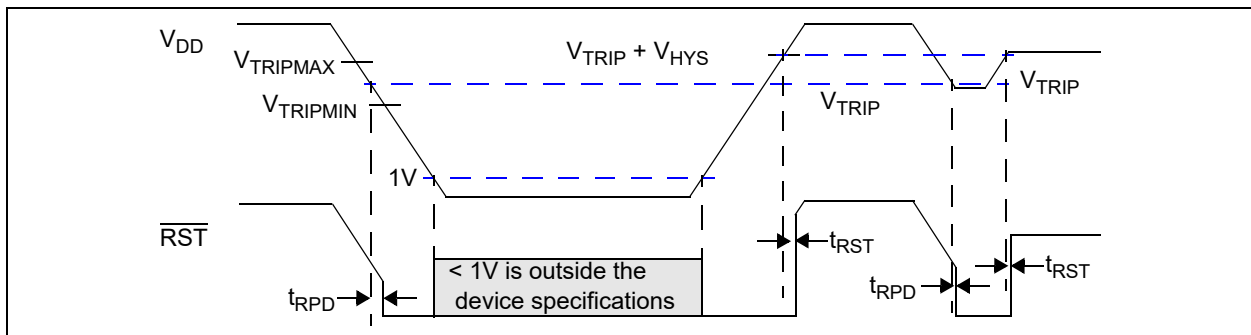


FIGURE 4-14: \overline{RST} Operation as Determined by the V_{TRIP} and V_{HYS} .

MCP131X/2X

4.2 Reset Delay Timer (t_{RST})

The Reset delay timer ensures that the MCP131X/2X device will “hold” the embedded system in Reset until the system voltage has stabilized. There are several time-out options to better meet the requirements of different applications. These Reset delay timer time-outs are shown in Table 4-2. The Standard offering time-out is typically 200 ms.

The Reset delay timer (t_{RST}) starts after the device voltage rises above the “actual” trip point (V_{TRIP}) plus the hysteresis (V_{HYS}). When the Reset delay timer times-out, the Reset output pin (RST/RST) is driven inactive.

Note: While the Reset delay timer (t_{RST}) is active, additional system current is consumed.

TABLE 4-2: RESET DELAY TIMER TIME OUTS (1)

t_{RST}			Units
Min	Typ	Max	
1.0	1.4	2.0	ms
20	30	40	ms
140	200	280	ms
1120	1.6	2.24	sec
↑		↑	
This is the minimum time that the Reset delay timer will “hold” the Reset pin active after V_{DD} rises above $V_{TRIP} + V_{HYS}$		This is the maximum time that the Reset delay timer will “hold” the Reset pin active after V_{DD} rises above $V_{TRIP} + V_{HYS}$	

Note 1: Shaded rows are custom ordered time outs.

Figure 4-15 illustrates when the Reset delay timer (t_{RST}) is active or inactive.

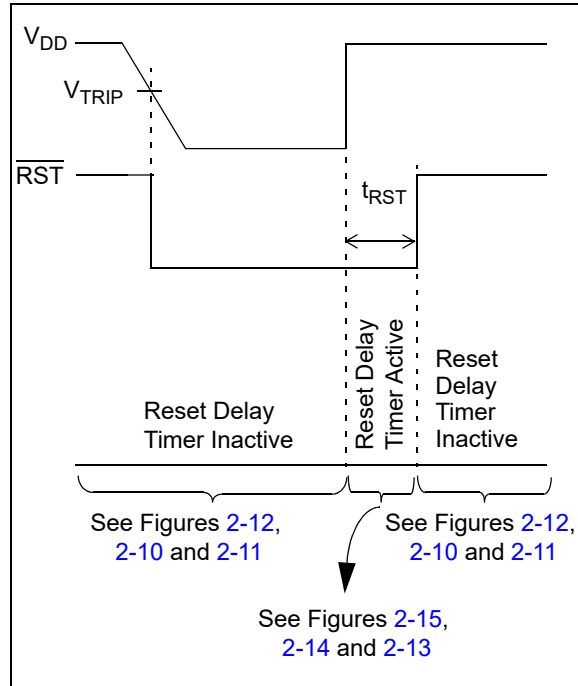


FIGURE 4-15: Reset Power-up Timer Waveform.

4.2.1 EFFECT OF TEMPERATURE ON RESET POWER-UP TIMER (T_{RPU})

The Reset delay timer time-out period (t_{RST}) determines how long the device remains in the Reset condition. This time-out is affected by both the device V_{DD} and temperature. Typical responses for different V_{DD} values and temperatures are shown in Figures 2-33, 2-32 and 2-31.

MCP131X/2X

4.5 Watchdog Timer

The purpose of the Watchdog Timer (WDT) is to increase system reliability. The Watchdog Timer feature can be used to detect when the Host Controller's program flow is not as expected. The Watchdog Timer monitors for activity on the Watchdog Input pin (WDI). The WDI pin is expected to be strobed within a given time frame. When this time frame is exceeded, without an edge transition on the WDI pin, the Reset pin is driven active to reset the system. This stops the Host Controller from continuing its erratic behavior ("run-away" code execution).

The Watchdog Timer is external to the main portion of the control system and monitors the operation of the system. This feature is enabled by a falling edge on the WDI pin (after device POR). Monitoring is then done by requiring the embedded controller to force an edge transition (falling or rising) on the WDI pin ("pet the Watchdog") within a predetermined time frame (T_{WDT}). If the MCP131X/2X does not detect an edge on the WDI pin within the expected time frame, the MCP131X/2X device will force the Reset pin active.

The Watchdog Timer is in the disabled state when:

- The Device Powers up
- A POR event occurred
- A WDT event occurred
- A Manual Reset (\overline{MR}) event occurred

When the Watchdog Timer is in the disabled state, the WDI pin has an internal smart pull-up resistor enabled. This pull-up resistor has a typical value of 52 k Ω . This pull-up resistor holds the WDI signal in the high state, until it is forced to another state.

After the embedded controller has initialized, if the Watchdog Timer feature is to be used, then the embedded controller can force the WDI pin low (V_{IL}). This also enables the Watchdog Timer feature and disables the WDI pull-up resistor. Disabling the pull-up resistor reduces the device's current consumption. The pull-up resistor will remain disconnected until the device has a power-on, a Reset event occurs, or after the WDT time out.

Once the Watchdog Timer has been enabled, the Host Controller must force an edge transition (falling or rising) on the WDI pin before the minimum Watchdog Timer time out to ensure that the Watchdog Timer does not force the Reset pins (RST/\overline{RST}) to the active state.

If an edge transition does not occur before the maximum time out occurs, then the MCP131X/2X will force the Reset pins to their active state.

The MCP131X/2X supports four time outs. The standard offering devices have a typical Watchdog Timer period (T_{WDT}) of 1.6 s. Table 4-3 shows the available Watchdog Timer periods. The t_{WDT} time-out is a function of the device voltage and temperature.

Figure 4-19 shows a block diagram for using the MCP131X/2X with a PIC[®] microcontroller (MCU) and the Watchdog input.

TABLE 4-3: WATCHDOG TIMER PERIODS (1)

t_{WDT}			Units
Min	Typ	Max	
4.3	6.3	9.3	ms
71	102	153	ms
1.12	1.6	2.4	sec
17.9	25.6	38.4	sec

↑

If the time between WDI edges is less than this, it ensures that the MCP131X/2X **never** forces a Reset

↑

If the time between WDI edges is greater than this, it ensures that the MCP131X/2X **always** forces a Reset

Note 1: Shaded rows are custom ordered Watchdog Timer Periods (t_{WDT}) time outs. For information on ordering devices with these t_{WDT} time outs, please contact your local Microchip sales office. Minimum purchase volumes are required.

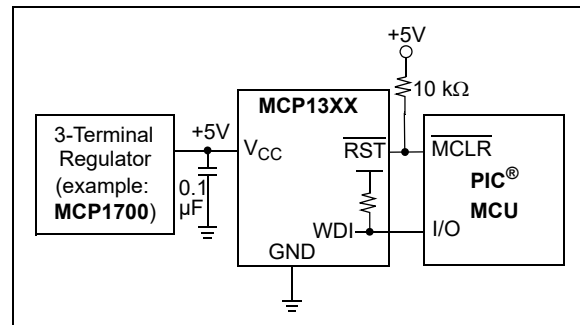


FIGURE 4-19: Watchdog Timer.

The software routine that strobes WDI is critical. The code must be in a section of software that is executed frequently enough so the time between edge transitions is less than the Watchdog time-out period. One common technique controls the Host Controllers I/O line from two sections of the program. The software might set the I/O line high while operating in the Foreground mode and set it low while in the Background or Interrupt modes. If both modes do not execute correctly, the Watchdog Timer issues Reset pulses.

5.0 APPLICATION INFORMATION

This section shows application-related information that may be useful for your particular design requirements.

5.1 Supply Monitor Noise Sensitivity

The MCP131X/2X devices are optimized for fast response to negative-going changes in V_{DD} . Systems with an inordinate amount of electrical noise on V_{DD} (such as systems using relays) may require a $0.01\ \mu\text{F}$ or $0.1\ \mu\text{F}$ bypass capacitor to reduce detection sensitivity. This capacitor should be installed as close to the MCP131X/2X as possible to keep the capacitor lead length short.

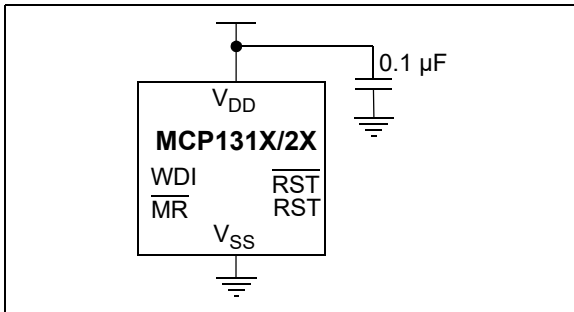


FIGURE 5-1: Typical Application Circuit with Bypass Capacitor.

5.2 Conventional Voltage Monitoring

Figure 5-2 and Figure 5-3 show the MCP131X/2X in conventional voltage monitoring applications.

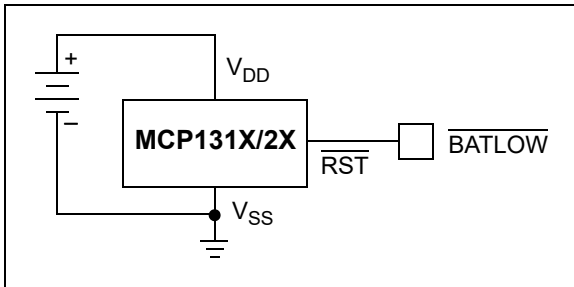


FIGURE 5-2: Battery Voltage Monitor.

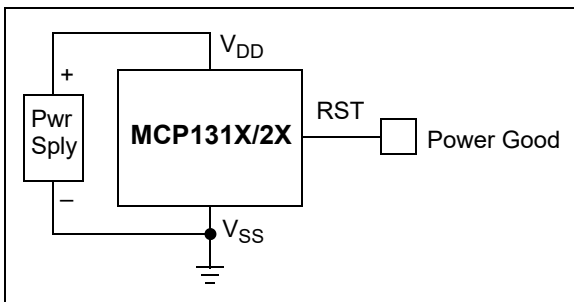


FIGURE 5-3: Power Good Monitor.

5.3 Using in PIC[®] Microcontroller, ICSP[™] Applications

Note: This operation can only be done using the device with the Open-Drain $\overline{\text{RST}}$ pin (MCP1320, MCP1321, and MCP1322). Devices that have the internal pull-up resistor are not recommended due to the current path of the internal pull-up resistor.

Figure 5-4 shows the typical application circuit for using the MCP132X for a voltage supervisory function when the PIC[®] microcontroller will be programmed via the In-Circuit Serial Programming[™] (ICSP[™]) feature. Additional information is available in TB087, "Using Voltage Supervisors with PIC[®] Microcontroller Systems which Implement In-Circuit Serial Programming[™]" (DS91087).

Note: It is recommended that the current into the $\overline{\text{RST}}$ pin be current limited by a $1\ \text{k}\Omega$ resistor.

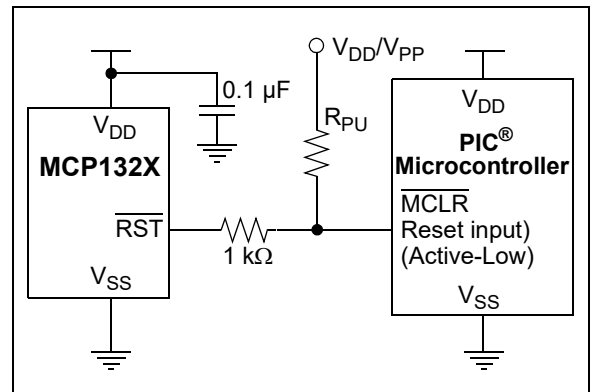


FIGURE 5-4: Typical Application Circuit for PIC[®] Microcontroller with the ICSP[™] Feature.

MCP131X/2X

5.4 Modifying The Trip Point, V_{TRIP}

Although the MCP131X/2X device has a fixed voltage trip point (V_{TRIP}), it is sometimes necessary to make custom adjustments. This can be accomplished by connecting an external resistor divider to the MCP131X/2X V_{DD} pin. This causes the V_{SOURCE} voltage to be at a higher voltage than when the MCP131X/2X input equals its V_{TRIP} voltage (Figure 5-5).

To maintain detector accuracy, the bleeder current through the divider should be significantly higher than the 10 μ A maximum operating current required by the MCP131X/2X. A reasonable value for this bleeder current is 1 mA (100 times the 10 μ A required by the MCP131X/2X). For example, if $V_{TRIP} = 2V$ and the desired trip point is 2.5V, the value of $R_1 + R_2$ is 2.5 k Ω (2.5V/1 mA). The value of $R_1 + R_2$ can be rounded to the nearest standard value and plugged into the equation of Figure 5-5 to calculate values for R_1 and R_2 . 1% tolerance resistors are recommended.

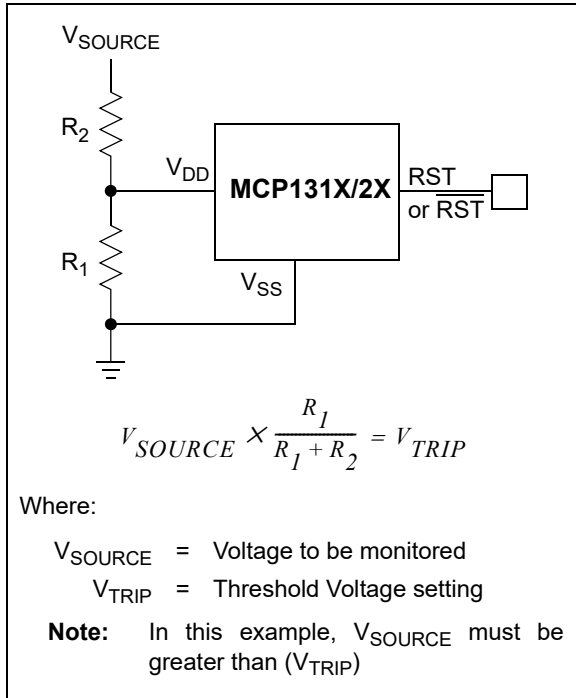


FIGURE 5-5: Modify Trip-Point using External Resistor Divider.

5.5 MOSFET Low-Drive Protection

Low operating power and small physical size make the MCP131X/2X series ideal for many voltage detector applications. Figure 5-6 shows a low-voltage gate drive protection circuit that prevents overheating of the logic-level MOSFET due to insufficient gate voltage. When the input signal is below the threshold of the MCP131X/2X, its output grounds the gate of the MOSFET.

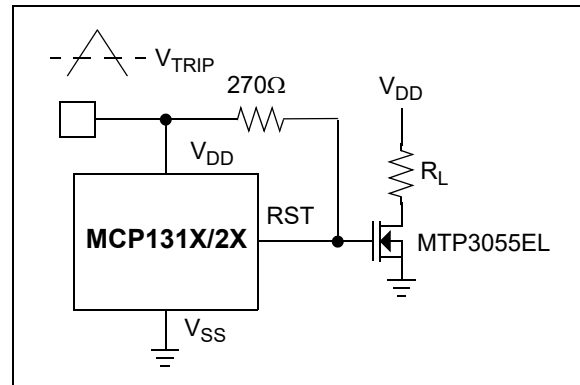


FIGURE 5-6: MOSFET Low-Drive Protection.

5.6 Low-Power Applications

In some low-power applications, the longer the microcontroller (such as a PIC[®] MCU) can be in the “Sleep mode”, the lower the average system current consumption will be.

The WDT feature can be used to “wake-up” the PIC MCU at a regular interval to service the required tasks before returning to sleep. This “wake-up” occurs after the PIC MCU detects a MCLR reset during Sleep mode (for mid-range family; POR = 1, BOR = 1, TO = 1, and PD = 1).

5.7 Controllers and Processors With Bidirectional I/O Pins

Some microcontrollers have bidirectional Reset pins. Depending on the current drive capability of the controller pin, an indeterminate logic level may result if there is a logic conflict. This can be avoided by adding a 4.7 kΩ resistor in series with the output of the MCP131X/2X (Figure 5-7). If there are other components in the system that require a Reset signal, they should be buffered so as not to load the Reset line. If the other components are required to follow the Reset I/O of the microcontroller, the buffer should be connected as shown with the solid line.

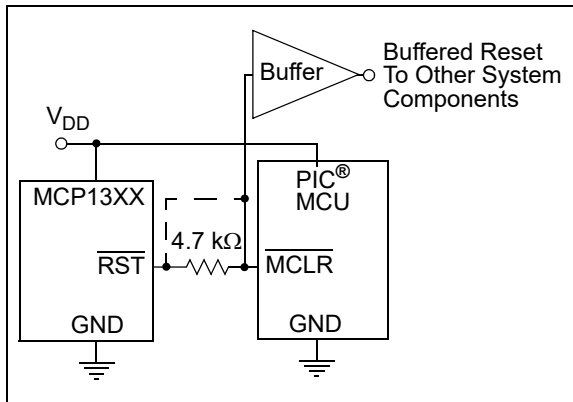


FIGURE 5-7: Interfacing the MCP131X/2X Push-Pull Outputs to a Bidirectional Reset I/O.

5.8 RESET Signal Integrity During Power-Down

The MCP131X/2X Reset output is valid to $V_{DD} = 1.0V$. Below this 1.0V, the output becomes an "open circuit" and does not sink or source current. This means CMOS logic inputs to the microcontroller will be floating at an undetermined voltage. Most digital systems are completely shut down well above this voltage. However, in situations where the Reset signal must be maintained valid to $V_{DD} = 0V$, external circuitry is required.

For devices where the Reset signal is active-low, a pull-down resistor must be connected from the MCP131X/2X Reset pin(s) to ground to discharge stray capacitances and hold the output low (Figure 5-8).

Similarly, for devices where the Reset signal is active-high, a pull-up resistor to V_{DD} is required to ensure a valid high Reset signal for V_{DD} below 1.0V.

This resistor value, though not critical, should be chosen such that it does not appreciably load the Reset pin(s) under normal operation (100 kΩ will be suitable for most applications).

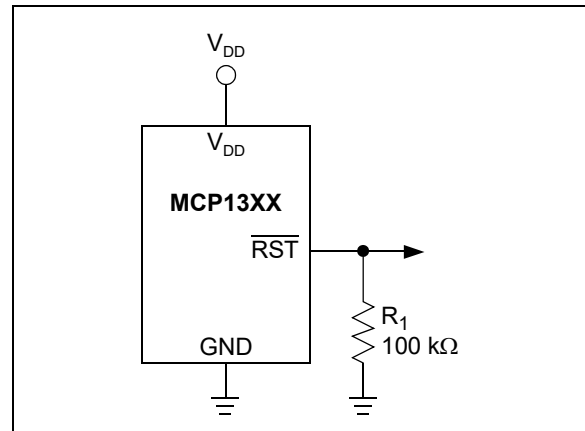


FIGURE 5-8: Ensuring a Valid Active-low Reset Pin Output State as V_{DD} Approaches 0V.

MCP131X/2X

NOTES:

6.0 STANDARD DEVICE OFFERINGS

Table 6-1 shows the standard devices that are available and their respective configuration. The configuration includes:

- Voltage Trip Point (V_{TRIP})
- Reset Time Out (t_{RST})
- Watchdog Time Out (t_{WDT})

Table 6-1 also shows the order number for that given device configuration.

TABLE 6-1: STANDARD VERSIONS

Device	Reset Threshold (V)	Reset Time Out (ms)		Watchdog Time Out (s)		Order Number
		Minimum	Typical	Minimum	Typical	
MCP1316	2.90	140	200	1.12	1.6	MCP1316T-29LE/OT
MCP1316	4.60	140	200	1.12	1.6	MCP1316T-46LE/OT
MCP1316M	2.90	140	200	1.12	1.6	MCP1316MT-29LE/OT
MCP1316M	4.60	140	200	1.12	1.6	MCP1316MT-46LE/OT
MCP1317	2.90	140	200	1.12	1.6	MCP1317T-29LE/OT
MCP1317	4.60	140	200	1.12	1.6	MCP1317T-46LE/OT
MCP1318	2.90	140	200	1.12	1.6	MCP1318T-29LE/OT
MCP1318	4.60	140	200	1.12	1.6	MCP1318T-46LE/OT
MCP1318M	2.90	140	200	1.12	1.6	MCP1318MT-29LE/OT
MCP1318M	4.60	140	200	1.12	1.6	MCP1318MT-46LE/OT
MCP1319	2.90	140	200	—	—	MCP1319T-29LE/OT
MCP1319	4.60	140	200	—	—	MCP1319T-46LE/OT
MCP1319M	2.90	140	200	—	—	MCP1319MT-29LE/OT
MCP1319M	4.60	140	200	—	—	MCP1319MT-46LE/OT
MCP1320	2.90	140	200	1.12	1.6	MCP1320T-29LE/OT
MCP1320	4.60	140	200	1.12	1.6	MCP1320T-46LE/OT
MCP1321	2.90	140	200	1.12	1.6	MCP1321T-29LE/OT
MCP1321	4.60	140	200	1.12	1.6	MCP1321T-46LE/OT
MCP1322	2.90	140	200	—	—	MCP1322T-29LE/OT
MCP1322	4.60	140	200	—	—	MCP1322T-46LE/OT

6.1 Custom Configurations

Table 6-2 shows the codes that specify the desired Reset time out (t_{RST}) and Watchdog Timer time-out (t_{WDT}) for custom devices.

The voltage trip point (V_{TRIP}) is specified by the two digits of the desired typical trip point voltage. As an example, if the desired V_{TRIP} selection has a typical V_{TRIP} of 2.7V, the code is 27.

TABLE 6-2: DELAY TIME-OUT ORDERING CODES

Code	Typical Delay Time (ms)		Comment	Code	Typical Delay Time (ms)		Comment
	Reset	WDT			Reset	WDT	
A	1.6	6.3	Note 1	J	200.0	6.3	Note 1
B	1.6	102.0	Note 1	K	200.0	102.0	Note 1
C	1.6	1600.0	Note 1	L	200.0	1600.0	Delay timings for standard device offerings
D	1.6	25600.0	Note 1	M	200.0	25600.0	Note 1
E	30.0	6.3	Note 1	N	1600.0	6.3	Note 1
F	30.0	102.0	Note 1	P	1600.0	102.0	Note 1
G	30.0	1600.0	Note 1	Q	1600.0	1600.0	Note 1
H	30.0	25600.0	Note 1	R	1600.0	25600.0	Note 1

Note 1: This delay timing combination is not the standard offering. For information on ordering devices with these delay times, contact your local Microchip sales office. Minimum purchase volumes are required.

MCP131X/2X

NOTES:

7.0 DEVELOPMENT TOOLS

7.1 Evaluation/Demonstration Boards

The SOT-23-5/6 Evaluation Board (VSUPEV2) can be used to evaluate the characteristics of the MCP131X/2X devices.

This blank PCB has footprints for:

- Pull-up Resistor
- Pull-down Resistor
- Loading Capacitor
- In-line Resistor

There is also a power supply filtering capacitor.

For evaluating the MCP131X/2X devices, the selected device should be installed into the Option A footprint.

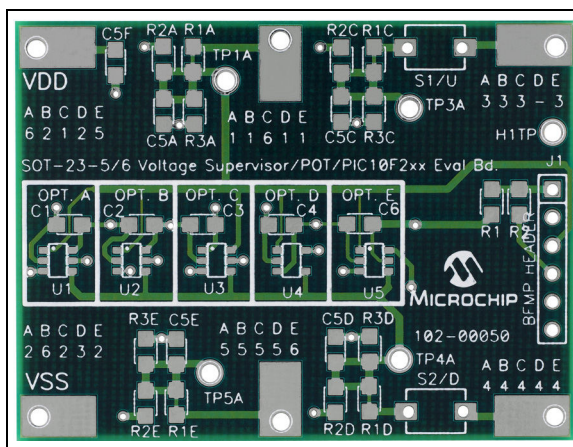


FIGURE 1: SOT-23-5/6 Voltage Supervisor Evaluation Board (VSUPEV2).

This board may be purchased directly from the Microchip web site at www.microchip.com.

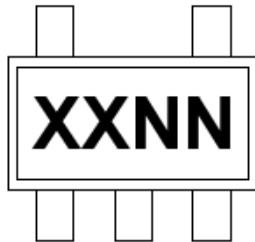
MCP131X/2X

NOTES:

8.0 PACKAGING INFORMATION

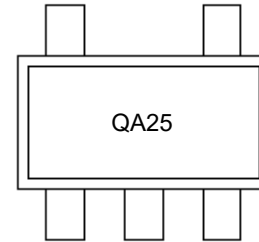
8.1 Package Marking Information

5-Pin SOT-23



Part Number	SOT-23
MCP1316T-29LE/OT	QANN
MCP1316MT-29LE/OT	QBNN
MCP1317T-29LE/OT	QCNN
MCP1318T-29LE/OT	QDNN
MCP1318MT-29LE/OT	QENN
MCP1319T-29LE/OT	QFNN
MCP1319MT-29LE/OT	QGNN
MCP1320T-29LE/OT	QHNN
MCP1321T-29LE/OT	QJNN
MCP1322T-29LE/OT	QKNN
MCP1316T-46LE/OT	QLNN
MCP1316MT-46LE/OT	QMNN
MCP1317T-46LE/OT	QPNN
MCP1318T-46LE/OT	QQNN
MCP1318MT-46LE/OT	QRNN
MCP1319T-46LE/OT	QSNN
MCP1319MT-46LE/OT	QTNN
MCP1320T-46LE/OT	QUNN
MCP1321T-46LE/OT	QVNN
MCP1322T-46LE/OT	QWNN

Example:



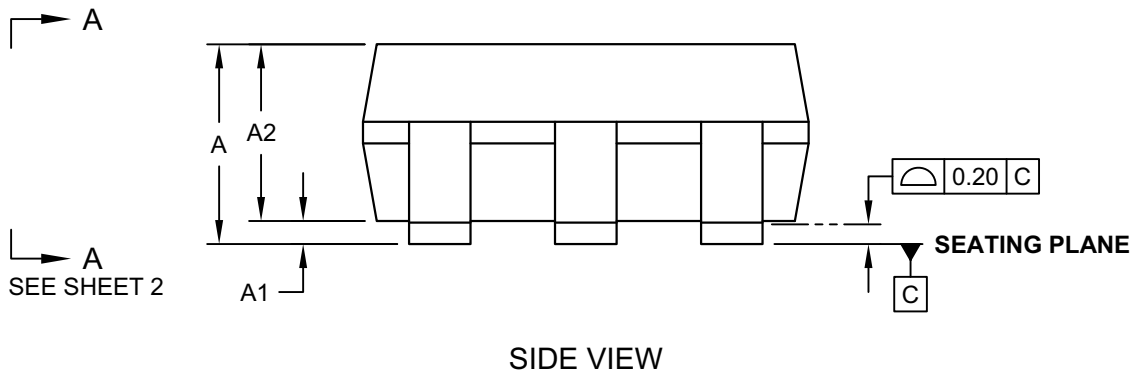
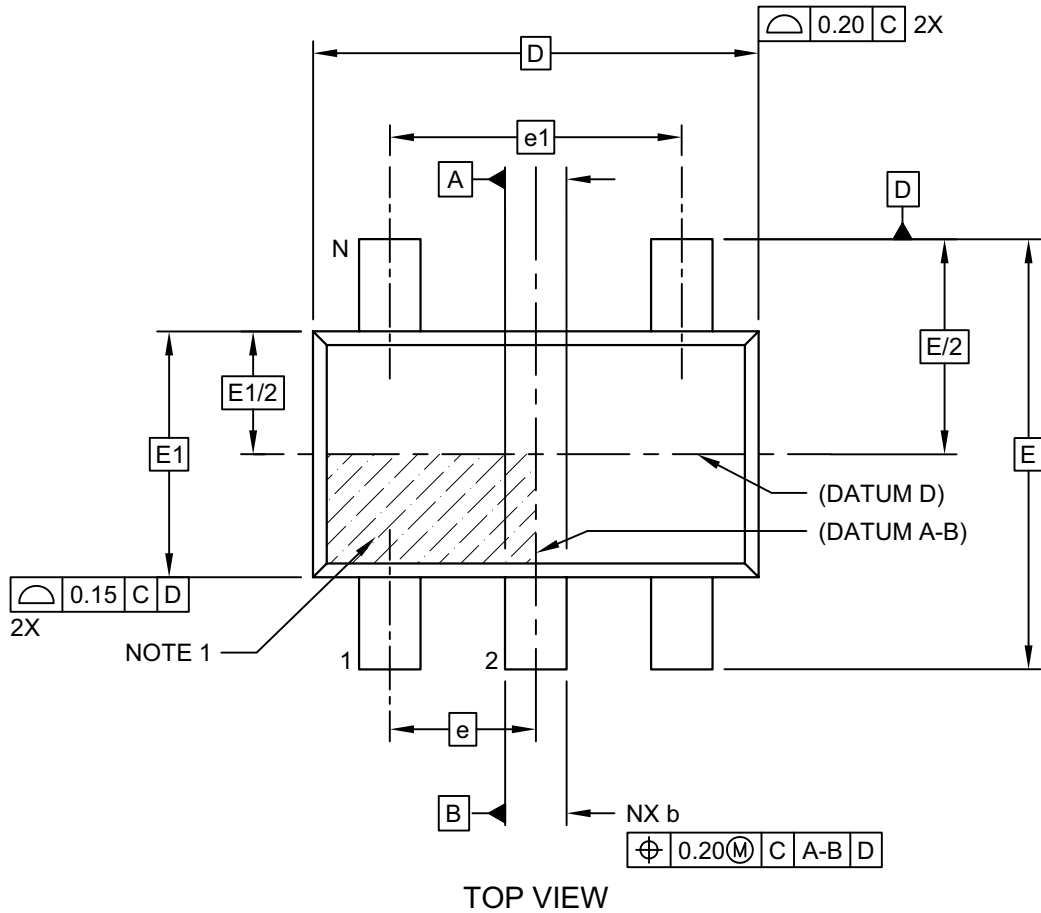
Legend:	XX...X	Customer-specific information
	Y	Year code (last digit of calendar year)
	YY	Year code (last 2 digits of calendar year)
	WW	Week code (week of January 1 is week '01')
	NNN	Alphanumeric traceability code
	(e3)	Pb-free JEDEC designator for Matte Tin (Sn)
	*	This package is Pb-free. The Pb-free JEDEC designator (e3) can be found on the outer packaging for this package.

Note: In the event the full Microchip part number cannot be marked on one line, it will be carried over to the next line, thus limiting the number of available characters for customer-specific information.

MCP131X/2X

5-Lead Plastic Small Outline Transistor (OT) [SOT23]

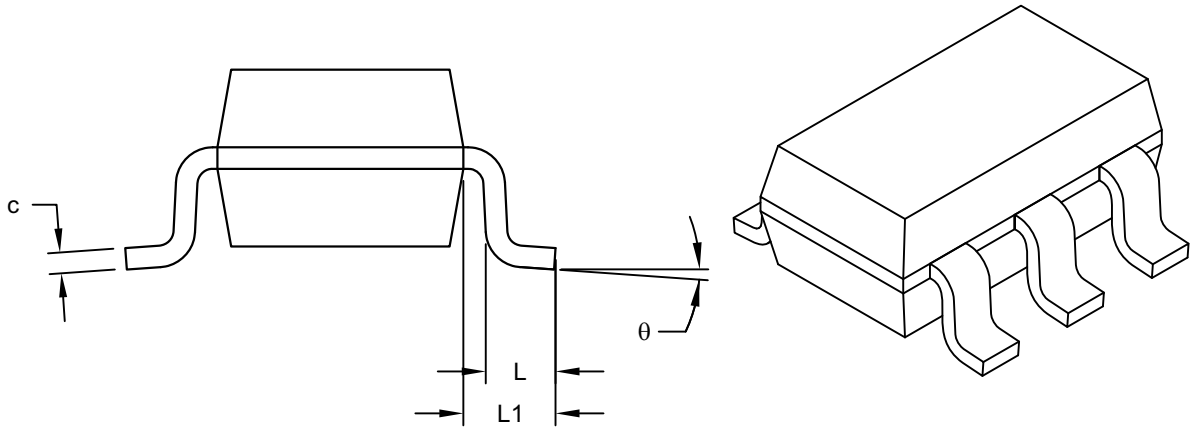
Note: For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>



Microchip Technology Drawing C04-091-OT Rev F Sheet 1 of 2

5-Lead Plastic Small Outline Transistor (OT) [SOT23]

Note: For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>



VIEW A-A
SHEET 1

Dimension Limits	Units	MILLIMETERS		
		MIN	NOM	MAX
Number of Pins	N	5		
Pitch	e	0.95 BSC		
Outside lead pitch	e1	1.90 BSC		
Overall Height	A	0.90	-	1.45
Molded Package Thickness	A2	0.89	-	1.30
Standoff	A1	-	-	0.15
Overall Width	E	2.80 BSC		
Molded Package Width	E1	1.60 BSC		
Overall Length	D	2.90 BSC		
Foot Length	L	0.30	-	0.60
Footprint	L1	0.60 REF		
Foot Angle	φ	0°	-	10°
Lead Thickness	c	0.08	-	0.26
Lead Width	b	0.20	-	0.51

Notes:

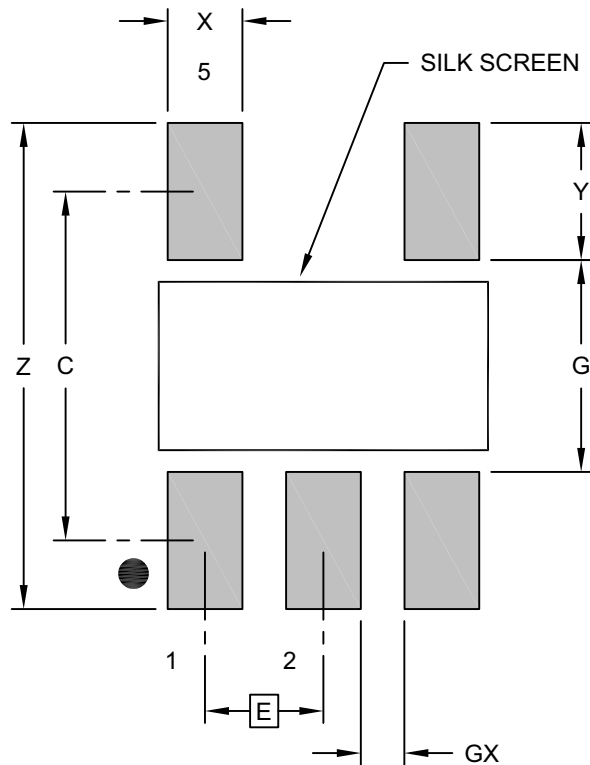
- Dimensions D and E1 do not include mold flash or protrusions. Mold flash or protrusions shall not exceed 0.25mm per side.
- Dimensioning and tolerancing per ASME Y14.5M
BSC: Basic Dimension. Theoretically exact value shown without tolerances.
REF: Reference Dimension, usually without tolerance, for information purposes only.

Microchip Technology Drawing C04-091-OT Rev F Sheet 2 of 2

MCP131X/2X

5-Lead Plastic Small Outline Transistor (OT) [SOT23]

Note: For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>



RECOMMENDED LAND PATTERN

Dimension Limits	Units	MILLIMETERS		
		MIN	NOM	MAX
Contact Pitch	E	0.95 BSC		
Contact Pad Spacing	C		2.80	
Contact Pad Width (X5)	X			0.60
Contact Pad Length (X5)	Y			1.10
Distance Between Pads	G	1.70		
Distance Between Pads	GX	0.35		
Overall Width	Z			3.90

Notes:

1. Dimensioning and tolerancing per ASME Y14.5M

BSC: Basic Dimension. Theoretically exact value shown without tolerances.

Microchip Technology Drawing No. C04-2091-OT Rev F

8.2 Product Tape and Reel Specifications

FIGURE 8-1: EMBOSSED CARRIER DIMENSIONS (8 MM TAPE ONLY)

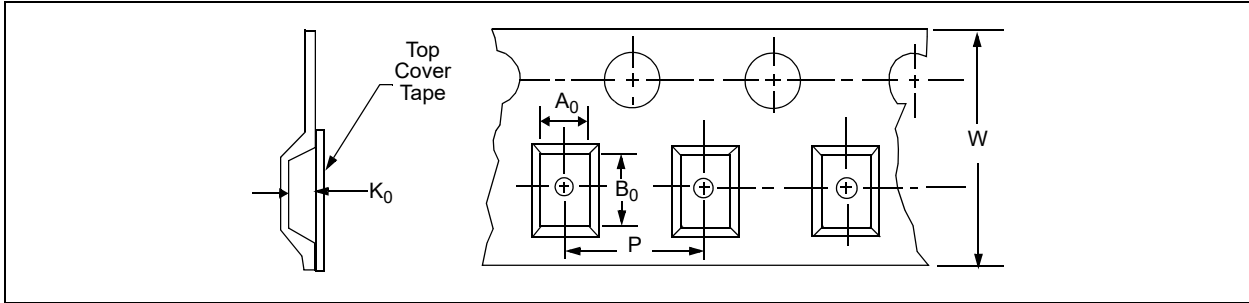
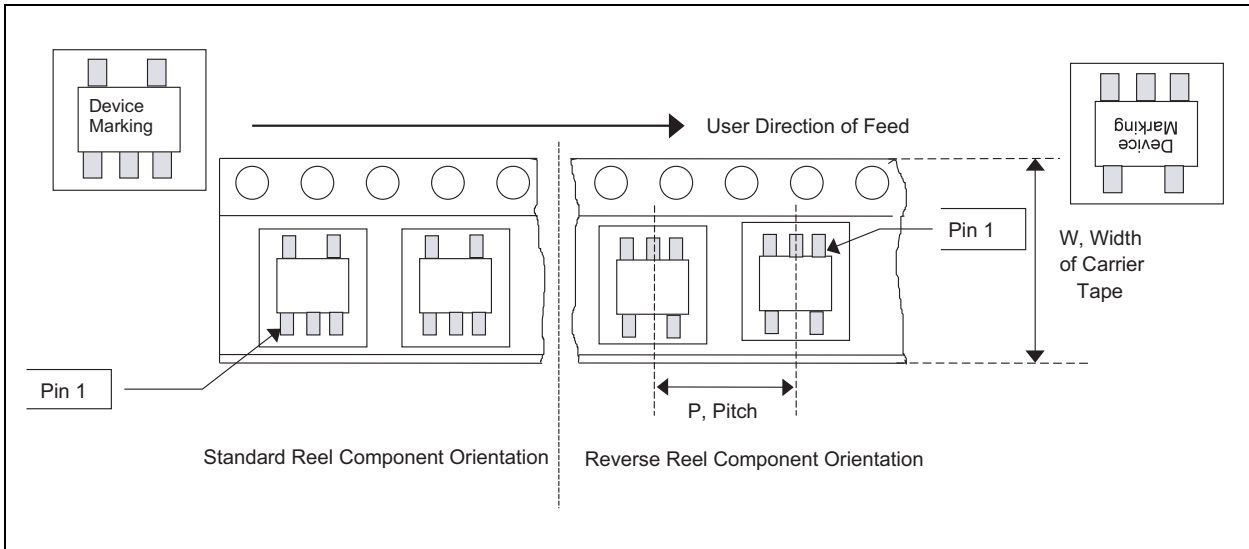


TABLE 8-1: CARRIER TAPE/CAVITY DIMENSIONS

Case Outline	Package Type		Carrier Dimensions		Cavity Dimensions			Output Quantity Units	Reel Diameter in mm
			W mm	P mm	A0 mm	B0 mm	K0 mm		
OT	SOT-23	3L	8	4	3.23	3.17	1.37	3000	180

FIGURE 8-2: 5-LEAD SOT-23 DEVICE TAPE AND REEL SPECIFICATIONS



MCP131X/2X

NOTES:

APPENDIX A: REVISION HISTORY

Revision E (January 2021)

The following is the list of modifications:

1. Added information about the automotive qualification status of the device in the [Features](#) section.
2. Updated the [Product Identification System](#) section with automotive qualified devices.

Revision D (August 2012)

The following is the list of modifications:

1. Updated [Figure 2-22](#) and [Figure 2-23](#).

Revision C (February 2012)

The following is the list of modifications:

1. Updated package temperature in the [Temperature Characteristics](#) table.
2. Corrected text in [Section 4.0, Operational Description](#).
3. Updated package specification in [Section 8.0, Packaging Information](#) to show all drawings available.
4. Other minor typographical corrections.

Revision B (October 2007)

The following is the list of modifications:

1. Clarified that devices with a Voltage Trip Point $\leq 2.4V$ are tested from $-40^{\circ}C$ to $+85^{\circ}C$. Devices with a Voltage Trip Point $\geq 2.5V$ are tested from $-40^{\circ}C$ to $+125^{\circ}C$.

Revision A (November 2005)

- Original Release of this Document.

MCP131X/2X

NOTES:

PRODUCT IDENTIFICATION SYSTEM

To order or obtain information, e.g., on pricing or delivery, refer to the factory or the listed sales office.

PART NO.	X ⁽¹⁾	XX	X	X	/XX	XXX	Examples:
Device	Tape/Reel Option	V _{TRIP} Options	Time-Out Options	Temperature Range	Package	Qualification	
Device:							
MCP1316T:							a) MCP1316T-29LE/OT: 5-Lead SOT-23
							b) MCP1316T-29LE/OTVAO: 5-Lead SOT-23, Automotive Qualified
MCP1316MT:							c) MCP1316T-46LE/OT: 5-Lead SOT-23
							d) MCP1316T-46LE/OTVAO: 5-Lead SOT-23, Automotive Qualified
MCP1317T:							e) MCP1316MT-29LE/OT: 5-Lead SOT-23
							f) MCP1316MT-29LE/OTVAO: 5-Lead SOT-23, Automotive Qualified
MCP1318T:							g) MCP1316MT-46LE/OT: 5-Lead SOT-23
							h) MCP1316MT-46LE/OTVAO: 5-Lead SOT-23, Automotive Qualified
MCP1318MT:							a) MCP1317T-29LE/OT: 5-Lead SOT-23
							b) MCP1317T-46LE/OT: 5-Lead SOT-23
MCP1319T:							a) MCP1318T-29LE/OT: 5-Lead SOT-23
							b) MCP1318T-29LE/OTVAO: 5-Lead SOT-23, Automotive Qualified
MCP1319MT:							c) MCP1318MT-29LE/OT: 5-Lead SOT-23
							d) MCP1318T-46LE/OT: 5-Lead SOT-23
MCP1320T:							e) MCP1318MT-46LE/OT: 5-Lead SOT-23
							a) MCP1319T-29LE/OT: 5-Lead SOT-23
MCP1321T:							b) MCP1319T-29LE/OTVAO: 5-Lead SOT-23, Automotive Qualified
							c) MCP1319MT-29LE/OT: 5-Lead SOT-23
MCP1322T:							d) MCP1319MT-29LE/OTVAO: 5-Lead SOT-23, Automotive Qualified
							e) MCP1319T-46LE/OT: 5-Lead SOT-23
							f) MCP1319T-46LE/OTVAO: 5-Lead SOT-23, Automotive Qualified
							g) MCP1319MT-46LE/OT: 5-Lead SOT-23
							h) MCP1319MT-46LE/OTVAO: 5-Lead SOT-23, Automotive Qualified
							a) MCP1320T-29LE/OT: 5-Lead SOT-23
							b) MCP1320T-29LE/OTVAO: 5-Lead SOT-23, Automotive Qualified
							c) MCP1320T-46LE/OT: 5-Lead SOT-23
							d) MCP1320T-46LE/OTVAO: 5-Lead SOT-23, Automotive Qualified
							a) MCP1321T-29LE/OT: 5-Lead SOT-23
							b) MCP1321T-29LE/OTVAO: 5-Lead SOT-23, Automotive Qualified
							c) MCP1321T-46LE/OT: 5-Lead SOT-23
							d) MCP1321T-46LE/OTVAO: 5-Lead SOT-23, Automotive Qualified
							a) MCP1322T-29LE/OT: 5-Lead SOT-23
							b) MCP1322T-29LE/OTVAO: 5-Lead SOT-23, Automotive Qualified
							c) MCP1322T-46LE/OT: 5-Lead SOT-23
							d) MCP1322T-46LE/OTVAO: 5-Lead SOT-23, Automotive Qualified
V_{TRIP} Options: (Note 2)		29 = 2.90V 46 = 4.60V					
Time-Out Options: (Note 2)			L = t _{RST} = 200 ms (typ.), t _{WDT} = 1.6 s (typ.)				
Temperature Range:				I = -40°C to +85°C (Only for trip points 2.0V to 2.4V) E = -40°C to +125°C (For trip point ≥ 2.5V)			
Package:					OT = SOT-23, 5-lead		
Qualification:						Blank = Standard Qualification VAO = AEC-Q100 Automotive Qualification	
							Note 1: Tape and Reel identifier only appears in the catalog part number description. This identifier is used for ordering purposes and is not printed on the device package. Check with your Microchip Sales Office for package availability with the Tape and Reel option.
							Note 2: Custom ordered voltage trip points and time-outs are available. Please contact your local Microchip sales office for additional information. Minimum purchase volumes are required.

MCP131X/2X

NOTES:

Note the following details of the code protection feature on Microchip devices:

- Microchip products meet the specifications contained in their particular Microchip Data Sheet.
- Microchip believes that its family of products is secure when used in the intended manner and under normal conditions.
- There are dishonest and possibly illegal methods being used in attempts to breach the code protection features of the Microchip devices. We believe that these methods require using the Microchip products in a manner outside the operating specifications contained in Microchip's Data Sheets. Attempts to breach these code protection features, most likely, cannot be accomplished without violating Microchip's intellectual property rights.
- Microchip is willing to work with any customer who is concerned about the integrity of its code.
- Neither Microchip nor any other semiconductor manufacturer can guarantee the security of its code. Code protection does not mean that we are guaranteeing the product is "unbreakable." Code protection is constantly evolving. We at Microchip are committed to continuously improving the code protection features of our products. Attempts to break Microchip's code protection feature may be a violation of the Digital Millennium Copyright Act. If such acts allow unauthorized access to your software or other copyrighted work, you may have a right to sue for relief under that Act.

Information contained in this publication is provided for the sole purpose of designing with and using Microchip products. Information regarding device applications and the like is provided only for your convenience and may be superseded by updates. It is your responsibility to ensure that your application meets with your specifications.

THIS INFORMATION IS PROVIDED BY MICROCHIP "AS IS". MICROCHIP MAKES NO REPRESENTATIONS OR WARRANTIES OF ANY KIND WHETHER EXPRESS OR IMPLIED, WRITTEN OR ORAL, STATUTORY OR OTHERWISE, RELATED TO THE INFORMATION INCLUDING BUT NOT LIMITED TO ANY IMPLIED WARRANTIES OF NON-INFRINGEMENT, MERCHANTABILITY, AND FITNESS FOR A PARTICULAR PURPOSE OR WARRANTIES RELATED TO ITS CONDITION, QUALITY, OR PERFORMANCE.

IN NO EVENT WILL MICROCHIP BE LIABLE FOR ANY INDIRECT, SPECIAL, PUNITIVE, INCIDENTAL OR CONSEQUENTIAL LOSS, DAMAGE, COST OR EXPENSE OF ANY KIND WHATSOEVER RELATED TO THE INFORMATION OR ITS USE, HOWEVER CAUSED, EVEN IF MICROCHIP HAS BEEN ADVISED OF THE POSSIBILITY OR THE DAMAGES ARE FORESEEABLE. TO THE FULLEST EXTENT ALLOWED BY LAW, MICROCHIP'S TOTAL LIABILITY ON ALL CLAIMS IN ANY WAY RELATED TO THE INFORMATION OR ITS USE WILL NOT EXCEED THE AMOUNT OF FEES, IF ANY, THAT YOU HAVE PAID DIRECTLY TO MICROCHIP FOR THE INFORMATION. Use of Microchip devices in life support and/or safety applications is entirely at the buyer's risk, and the buyer agrees to defend, indemnify and hold harmless Microchip from any and all damages, claims, suits, or expenses resulting from such use. No licenses are conveyed, implicitly or otherwise, under any Microchip intellectual property rights unless otherwise stated.

Trademarks

The Microchip name and logo, the Microchip logo, Adaptec, AnyRate, AVR, AVR logo, AVR Freaks, BesTime, BitCloud, chipKIT, chipKIT logo, CryptoMemory, CryptoRF, dsPIC, FlashFlex, flexPWR, HELDO, IGL00, JukeBlox, KeeLoq, Kleer, LANCheck, LinkMD, maXStylus, maXTouch, MediaLB, megaAVR, Microsemi, Microsemi logo, MOST, MOST logo, MPLAB, OptoLyzer, PackeTime, PIC, picoPower, PICSTART, PIC32 logo, PolarFire, Prochip Designer, QTouch, SAM-BA, SenGenuity, SpyNIC, SST, SST Logo, SuperFlash, Symmetricom, SyncServer, Tachyon, TimeSource, tinyAVR, UNI/O, Vectron, and XMEGA are registered trademarks of Microchip Technology Incorporated in the U.S.A. and other countries.

AgileSwitch, APT, ClockWorks, The Embedded Control Solutions Company, EtherSynch, FlashTec, Hyper Speed Control, HyperLight Load, IntelliMOS, Libero, motorBench, mTouch, Powermite 3, Precision Edge, ProASIC, ProASIC Plus, ProASIC Plus logo, Quiet-Wire, SmartFusion, SyncWorld, Temux, TimeCesium, TimeHub, TimePictra, TimeProvider, WinPath, and ZL are registered trademarks of Microchip Technology Incorporated in the U.S.A.

Adjacent Key Suppression, AKS, Analog-for-the-Digital Age, Any Capacitor, AnyIn, AnyOut, Augmented Switching, BlueSky, BodyCom, CodeGuard, CryptoAuthentication, CryptoAutomotive, CryptoCompanion, CryptoController, dsPICDEM, dsPICDEM.net, Dynamic Average Matching, DAM, ECAN, Espresso T1S, EtherGREEN, IdealBridge, In-Circuit Serial Programming, ICSP, INICnet, Intelligent Paralleling, Inter-Chip Connectivity, JitterBlocker, maxCrypto, maxView, memBrain, Mindi, MiWi, MPASM, MPF, MPLAB Certified logo, MPLIB, MPLINK, MultiTRAK, NetDetach, Omniscient Code Generation, PICDEM, PICDEM.net, PICkit, PICtail, PowerSmart, PureSilicon, QMatrix, REAL ICE, Ripple Blocker, RTAX, RTG4, SAM-ICE, Serial Quad I/O, simpleMAP, SimpliPHY, SmartBuffer, SMART-I.S., storClad, SQI, SuperSwitcher, SuperSwitcher II, Switchtec, SynchroPHY, Total Endurance, TSHARC, USBCheck, VariSense, VectorBlox, VeriPHY, ViewSpan, WiperLock, XpressConnect, and ZENA are trademarks of Microchip Technology Incorporated in the U.S.A. and other countries.

SQTP is a service mark of Microchip Technology Incorporated in the U.S.A.

The Adaptec logo, Frequency on Demand, Silicon Storage Technology, and Symmcom are registered trademarks of Microchip Technology Inc. in other countries.

GestIC is a registered trademark of Microchip Technology Germany II GmbH & Co. KG, a subsidiary of Microchip Technology Inc., in other countries.

All other trademarks mentioned herein are property of their respective companies.

© 2005-2021, Microchip Technology Incorporated, All Rights Reserved.

ISBN: 978-1-5224-7448-7

For information regarding Microchip's Quality Management Systems, please visit www.microchip.com/quality.



MICROCHIP

Worldwide Sales and Service

AMERICAS

Corporate Office
2355 West Chandler Blvd.
Chandler, AZ 85224-6199
Tel: 480-792-7200
Fax: 480-792-7277
Technical Support:
<http://www.microchip.com/support>
Web Address:
www.microchip.com

Atlanta

Duluth, GA
Tel: 678-957-9614
Fax: 678-957-1455

Austin, TX

Tel: 512-257-3370

Boston

Westborough, MA
Tel: 774-760-0087
Fax: 774-760-0088

Chicago

Itasca, IL
Tel: 630-285-0071
Fax: 630-285-0075

Dallas

Addison, TX
Tel: 972-818-7423
Fax: 972-818-2924

Detroit

Novi, MI
Tel: 248-848-4000

Houston, TX

Tel: 281-894-5983

Indianapolis

Noblesville, IN
Tel: 317-773-8323
Fax: 317-773-5453
Tel: 317-536-2380

Los Angeles

Mission Viejo, CA
Tel: 949-462-9523
Fax: 949-462-9608
Tel: 951-273-7800

Raleigh, NC

Tel: 919-844-7510

New York, NY

Tel: 631-435-6000

San Jose, CA

Tel: 408-735-9110
Tel: 408-436-4270

Canada - Toronto

Tel: 905-695-1980
Fax: 905-695-2078

ASIA/PACIFIC

Australia - Sydney
Tel: 61-2-9868-6733

China - Beijing
Tel: 86-10-8569-7000

China - Chengdu
Tel: 86-28-8665-5511

China - Chongqing
Tel: 86-23-8980-9588

China - Dongguan
Tel: 86-769-8702-9880

China - Guangzhou
Tel: 86-20-8755-8029

China - Hangzhou
Tel: 86-571-8792-8115

China - Hong Kong SAR
Tel: 852-2943-5100

China - Nanjing
Tel: 86-25-8473-2460

China - Qingdao
Tel: 86-532-8502-7355

China - Shanghai
Tel: 86-21-3326-8000

China - Shenyang
Tel: 86-24-2334-2829

China - Shenzhen
Tel: 86-755-8864-2200

China - Suzhou
Tel: 86-186-6233-1526

China - Wuhan
Tel: 86-27-5980-5300

China - Xian
Tel: 86-29-8833-7252

China - Xiamen
Tel: 86-592-2388138

China - Zhuhai
Tel: 86-756-3210040

ASIA/PACIFIC

India - Bangalore
Tel: 91-80-3090-4444

India - New Delhi
Tel: 91-11-4160-8631

India - Pune
Tel: 91-20-4121-0141

Japan - Osaka
Tel: 81-6-6152-7160

Japan - Tokyo
Tel: 81-3-6880-3770

Korea - Daegu
Tel: 82-53-744-4301

Korea - Seoul
Tel: 82-2-554-7200

Malaysia - Kuala Lumpur
Tel: 60-3-7651-7906

Malaysia - Penang
Tel: 60-4-227-8870

Philippines - Manila
Tel: 63-2-634-9065

Singapore
Tel: 65-6334-8870

Taiwan - Hsin Chu
Tel: 886-3-577-8366

Taiwan - Kaohsiung
Tel: 886-7-213-7830

Taiwan - Taipei
Tel: 886-2-2508-8600

Thailand - Bangkok
Tel: 66-2-694-1351

Vietnam - Ho Chi Minh
Tel: 84-28-5448-2100

EUROPE

Austria - Wels
Tel: 43-7242-2244-39
Fax: 43-7242-2244-393

Denmark - Copenhagen
Tel: 45-4485-5910
Fax: 45-4485-2829

Finland - Espoo
Tel: 358-9-4520-820

France - Paris
Tel: 33-1-69-53-63-20
Fax: 33-1-69-30-90-79

Germany - Garching
Tel: 49-8931-9700

Germany - Haan
Tel: 49-2129-3766400

Germany - Heilbronn
Tel: 49-7131-72400

Germany - Karlsruhe
Tel: 49-721-625370

Germany - Munich
Tel: 49-89-627-144-0
Fax: 49-89-627-144-44

Germany - Rosenheim
Tel: 49-8031-354-560

Israel - Ra'anana
Tel: 972-9-744-7705

Italy - Milan
Tel: 39-0331-742611
Fax: 39-0331-466781

Italy - Padova
Tel: 39-049-7625286

Netherlands - Drunen
Tel: 31-416-690399
Fax: 31-416-690340

Norway - Trondheim
Tel: 47-7288-4388

Poland - Warsaw
Tel: 48-22-3325737

Romania - Bucharest
Tel: 40-21-407-87-50

Spain - Madrid
Tel: 34-91-708-08-90
Fax: 34-91-708-08-91

Sweden - Gothenberg
Tel: 46-31-704-60-40

Sweden - Stockholm
Tel: 46-8-5090-4654

UK - Wokingham
Tel: 44-118-921-5800
Fax: 44-118-921-5820