Product / Process Change Notification



N° 2020-153-A

Dear Customer,

please find attached our INFINEON Technologies PCN:

Several Changes Affecting Product TLS835D2EL VSE

Important information for your attention:

- Please respond to this PCN by indicating your decision on the approval form, sign it and return to your sales partner before 2021-01-11.
- Infineon aligns with the widely-recognized JEDEC STANDARD "JESD46", which stipulates: "Lack of acknowledgement of the PCN within 30 days constitutes acceptance of the change." Notwithstanding the aforesaid individual agreements shall prevail.

Your prompt reply will help Infineon Technologies to assure a smooth and well-executed transition. If Infineon does not hear from your side by the due date, we will assume your full acceptance to this proposed change and its implementation.

Your attention and response to this matter is greatly appreciated.

Infineon Technologies AG Postal Address Headquarters: Am Campeon 1-15, D-85579 Neubiberg, Phone +49 (0)89 234-0 Chairman of the Supervisory Board: Dr. Wolfgang Eder

Management Board: Dr. Reinhard Ploss (CEO), Dr. Helmut Gassel, Jochen Hanebeck, Dr. Sven Schneider

Registered Office: Neubiberg

Commercial Register: München HRB 126492

Product / Process Change Notification



N° 2020-153-A		
Products affected	Please refer to attached affected product	: list 1_cip20153_A
Detailed Change Information		
Subject:	Several Changes Affecting Product TLS83	5D2EL VSE
Reason / Motivation:	Expansion of wafer production and wafer test to assure continuity of supply and enable flexible manufacturing. Rerouting including datasheet update according harmonization of OPTIREG [™] linear voltage regulator family in Kulim.	
Description	Old	New
PROCESS - WAFER PRODUCTION: Move all or parts of production to a different wafer fab site.	Infineon Technologies Austria AG, Villach, Austria	Infineon Technologies Austria AG, Villach, Austria and Infineon Technologies (Kulim) Sdn. Bhd., Kulim, Malaysia
TEST FLOW: Move of all or part of electrical wafer test and/or final test to a different test site.		Infineon Technologies Austria AG, Villach, Austria and Infineon Technologies (Kulim) Sdn. Bhd., Kulim, Malaysia
DATA SHEET: Change of datasheet parameters/electrical specification (min./mac./typ. Values) and/or AC/DC specification	Dov: 1.1	Rev. 1.2
DESIGN: Design changes in routing.	No change	Changes in passive elements and metal mask
Product Identification	Wafer lot numbers from Villach start with VExxxxxx. Wafer lot numbers from Kulim with 1Exxxxxx Traceability assured via date code. No change in SP ordering number	
Anticipated Impact of Change	Based on the qualification performed, Infineon does not expect any negative impact on quality, function and reliability. No change in fit, form and function expected.	
	DeQuMa-ID(s): SEM-DS-01 / SEM-DE-02	/ JEIVI-F W-13 / JEIVI-IF-UI

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Attachments

1_cip20153_A	affected product list
3_cip20153_A	customer information package
4_cip20153_A	datasheet

Time Schedule

Final qualification report	available
First samples available	on request
Intended start of delivery [1]	2021-06-01
Last Order Date (LOD) [2]	2021-06-01
Last Delivery Date (LDD) [3]	2022-06-01

[1] provided date or earlier after customer approval

[2] Last time buy volume to be placed latest until LOD

[3] Delivery of new product can be earlier (see Intended start of delivery) and depends on approval

If you have any questions, please do not hesitate to contact your local Sales Office.