



Customer Information Notification

202101001I : i.MX RT1015/RT1020 Errata Rev 2 Update, i.MX RT1024 Errata Rev 1 Update and i.MX RT1050 Errata Rev 2.3 Update

Note: This notice is NXP Company Proprietary.

Issue Date: Jan 21, 2021 **Effective date:**Jan 22, 2021

Dear Emma Tempest,

Here is your personalized notification about a NXP general announcement. For detailed information we invite you to [view this notification online](#)

Change Category

- Wafer Fab Process
- Assembly Process
- Product Marking
- Test Process
- Design
- Wafer Fab Materials
- Assembly Materials
- Mechanical Specification
- Test Equipment
- Errata
- Wafer Fab Location
- Assembly Location
- Packing/Shipping/Labeling
- Test Location
- Electrical spec./Test coverage
- Firmware
- Other



PCN Overview Description

NXP Semiconductors announces chip errata update to revision 2 for i.MX RT1015&RT1020, chip errata update to revision 1 for i.MX RT1024 and chip errata update to revision 2.3 for i.MX RT1050. The revision history included in the updated documents provides a detailed description of the changes. Changes are summarized below.

i.MX RT1015/RT1020/RT1024/RT1050 Chip Errata has the following common changes:

? Added the following errata:

? ERR050143: CCM: SoC will enter low power mode before the Arm CPU executes WFI when improper low power sequence is used

? Removed the following errata:

? ERR007265(replace ERR007265 with ERR050143): CCM: When improper low-power sequence is used, the SoC enters low power mode before the Arm core executes WFI

i.MX RT1015/RT1020/RT1024 Chip Errata has the following additional changes:

? Removed the following errata:

? ERR011150: SAI: Internally generated receive or transmit BCLK cannot be re-enabled if it is first disabled when RCR2[DIV] or TCR2[DIV] > 0

? ERR011096: SAI: The internal bit clock cannot be generated when BCI = 1

i.MX RT1015/RT1020/RT1050 Chip Errata has the following additional change:
? Updated the Figure 1, "Revision Level to Part Marking Cross-Reference"

i.MX RT1020/RT1024/RT1050 Chip Errata has the following additional change:
? Added the following errata:
? ERR050577: SEMC: auto-refresh can fail to be triggered during long back to back write(or read)

The i.MX RT1015 errata is attached to this notice, and can be found at:
https://www.nxp.com/products/processors-and-microcontrollers/arm-microcontrollers/i-mx-rt-crossover-mcus/i-mx-rt1015-crossover-mcu-with-arm-cortex-m7-core:i.MX-RT1015?tab=Documentation_Tab&linkline=Errata

The i.MX RT1020 errata is attached to this notice, and can be found at:
https://www.nxp.com/products/processors-and-microcontrollers/arm-microcontrollers/i-mx-rt-crossover-mcus/i-mx-rt1020-crossover-mcu-with-arm-cortex-m7-core:i.MX-RT1020?tab=Documentation_Tab&linkline=Errata

The i.MX RT1024 errata is attached to this notice, and can be found at:
https://www.nxp.com/products/processors-and-microcontrollers/arm-microcontrollers/i-mx-rt-crossover-mcus/i-mx-rt1024-crossover-mcu-with-arm-cortex-m7-core:i.MX-RT1024?tab=Documentation_Tab&linkline=Errata

The i.MXRT1050 errata is attached to this notice, and can be found at:
https://www.nxp.com/products/processors-and-microcontrollers/arm-microcontrollers/i-mx-rt-crossover-mcus/i-mx-rt1050-crossover-mcu-with-arm-cortex-m7-core:i.MX-RT1050?tab=Documentation_Tab&linkline=Errata

Reason

The errata was added or removed for additional technical clarification on some device features.

Identification of Affected Products

Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No Impact on form, fit, function, reliability or quality

Additional information

Additional documents: [view online](#)

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

Name technical support
e-mail address tech.support@nxp.com

At NXP Semiconductors we are constantly striving to improve our product and processes to ensure they reach the highest possible Quality Standards. Customer Focus, Passion to Win.

NXP Quality Management Team.

About NXP Semiconductors

NXP Semiconductors N.V. (NASDAQ: NXPI) provides High Performance Mixed Signal and Standard Product solutions that leverage its leading RF, Analog, Power Management, Interface, Security and Digital Processing expertise. These innovations are used in a wide range of automotive, identification, wireless infrastructure, lighting, industrial, mobile, consumer and computing applications.

You have received this email because you are a designated contact or subscribed to NXP Quality Notifications. NXP shall not be held liable if this Notification is not correctly distributed within your organization.

This message has been automatically distributed. Please do not reply .

NXP Semiconductors
High Tech Campus, 5656 AG Eindhoven, The Netherlands

© 2006- 2021 NXP Semiconductors. All rights reserved.