

Secure 2 click

PID: MIKROE-2760

Ensure all your projects are secure

Secure 2 click carries the <u>ATAES132A</u>, a cryptographic coprocessor with secure hardware-based key storage from Microchip. The click is designed to run on either 3.3V or 5V power supply. Secure 2 click communicates with the target microcontroller over SPI and I2C interface, with additional functionality provided by the INT pin on the mikroBUS[™] line.

NOTE: The click comes with stacking headers which allow you to combine it with other clicks more easily by using just one mikroBUS™ socket.



ATAES132A features

The ATAES132A is a high-security, Serial Electrically-Erasable and Programmable Read-Only Memory (Serial EEPROM) providing both authentication and confidential nonvolatile data storage capabilities. Access restrictions for the 16 user zones are independently configured, and any key can be used with any zone. In addition, keys can be used for standalone authentication.



The AES-128 cryptographic engine operates in AES-CCM mode to provide authentication, stored data encryption/decryption, and Message Authentication Codes. Data encryption/decryption can be performed for internally stored data or for small external data packets, depending upon the configuration. Data encrypted by one ATAES132A device can be decrypted by another, and vice versa.

Specifications

Туре	EEPROM
On-board modules	ATAES132A - a cryptographic coprocessor with secure hardware-based key storage from Microchip
Key Features	Crypto element device with secure hardware-based key storage, 512 bit OTP (One Time Programmable) Bits for Fixed Information
Interface	I2C,SPI
Input Voltage	3.3V or 5V
Click board size	M (42.9 x 25.4 mm)

Pinout diagram

This table shows how the pinout on Secure 2 click corresponds to the pinout on the mikroBUS $^{\text{TM}}$ socket (the latter shown in the two middle columns).

Notes	Pin	♥ ♥ mikro™ • • • BUS				Pin	Notes
	NC	1	AN	PWM	16	NC	
	NC	2	RST	INT	15	AUTH	Auth signaling
Chip select	CS	3	CS	TX	14	NC	
SPI clock	SCK	4	SCK	RX	13	NC	
Slave data out for SPI	SDO	5	MISO	SCL	12	SCL	I2C clock
Slave data in for SPI	SDI	6	MOSI	SDA	11	SDA	I2C data
Power supply	+3.3V	7	3.3V	5V	10	+5V	Power supply
Ground	GND	8	GND	GND	9	GND	Ground