



60V +175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

Ensures More Reliable and Robust End Application

Lead-Free Finish; RoHS Compliant (Notes 1 & 2) Halogen and Antimony Free. "Green" Device (Note 3) For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/guality/product-definitions/

Low RDS(ON) - Minimizes On State Losses

Rated to +175°C - Ideal for High Ambient Temperature

100% Unclamped Inductive Switching (UIS) Test in Production -

An Automotive-Compliant Part is Available Under Separate

Terminals: Finish - Matte Tin Annealed over Copper Leadframe.

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Case Material: Molded Plastic, "Green" Molding Compound.

UL Flammability Classification Rating 94V-0

Moisture Sensitivity: Level 1 per J-STD-020

Terminal Connections: See Diagram Below

Weight: 0.097 grams (Approximate)

Solderable per MIL-STD-202, Method 208 (63)

Features

Environments

High Conversion Efficiency

Datasheet (DMTH61M8LPSQ)

Mechanical Data

Case: PowerDI®5060-8

D

S

Internal Schematic

G

G

Low Input Capacitance Fast Switching Speed

Product Summary

BV _{DSS}	Rds(on) Max	I⊳ Max Tc = +25°C
60V	1.6mΩ @ V _{GS} = 10V	225A
607	2.8mΩ @ V _{GS} = 4.5V	180A

Description and Applications

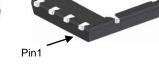
This MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Engine Management Systems
- **Body Control Electronics**
- **DC-DC Converters**

Site1:



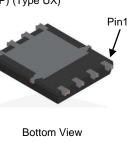
Top View

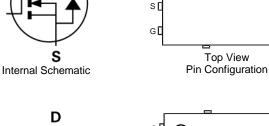


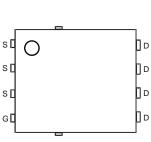
Bottom View

Site2









Top View

Pin Configuration

Top View

ΠD

ПD

ΠD

Πр

Ordering Information (Note 4)

Part Number	Case	Packaging
DMTH61M8LPS-13	PowerDI5060-8 (Type K)	2,500 / Tape & Reel
DMTH61M8LPS-13	PowerDI5060-8 (SWP) (Type UX)	2,500 / Tape & Reel

1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied. Notes:

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free. 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and

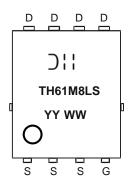
<1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

PowerDI is a registered trademark of Diodes Incorporated.



Marking Information



 \bigcirc + = Manufacturer's Marking TH61M8LS = Product Type Marking Code YYWW or $\overleftarrow{YY}WW$ = Date Code Marking YY or \overleftarrow{YY} = Year (ex: 20 = 2020) WW = Week (01 to 53)

Maximum Ratings (@T_C = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage		Vdss	60	V
Gate-Source Voltage		Vgss	±20	V
	T _C = +25°C	- I _D	225	A
Continuous Drain Current, $V_{GS} = 10V$ (Note 6)	Tc = +100°C		160	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		IDM	900	A
Maximum Continuous Body Diode Forward Current (Note 6)	Tc = +25°C	ls	225	A
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)		Ism	900	A
Avalanche Current, L = 1mH		las	34.8	А
Avalanche Energy, L = 1mH		Eas	605	mJ

Thermal Characteristics (@T_C = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T _A = +25°C	PD	3.2	W
Thermal Resistance, Junction to Ambient (Note 5)		R _{0JA}	46	°C/W
Total Power Dissipation (Note 6)	Tc = +25°C	PD	187.5	W
Thermal Resistance, Junction to Case (Note 6)		R _{θJC}	0.8	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +175	°C

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate. 6. Thermal resistance from junction to soldering point (on the exposed drain pad).

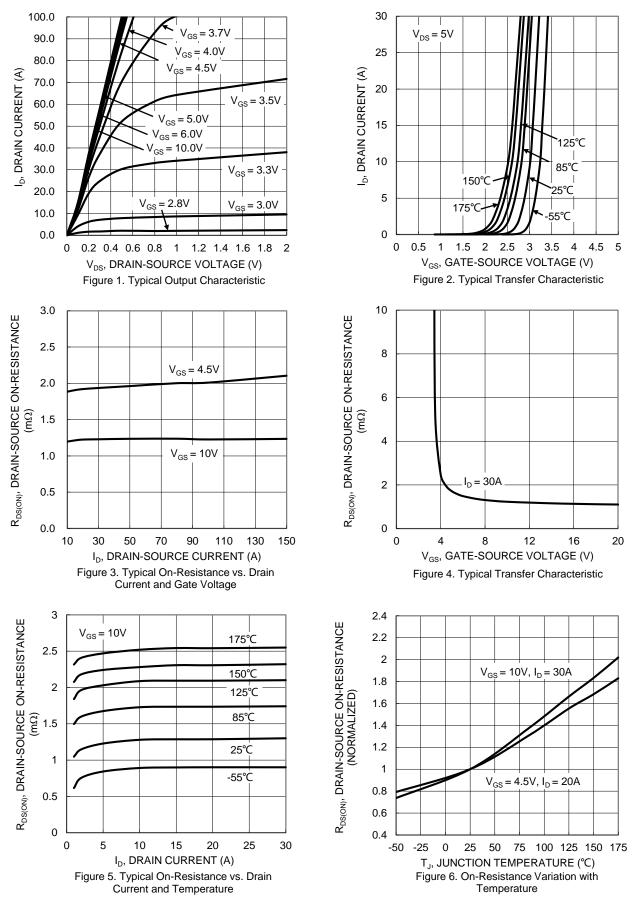


Electrical Characteristics (@Tc = +25°C, unless otherwise specified.)

		-				
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BVDSS	60	—	—	V	$V_{GS} = 0V, I_D = 250 \mu A$
Zero Gate Voltage Drain Current	IDSS		—	1	μA	$V_{DS} = 48V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}		—	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	Vgs(th)	1	—	3	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$
Static Drain-Source On-Resistance	Descent		1.2	1.6	mΩ	V _{GS} = 10V, I _D = 30A
Static Drain-Source Off-Resistance	Rds(on)	_	1.9	2.8	11122	V _{GS} = 4.5V, I _D = 20A
Diode Forward Voltage	V _{SD}	_	0.7	1.2	V	$V_{GS} = 0V, I_{S} = 20A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss		8320	-		V _{DS} = 30V, V _{GS} = 0V, f = 1MHz
Output Capacitance	Coss	_	2298	_	pF	
Reverse Transfer Capacitance	Crss	_	157	_		
Gate Resistance	Rg	_	3	—	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	53.3	_		
Total Gate Charge (V _{GS} = 10V)	Qg	_	115.5	_	-0	V _{DS} = 30V, I _D = 30A
Gate-Source Charge	Q _{gs}		27.8	_	nC	
Gate-Drain Charge	Qgd	_	16.5	_		
Turn-On Delay Time	t _{D(ON)}		10.3	_		V _{DD} = 30V, V _{GS} = 10V,
Turn-On Rise Time	tR		23.9	_	ns	
Turn-Off Delay Time	tD(OFF)		108.3	_		$I_D = 30A, R_g = 3\Omega$
Turn-Off Fall Time	tF	-	51.7	—		
Body Diode Reverse Recovery Time	trr	_	64	_	ns	
Body Diode Reverse Recovery Charge	Q _{RR}		124	—	nC	IF = 30A, di/dt = 100A/µs

Notes:7. Short duration pulse test used to minimize self-heating effect.8. Guaranteed by design. Not subject to product testing.



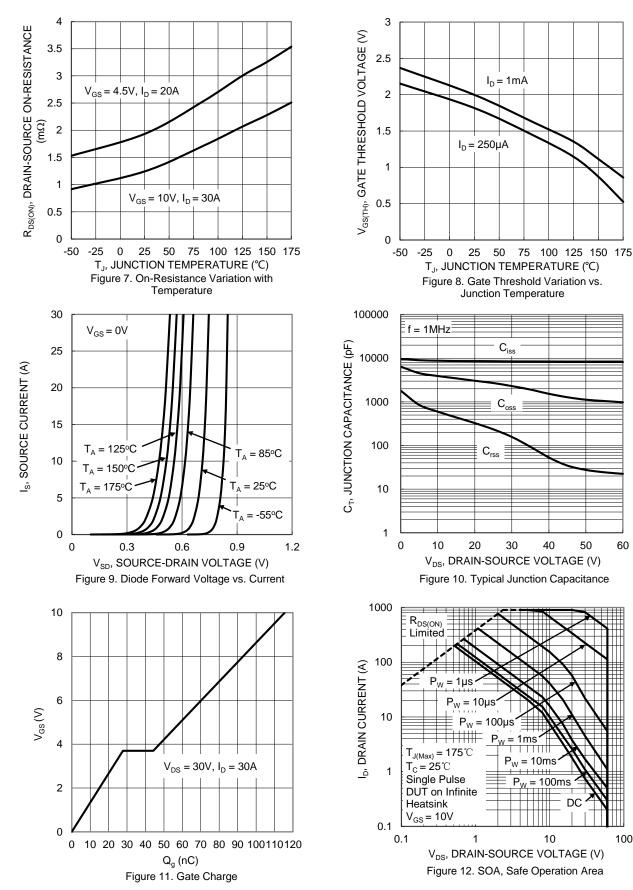




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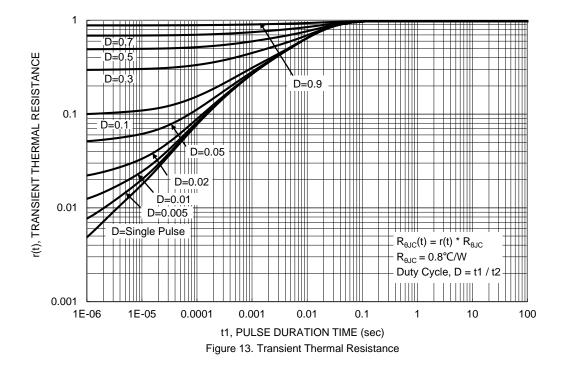
DC

60



100



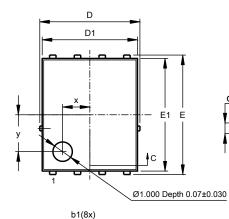




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version. Site1:

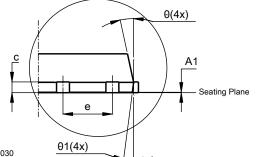
PowerDI5060-8 (Type K)



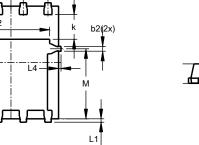
e/2

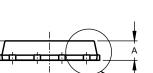
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D2



DETAIL A





DETAIL A

	PowerDI5060-8 (Type K)			
Dim	Min	Max	Тур	
Α	0.90	1.10	1.00	
A1	0	0.05	0.02	
b	0.33	0.51	0.41	
b1	0.300	0.366	0.333	
b2	0.20	0.35	0.25	
С	0.23	0.33	0.277	
D	5	.15 BS(0	
D1	4.85	4.95	4.90	
D2	-	-	3.98	
E	6	6.15 BS0	0	
E1	5.75	5.85	5.80	
E2	3.56	3.725	3.66	
е	1	1.27BSC	~	
k	-	-	1.27	
L	0.51	0.71	0.61	
La	0.51	0.675	0.61	
L1	0.05	0.20	0.175	
L4	-	-	0.125	
М	3.50	3.71	3.605	
x	-	-	1.400	
У	-	-	1.900	
θ	10°	12°	11°	
θ1	6°	8°	7°	
All	All Dimensions in mm			

Site2:

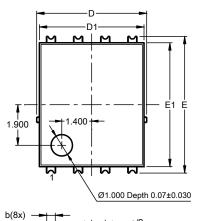
La

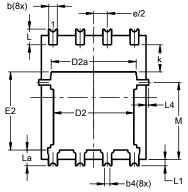
b(8x)

L

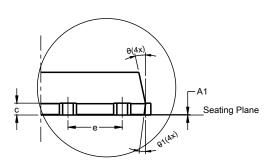
T

E2

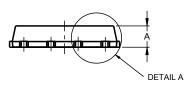




PowerDI5060-8 (SWP) (Type UX)



DETAIL A



Pov	PowerDI5060-8 (SWP) (Type UX)			
Dim	Min	Max	Тур	
Α	0.90	1.10	1.00	
A1	0	0.05		
b	0.30	0.50	0.41	
b2	0.20	0.35	0.25	
b4	().25REF	-	
С	0.230	0.330	0.277	
D	5	.15 BS0	C	
D1	4.70	5.10	4.90	
D2	3.56	3.96	3.76	
D2a	3.78	4.18	3.98	
E	6	.40 BS0	0	
E1	5.60	6.00	5.80	
E2	3.46	3.86	3.66	
E2a	4.195	4.595	4.395	
е	1	.27BSC)	
k	1.05			
L	0.635	0.835	0.735	
La	0.635	0.835	0.735	
L1	0.200	0.400	0.300	
L1a	0.050REF			
L4	0.025	0.225	0.125	
Μ	3.205	4.005	3.605	
θ	10°	12°	11°	
θ1	6°	8°	7°	
All Dimensions in mm				

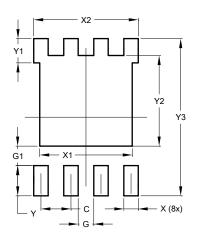


Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

Site1:

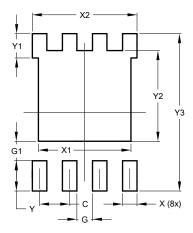
PowerDI5060-8 (Type K)



Dimensions	Value (in mm)	
С	1.270	
G	0.660	
G1	0.820	
Х	0.610	
X1	3.910	
X2	4.420	
Y	1.270	
Y1	1.020	
Y2	3.810	
Y3	6.610	

Site2:

PowerDI5060-8 (SWP) (Type UX)



Dimensions	Value (in mm)	
С	1.270	
G	0.660	
G1	0.820	
Х	0.610	
X1	4.100	
X2	4.420	
Y	1.270	
Y1	1.020	
Y2	3.810	
Y3	6.610	



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