



## Customer Information Notification

2018090171

**Issue Date:** 22-Dec-2018

**Effective Date:** 23-Dec-2018

Dear *Emma Tempest*,

Here's your personalized quality information concerning products Premier Farnell PLC purchased from NXP.

For detailed information we invite you to [view this notification online](#)

**This notice is NXP Company Proprietary.**



### Change Category

<input type="checkbox"/> Wafer Fab Process	<input type="checkbox"/> Assembly Process	<input type="checkbox"/> Product Marking	<input type="checkbox"/> Test Location	<input type="checkbox"/> Design
<input type="checkbox"/> Wafer Fab Materials	<input type="checkbox"/> Assembly Materials	<input type="checkbox"/> Mechanical Specification	<input type="checkbox"/> Test Process	<input type="checkbox"/> Errata
<input type="checkbox"/> Wafer Fab Location	<input type="checkbox"/> Assembly Location	<input type="checkbox"/> Packing/Shipping/Labeling	<input type="checkbox"/> Test Equipment	<input checked="" type="checkbox"/> Electrical spec./Test coverage
<input type="checkbox"/> Firmware	<input type="checkbox"/> Other			

MC9S12ZVL Family  
Reference Manual and  
Datasheet Update from  
Rev2.41 to Rev2.47

### Description

NXP Semiconductors announces a Reference Manual and Datasheet update for the MC9S12ZVL Family from revision 2.41 to revision 2.47.

All revisions are included in the CIN/updated document. The revision history included in the updated document provides a detailed description of the changes.

Main changes are summarized below.

1. Added version 2.1 of Chapter 1, "Device Overview MC9S12ZVL-Family"
2. Updated Appendix A, "MCU Electrical Specifications"
  - VSUP max 28.5V for 1h over lifetime. Changed Section Table A-6, "Operating Conditions"
  - Changed item 1 Table A-6 VSUP min 3.5V
  - Added W Temperature Option to Appendix N, "Ordering Information"
  - Added "Date Codes for fully trimmed ACLK Parts"

3. Updated Chapter 2, Port Integration Module 2.11
4. Other change detail please refer to Reference Manual and Datasheet revision history.

The MC9S12ZVL Family Reference Manual and Datasheet revision 2.47 is attached to this notice, and can be found at:

[https://www.nxp.com/products/processors-and-microcontrollers/additional-processors-and-mcus/8-16-bit-mcus/16-bit-s12-magniv/s12zvl-mixed-signal-mcu-for-automotive-industrial-lin-applications:S12ZVL?tab=Documentation\\_Tab](https://www.nxp.com/products/processors-and-microcontrollers/additional-processors-and-mcus/8-16-bit-mcus/16-bit-s12-magniv/s12zvl-mixed-signal-mcu-for-automotive-industrial-lin-applications:S12ZVL?tab=Documentation_Tab)

Corresponding ZVEI Delta Qualification Matrix ID: SEM-DS-02/SEM-DS-03

**Reason**

The Reference Manual and Datasheet has been updated to correct errors and / or provide additional technical clarification on some device features.

**Identification of Affected Products**

Product identification does not change

**Anticipated Impact on Form, Fit, Function, Reliability or Quality**

No impact on form, fit, function, reliability or quality.

**Data Sheet Revision**

A new datasheet will be issued

**Additional information**

Affected products and sales history information: see attached file

Additional documents: [view online](#)

**Contact and Support**

For all inquiries regarding the ePCN tool application or access issues, please [contact NXP "Global Quality Support Team"](#).

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

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**Position** Product Engineer  
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At NXP Semiconductors we are constantly striving to improve our product and processes to ensure they reach the highest possible Quality Standards.

Customer Focus, Passion to Win.

NXP Quality Management Team.

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