

XinaBox Datasheet IP03 - SWD/JTAG/SPI/USB Interface



Overview

This xCHIP is an interface module and is equipped to interface with SWD (https://en.wikipedia.org/wiki/JTAG#Serial_Wire_Debug), JTAG (<https://en.wikipedia.org/wiki/JTAG>) and SPI (https://en.wikipedia.org/wiki/Serial_Peripheral_Interface_Bus) programmers to program other xCHiPs via \boxtimes PDI and \boxtimes BUS. Voltage regulation from USB power is provided by on-board AP2114 LDO, which features 1 A continuous current output, good output regulation accuracy and short circuit protection.

Product Highlights

- Multifunctional Programming Interface Headers
- SWD/JTAG/SPI Interface
- Facilitates Low and High Level Programming
- USB Data Throughput on \boxtimes PDI
- USB Power Supply

Specifications

AP2114

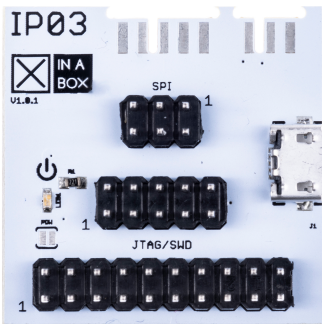
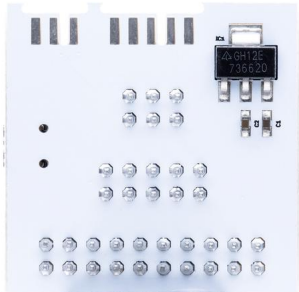
- Output Voltage Accuracy: $\pm 1.5\%$
- Output Current: 1A
- Fold-back Short Current Protection: 50mA
- Low Quiescent Current: 60 μ A
- Low Output Noise: 30 μ V RMS
- OTSD Protection
- Operating Temperature Range: -40°C to 85°C

External Links

GitHub

- IP03 on GitHub (<https://github.com/xinabox/IP03>)

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Front	
	
Back	
\boxtimes CHIP	
Main Category	Interface
Sub Category	Programmer
Introduced	1 January 2017
Current version	1.0.1
Current version date	1 January 2017
Dimensions	
Size	2x2U (32x32mm)
Weight	5.6 g
Height	11.3/8.0/1.6 mm
Non- \boxtimes BUS Connections	
North	\boxtimes BUS & \boxtimes PDI
East	USB Micro B
Main Chip Set	
Main Chip	AP2114