# MOSFET - Power, N-Channel, SUPERFET<sup>®</sup> III, FRFET<sup>®</sup> 650 V, 24 A, 150 m $\Omega$

# NTMT150N65S3HF

# Description

SUPERFET III MOSFET is ON Semiconductor's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This advanced technology is tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate.

Consequently, SUPERFET III MOSFET is very suitable for the various power systems for miniaturization and higher efficiency. SUPERFET III FRFET MOSFET's optimized reverse recovery performance of body diode can remove additional component and improve system reliability.

### **Features**

- 700 V @ T<sub>J</sub> = 150°C
- Typ.  $R_{DS(on)} = 121 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ. Q<sub>g</sub> = 43 nC)
- Low Effective Output Capacitance (Typ. Coss(eff.) = 400 pF)
- 100% Avalanche Tested
- These Devices are Pb-Free and are RoHS Compliant

### **Applications**

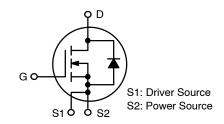
- Telecom / Server Power Supplies
- Industrial Power Supplies
- UPS / Solar
- Lighting



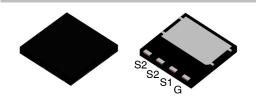
# ON Semiconductor®

### www.onsemi.com

V <sub>DSS</sub>	R <sub>DS(ON)</sub> MAX	I <sub>D</sub> MAX
650 V	150 m $\Omega$ @ 10 V	24 A



**POWER MOSFET** 



Power88 PQFN4 8X8 2P CASE 483AP

# **MARKING DIAGRAM**

NTMT150 N65S3HF AWLYWW

NTMT150N65S3HF = Specific Device Code A = Assembly Location

WL = Wafer Lot Y = Year WW = Work Week

### **ORDERING INFORMATION**

See detailed ordering and shipping information on page 2 of this data sheet.

# **ABSOLUTE MAXIMUM RATINGS** ( $T_C = 25^{\circ}C$ , Unless otherwise noted)

Symbol	Parameter	Value	Unit		
$V_{DSS}$	Drain to Source Voltage		650	V	
$V_{GSS}$	Gate to Source Voltage – DC		±30	V	
		- AC (f > 1 Hz)	±30		
I <sub>D</sub>	Drain Current	– Continuous (T <sub>C</sub> = 25°C)	24	А	
		- Continuous (T <sub>C</sub> = 100°C)	15.2		
I <sub>DM</sub>	Drain Current	- Pulsed (Note 1)	60	А	
E <sub>AS</sub>	E <sub>AS</sub> Single Pulsed Avalanche Energy (Note 2)  I <sub>AS</sub> Avalanche Current (Note 2)  E <sub>AR</sub> Repetitive Avalanche Energy (Note 1)  dv/dt MOSFET dv/dt  Peak Diode Recovery dv/dt (Note 3)		275	mJ	
I <sub>AS</sub>			3.8	А	
E <sub>AR</sub>			1.92	mJ	
dv/dt			100	V/ns	
			50		
$P_{D}$	Power Dissipation	(T <sub>C</sub> = 25°C)	192	W	
		- Derate Above 25°C	1.54	W/°C	
T <sub>J</sub> , T <sub>STG</sub>	T <sub>J</sub> , T <sub>STG</sub> Operating and Storage Temperature Range T <sub>L</sub> Maximum Lead Temperature for Soldering, 1/8" from Case for 5 seconds		-55 to +150	°C	
TL			300	°C	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 1. Repetitive rating: pulse–width limited by maximum junction temperature. 
  2.  $I_{AS}=3.8$  A,  $R_{G}=25$   $\Omega$ , starting  $T_{J}=25^{\circ}C$ . 
  3.  $I_{SD}\leq 12$  A, di/dt  $\leq 200$  A/ $\mu$ s,  $V_{DD}\leq 400$  V, starting  $T_{J}=25^{\circ}C$ .

## THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
$R_{ heta JC}$	Thermal Resistance, Junction to Case, Max.	0.65	°C/W
$R_{ heta JA}$	Thermal Resistance, Junction to Ambient, Max., (Note 4)	45	

<sup>4.</sup> Device on 1 in<sup>2</sup> 2-oz copper pad on 1.5 x 1.5 in. board of FR-4 material.

# PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Part Number Top Marking		Reel Size	Tape Width	Shipping <sup>†</sup>
NTMT150N65S3HF	NTMT150N65S3HF	Power88	13″	13.3 mm	3000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
OFF CHARACT	ERISTICS		•	•		
BV <sub>DSS</sub>	Drain to Source Breakdown Voltage	$V_{GS} = 0 \text{ V, } I_D = 1 \text{ mA, } T_J = 25^{\circ}\text{C}$	650			V
		V <sub>GS</sub> = 0 V, I <sub>D</sub> = 1 mA, T <sub>J</sub> = 150°C	700			V
$\Delta BV_{DSS}/\Delta T_{J}$	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 10 mA, Referenced to 25°C		0.62		V/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 650 V, V <sub>GS</sub> = 0 V			10	μΑ
		V <sub>DS</sub> = 520 V, T <sub>C</sub> = 125°C		10		
I <sub>GSS</sub>	Gate to Body Leakage Current	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$			±100	nA
ON CHARACTE	RISTICS					
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 0.54 \text{ mA}$	3.0		5.0	V
R <sub>DS(on)</sub>	Static Drain to Source On Resistance	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 12 A		121	150	mΩ
9FS	Forward Transconductance	V <sub>DS</sub> = 20 V, I <sub>D</sub> = 12 A		14		S
DYNAMIC CHAI	RACTERISTICS				-	
C <sub>iss</sub>	Input Capacitance	.,		1985		pF
C <sub>oss</sub>	Output Capacitance	$V_{DS} = 400 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		40		pF
C <sub>oss(eff.)</sub>	Effective Output Capacitance	V <sub>DS</sub> = 0 V to 400 V, V <sub>GS</sub> = 0 V		400		pF
C <sub>oss(er.)</sub>	Energy Related Output Capacitance	V <sub>DS</sub> = 0 V to 400 V, V <sub>GS</sub> = 0 V		71		pF
Q <sub>g(tot)</sub>	Total Gate Charge at 10 V	V <sub>DS</sub> = 400 V, I <sub>D</sub> = 12 A, V <sub>GS</sub> = 10 V (Note 5)		43		nC
Q <sub>gs</sub>	Gate to Source Gate Charge			13		nC
$Q_{gd}$	Gate to Drain "Miller" Charge			17		nC
ESR	Equivalent Series Resistance	f = 1 MHz		3.1		Ω
WITCHING CH	IARACTERISTICS					
t <sub>d(on)</sub>	Turn-On Delay Time			24		ns
t <sub>r</sub>	Turn-On Rise Time	$V_{DD} = 400 \text{ V}, I_D = 12 \text{ A}, V_{GS} = 10 \text{ V}$		12		ns
t <sub>d(off)</sub>	Turn-Off Delay Time	$R_g = 4.7 \Omega$ (Note 5)		60		ns
t <sub>f</sub>	Turn-Off Fall Time	1		3.1		ns
SOURCE-DRAII	N DIODE CHARACTERISTICS					
I <sub>S</sub>	Maximum Continuous Source to Drain Diode Forward Current				24	Α
I <sub>SM</sub>	Maximum Pulsed Source to Drain Diode Forward Current				60	Α
V <sub>SD</sub>	Source to Drain Diode Forward Voltage	V <sub>GS</sub> = 0 V, I <sub>SD</sub> = 12 A			1.3	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>DD</sub> = 400 V, I <sub>SD</sub> = 12 A,		80		ns
Q <sub>rr</sub>	Reverse Recovery Charge	dl <sub>F</sub> /dt = 100 A/μs		285		nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

5. Essentially independent of operating temperature typical characteristics.

### TYPICAL CHARACTERISTICS

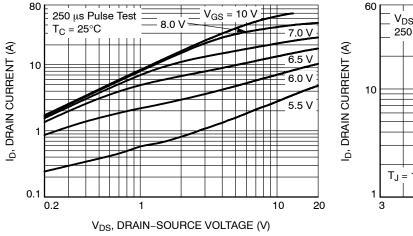


Figure 1. On-Region Characteristics

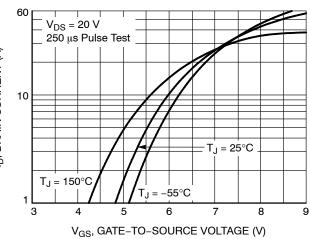


Figure 2. Transfer Characteristics

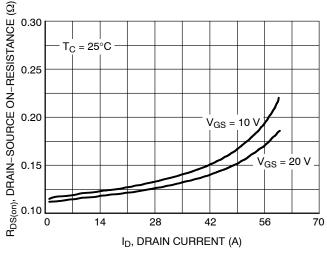


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

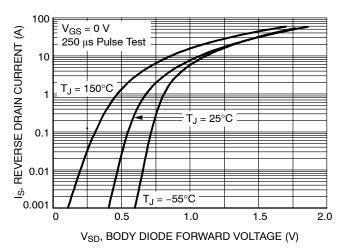


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

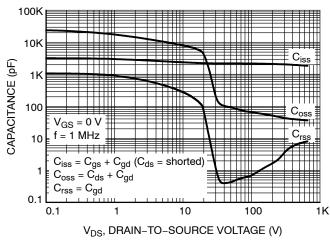


Figure 5. Capacitance Characteristics

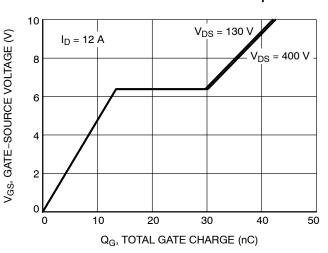


Figure 6. Gate Charge Characteristics

## **TYPICAL CHARACTERISTICS**

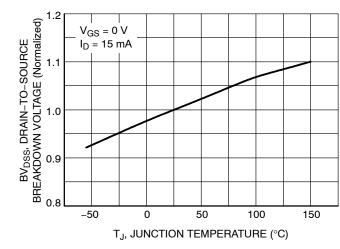


Figure 7. Breakdown Voltage Variation vs. Temperature

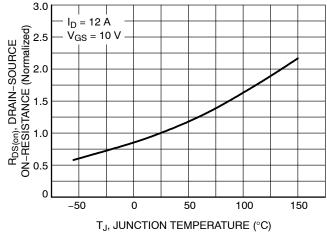


Figure 8. On–Resistance Variation vs. Temperature

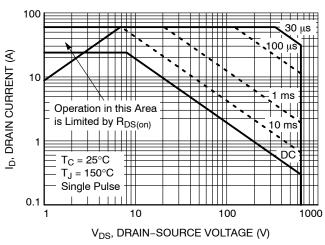


Figure 9. Maximum Safe Operating Area

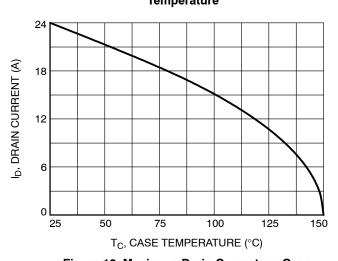


Figure 10. Maximum Drain Current vs. Case Temperature

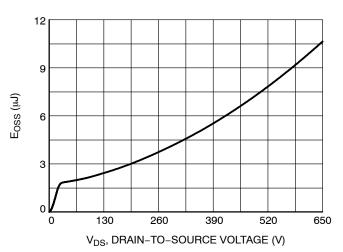


Figure 11. E<sub>OSS</sub> vs. Drain-to-Source Voltage

# **TYPICAL CHARACTERISTICS**

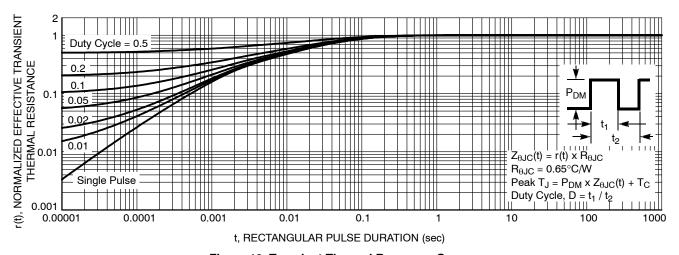


Figure 12. Transient Thermal Response Curve

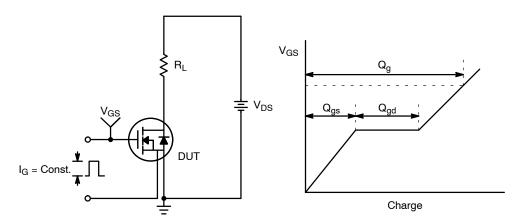


Figure 13. Gate Charge Test Circuit & Waveform

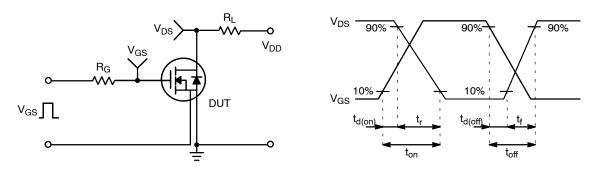


Figure 14. Resistive Switching Test Circuit & Waveforms

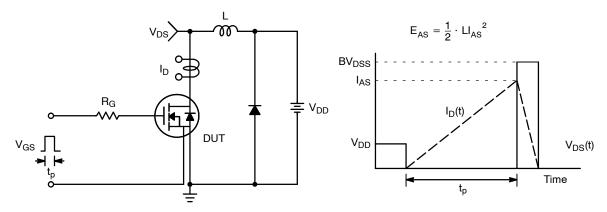


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms

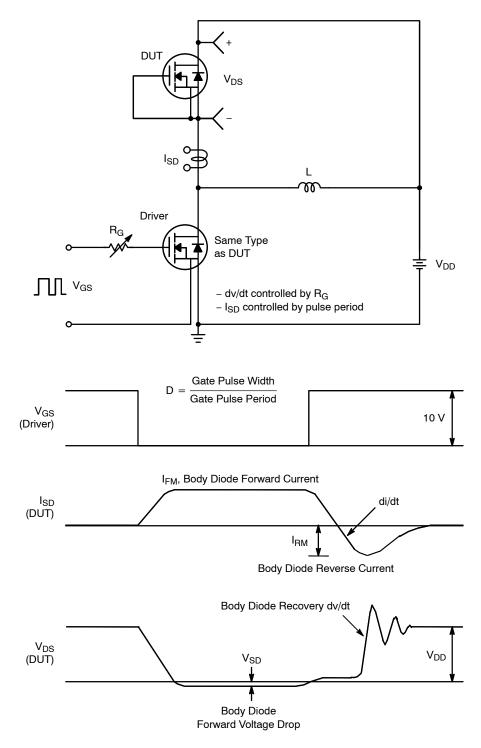
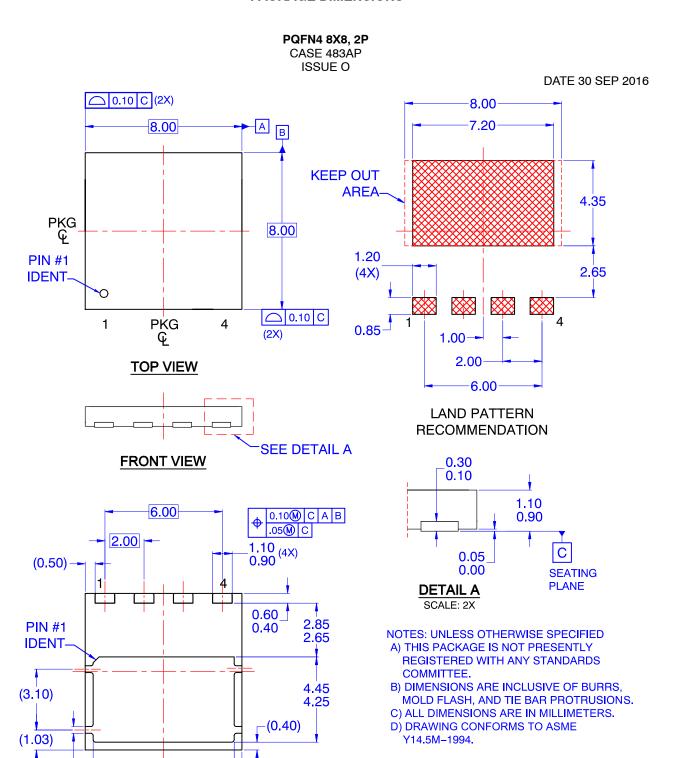


Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms

## **PACKAGE DIMENSIONS**



(0.40)

7,30

7.10

**BOTTOM VIEW** 

(0.35)

(4X)

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