

6th Generation CoolSiC™

650V SiC Schottky Diode

The CoolSiCTM Generation 6 is the leading edge technology from Infineon for the SiC Schottky Barrier diodes. The Infineon proprietary diffusion soldering process is combined with a more compact design, thin-wafer technology and a novel Schottky metal system. The result is a family of products with improved efficiency over all load conditions, resulting from a lower figure of merit ($Q_c \times V_F$). CoolSiCTM Generation 6 has been designed to complement our 600V and 650V CoolMOSTM 7 families, meeting the most stringent application requirements in this voltage range.

Table 1 Key performance parameters

Parameter	Value	Unit
V_{RRM}	650	V
$\overline{Q_C (V_R = 400 \text{ V})}$	12.2	nC
$E_C (V_R = 400 \text{ V})$	2.2	μЈ
$I_F (T_C \le 145 ^{\circ}\text{C}, D = 1)$	8	А
$V_F (I_F = 8 \text{ A}, T_j = 25 \text{ °C})$	1.25	V

Type / ordering Code	Package	Marking
IDH08G65C6	PG-TO220-2	D0865C6

PG-TO220-2 CASE 1) Cathode 2) Anode 1 O O CASE

Features

- Best in class forward voltage (1.25 V)
- Best in class figure of merit $(Q_c \times V_F)$
- High dv/dt ruggedness (150 V/ns)

Benefits

- System efficiency improvement
- System cost and size savings due to the reduced cooling requirements
- · Enabling higher frequency and increased power density

Potential Applications

- Power factor correction in SMPS
- Solar inverter
- Uninterruptible power supply

Product Validation

Qualified for industrial applications according to the relevant tests of JEDEC (J-STD20 and JESD22)



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1 Maximum ratings

Table 3 Maximum ratings

Damamatan	Comple al	Values			Unit		
Parameter	Symbol	Min.	Тур.	Гур. Мах.		Note/Test condition	
	I _F	-	_	8		$T_C \le 145 ^{\circ}\text{C}, D = 1$	
Continuous forward current		-	_	11		$T_C \le 125 ^{\circ}\text{C}, D = 1$	
		-	_	20		$T_C \le 25 ^{\circ}\text{C}, D = 1$	
Surge-repetitive forward current, sine halfwave ¹	$I_{F,RM}$	-	_	35	A	$T_C = 25 ^{\circ}\text{C}, t_p = 10 \text{ms}$	
Surge non-repetitive forward		-	_	47		$T_C = 25 ^{\circ}\text{C}, t_{\rho} = 10 \text{ms}$	
current, sine halfwave	$I_{F,SM}$	_	_	37		$T_C = 150 ^{\circ}\text{C}, t_p = 10 \text{ms}$	
Non-repetitive peak forward current	I _{F,max}	-	-	530		$T_C = 25 ^{\circ}\text{C}, t_p = 10 \mu\text{s}$	
:2+	(:2d+	-	-	11	۸20	$T_C = 25 ^{\circ}\text{C}, t_p = 10 \text{ms}$	
i ² t value	∫i²dt	_	_	6.9	A ² s	$T_C = 150 ^{\circ}\text{C}, t_p = 10 \text{ms}$	
Repetitive peak reverse voltage	V_{RRM}	_	_	650	V	<i>T_c</i> = 25 °C	
Diode dv/dt ruggedness	dv/dt	_	_	150	V/ns	V _R = 0480 V	
Power dissipation	P _{tot}	-	-	63	W	$T_C = 25$ °C, $R_{thJC,max}$	
Operating and storage temperature	T_j T_{stg}	-55	-	175	°C	-	
Mounting torque	_	_	-	70	Ncm	M3 screw	

2 Thermal characteristics

Table 4 Thermal characteristics (PG-TO-220-2)

Davamatar	Complete		Values		Unit	Note/Test condition
Parameter	Symbol	Min.	Тур.	Max.		
Thermal resistance, junction- case	R_{thJC}	_	1.4	2.4	12/\AI	_
Thermal resistance, junction- ambient	R_{thJA}	_	_	62	K/W	leaded
Soldering temperature, wavesoldering only allowed at leads	T_{sold}	_	-	260	°C	1.6 mm (0.063 in.) from case for 10 s

Final Datasheet 3 Rev. 2.0, 2017-05-23

¹ The surge-repetitive forward current test was performed with 1000 pulses (half-wave rectified sine with the 10 ms period).



3 Electrical characteristics

3.1 Static characteristics

Table 5Static characteristics

Da wa wa aka w	Complete	Values			1144	Note/Test condition
Parameter	Symbol	Min.	Тур.	Max.	Unit	Note/Test condition
DC blocking voltage	V_{DC}	650	_	_		<i>T_j</i> = 25 °C
Die de femuerd voltege	17	_	1.25 1.35 V I _F	$I_F = 8 \text{ A}, T_j = 25 ^{\circ}\text{C}$		
Diode forward voltage	V_F	_	1.5	_		$I_F = 8 \text{ A}, T_j = 150 ^{\circ}\text{C}$
Reverse current		_	0.8	27		$V_R = 420 \text{ V}, T_j = 25 \text{ °C}$
	I_R	_	27	_	μΑ	$V_R = 420 \text{ V}, T_j = 125 \text{ °C}$
		_	62	_		$V_R = 420 \text{ V}, T_j = 150 \text{ °C}$

3.2 AC characteristics

Table 6 AC characteristics

Davamatar	Comple of	Values			l lmit	Note/Test Condition
Parameter	Symbol	Min.	Тур.	Max.	Unit	Note/Test Condition
Total capacitive charge	Q_c	-	12.2	-	nC	V_R = 400 V, T_j = 150 °C, di/dt = 200 A/ μ s, $I_F \le I_{F,MAX}$
		-	401	-	$PF = V, f = 1 \text{ MHz},$ $T_{j} = 25 \text{ °C}$ $V_{R} = 300 \text{ V}, f = 1 \text{ MHz},$ $T_{j} = 25 \text{ °C}$ $V_{R} = 600 \text{ V}, f = 1 \text{ MHz},$ $T_{j} = 25 \text{ °C}$, ,
Total capacitance	С	_	24	-		, ,
		_	23	_		, ,



4 Diagrams

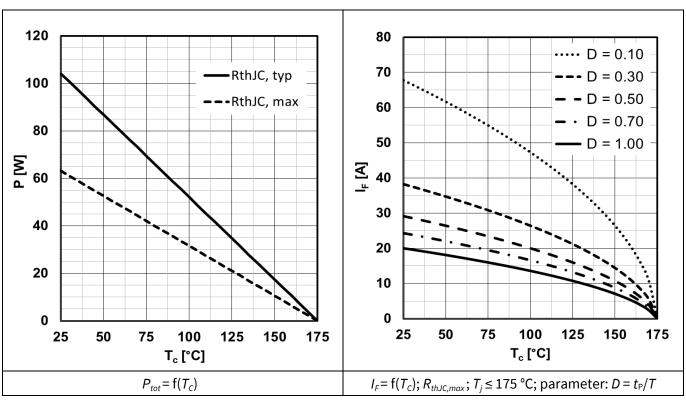


Figure 1 Power dissipation

Figure 2 Max. forward current

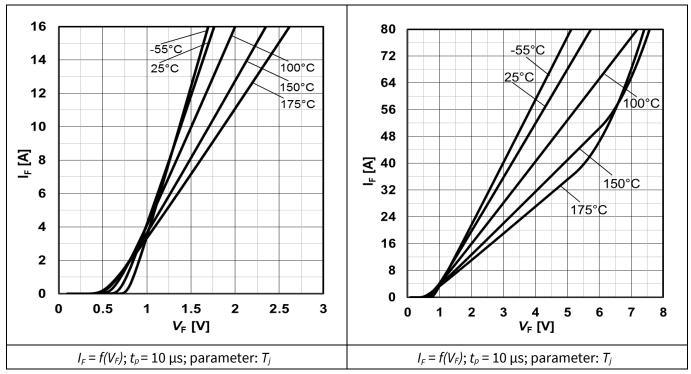


Figure 3 Typ. forward characteristics

Figure 4 Typ. forward characteristics in surge current



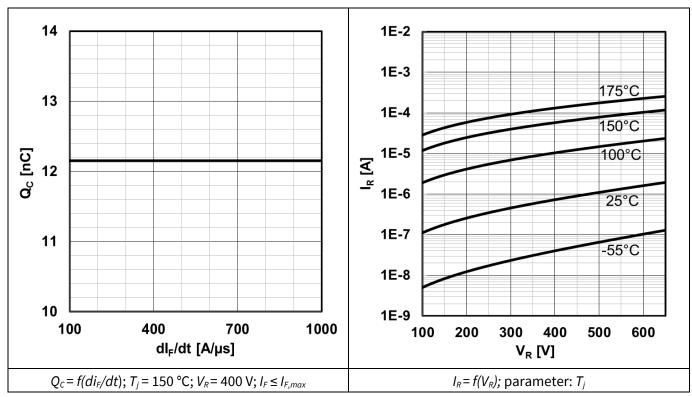


Figure 5 Typ. cap. charge vs. current slope

Figure 6 Typ. reverse current vs. reverse voltage

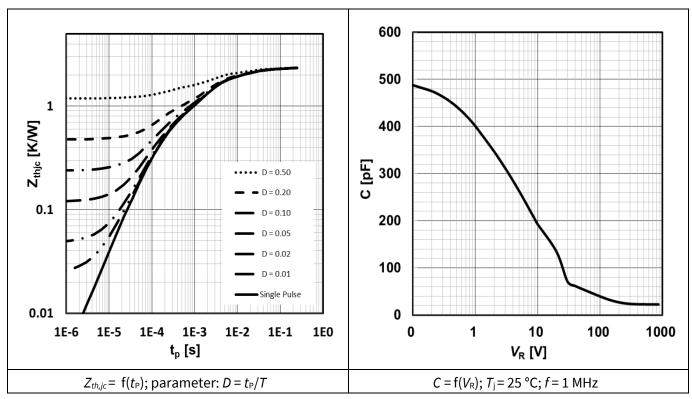


Figure 7 Max. transient thermal impedance

Figure 8 Typ. capacitance vs. reverse voltage



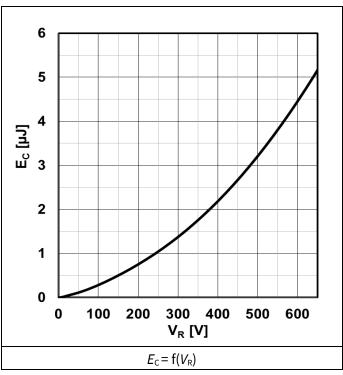


Figure 9 Typ. capacitance stored energy

5 Simplified forward characteristic

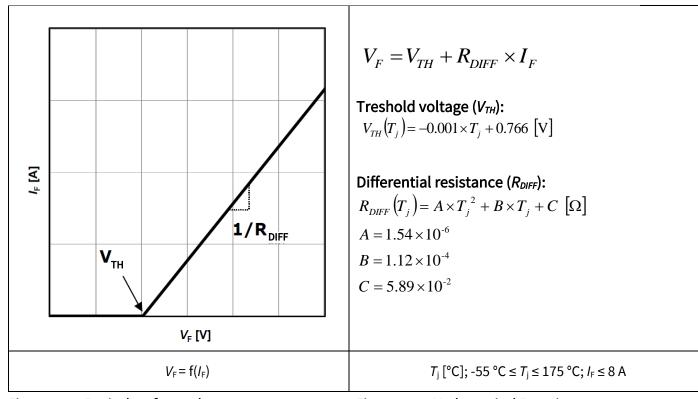


Figure 10 Equivalent forward current curve

Figure 11 Mathematical Equation



6 Package outlines

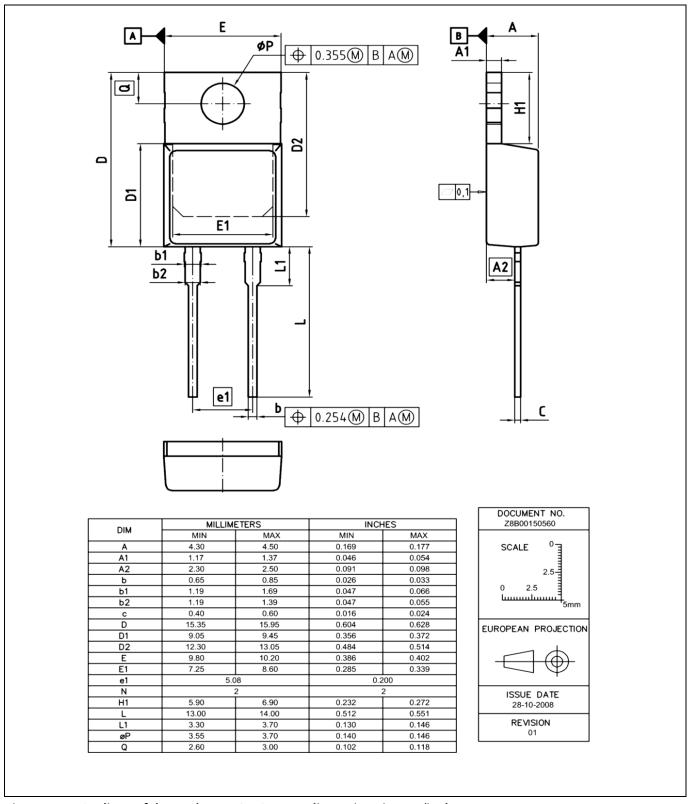


Figure 12 Outlines of the package PG-TO220-2, dimensions in mm/inches

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IDH08G65C6



Revision History

Major changes since the last revision

Revision	Date	Subject (major changes since last revision)
2.0	2017-05-23	Release of final version

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Document reference

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