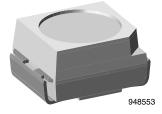
## VSMY385010X01



**Vishay Semiconductors** 

### High Speed Infrared Emitting Diode, 850 nm, Surface Emitter Technology



### **FEATURES**

- Package type: surface-mount
- Package form: PLCC-2
- Dimensions (L x W x H in mm): 3.5 x 2.8 x 1.75
- AEC-Q101 gualified
- Peak wavelength:  $\lambda_p = 850 \text{ nm}$
- High reliability
- · High radiant intensity
- Angle of half intensity:  $\varphi = \pm 60^{\circ}$
- · Suitable for high pulse current operation
- · Floor life: 168 h, MSL 3, according to J-STD-020
- Lead (Pb)-free reflow soldering
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **RELEASED FOR APPLICATIONS**

- Infrared radiation source for operation with CMOS cameras (illumination)
- Automotive sensors
- Light curtain

# DESCRIPTION

As part of the <u>SurfLight<sup>TM</sup></u> portfolio, the VSMY385010X01 is an infrared, 850 nm emitting diode based on surface emitter technology with high radiant intensity, high optical power and high speed, molded in a PLCC-2 package for surface mounting (SMD).

#### BRODUCT SUMMARY

PRODUCT SUMMARY					
COMPONENT	l <sub>e</sub> (mW/sr)	φ (°)	λ <sub>P</sub> (nm)	t <sub>r</sub> (ns)	
VSMY385010X01	12	± 60	850	7	

#### Note

Test conditions see table "Basic Characteristics"

ORDERING INFORMATION	l			
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM	
VSMY385010X01-GS08	Tape and reel	MOQ: 7500 pcs, 1500 pcs/reel	PLCC-2	
VSMY385010X01-GS18	Tape and reel	MOQ: 8000 pcs, 8000 pcs/reel	PLCC-2	

Note

MOQ: minimum order quantity



RoHS COMPLIANT

HALOGEN

FREE **GREEN** 

(5-2008)

## VSMY385010X01



### **Vishay Semiconductors**

ABSOLUTE MAXIMUM RATIN	MUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)			
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Forward current		١ <sub>F</sub>	70	mA
Pulse peak forward current	$t_p/T = 0.5, t_p = 100 \ \mu s$	I <sub>FM</sub>	140	mA
Surge forward current	t <sub>p</sub> = 100 μs	I <sub>FSM</sub>	0.5	A
Power dissipation		Pv	140	mW
Junction temperature		Тj	110	°C
Operating temperature range		T <sub>amb</sub>	-40 to +105	°C
Storage temperature range		T <sub>stg</sub>	-40 to +110	°C
Soldering temperature	According to Fig. 7, J-STD-020	T <sub>sd</sub>	260	°C
Thermal resistance junction-to-ambient	JESD51	R <sub>thJA</sub>	250	K/W

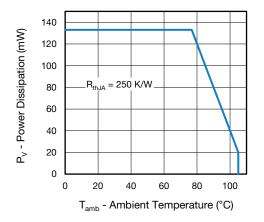
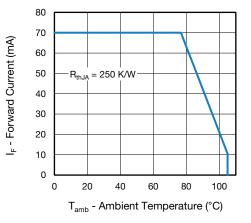


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature





PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
	I <sub>F</sub> = 50 mA, t <sub>p</sub> = 20 ms	V <sub>F</sub>	-	1.6	1.8	V
orward voltage	I <sub>F</sub> = 70 mA, t <sub>p</sub> = 20 ms	V <sub>F</sub>	-	1.65	-	V
	$I_F = 0.5 \text{ A}, t_p = 100 \ \mu \text{s}$	V <sub>F</sub>	-	2.8	-	V
Temperature coefficient of V <sub>F</sub>	I <sub>F</sub> = 70 mA	TK <sub>VF</sub>	-	-1.3	-	mV/K
Reverse current		I <sub>R</sub>	Not designed for reverse operation		μA	
Junction capacitance	V <sub>R</sub> = 0 V, f = 1 MHz, E = 0	Cj	-	30	-	pF
Radiant intensity	I <sub>F</sub> = 50 mA, t <sub>p</sub> = 20 ms	l <sub>e</sub>	5.5	8	11	mW/sr
	I <sub>F</sub> = 70 mA, t <sub>p</sub> = 20 ms	l <sub>e</sub>	-	12	-	mW/sr
	$I_F = 0.5 \text{ A}, t_p = 100 \ \mu \text{s}$	l <sub>e</sub>	-	70	-	mW/sr
Radiant power	I <sub>F</sub> = 70 mA, t <sub>p</sub> = 20 ms	фе	-	40	-	mW
Temperature coefficient of $\phi_{\text{e}}$	I <sub>F</sub> = 70 mA	ΤKφ <sub>e</sub>	-	-0.2	-	%/K
Angle of half intensity		φ	-	± 60	-	0
Peak wavelength	I <sub>F</sub> = 50 mA	λρ	840	850	870	nm
Spectral bandwidth	I <sub>F</sub> = 70 mA	Δλ	-	35	-	nm
Temperature coefficient of $\lambda_p$	I <sub>F</sub> = 70 mA	ΤΚλ <sub>ρ</sub>	-	0.25	-	nm/K
Rise time	I <sub>F</sub> = 70 mA, 10 % to 90 %	t <sub>r</sub>	-	7	-	ns
Fall time	I <sub>F</sub> = 70 mA, 10 % to 90 %	t <sub>f</sub>	-	6	-	ns

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Document Number: 84590

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## VSMY385010X01





#### BASIC CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

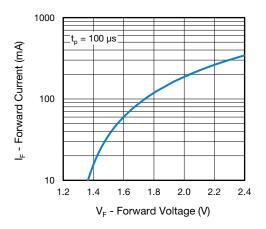


Fig. 3 - Forward Current vs. Forward Voltage

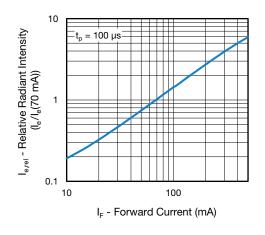


Fig. 4 - Relative Radiant Intensity vs. Forward Current

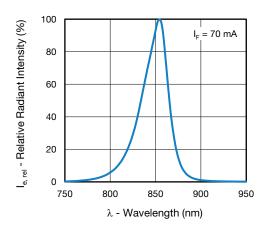


Fig. 5 - Relative Radiant Power vs. Wavelength

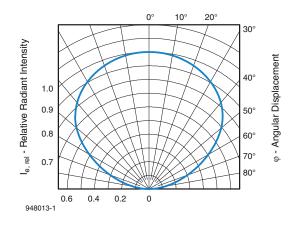


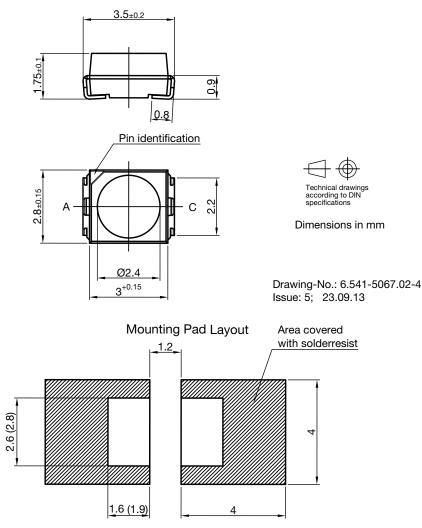
Fig. 6 - Relative Radiant Intensity vs. Angular Displacement



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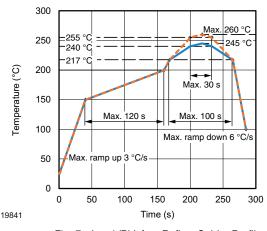
### **Vishay Semiconductors**

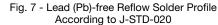
#### **PACKAGE DIMENSIONS** in millimeters



Dimensions: Reflow and vapor phase (wave soldering)

#### SOLDER PROFILE





#### DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

#### **FLOOR LIFE**

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 168 h

Conditions: T<sub>amb</sub> < 30 °C, RH < 60 %

Moisture sensitivity level 3, according to J-STD-020

#### DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-033D or label. Devices taped on reel dry using recommended conditions 192 h at 40 °C (+ 5 °C), RH < 5 %.

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#### TAPE AND REEL

PLCC-2 components are packed in antistatic blister tape (DIN IEC (CO) 564) for automatic component insertion. Cavities of blister tape are covered with adhesive tape.

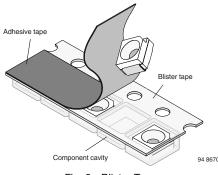


Fig. 8 - Blister Tape

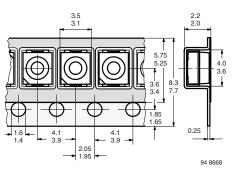


Fig. 9 - Tape Dimensions in mm for PLCC-2

#### **MISSING DEVICES**

A maximum of 0.5 % of the total number of components per reel may be missing, exclusively missing components at the beginning and at the end of the reel. A maximum of three consecutive components may be missing, provided this gap is followed by six consecutive components.

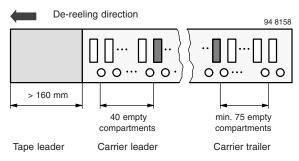


Fig. 10 - Beginning and End of Reel

### **Vishay Semiconductors**

The tape leader is at least 160 mm and is followed by a carrier tape leader with at least 40 empty compartments. The tape leader may include the carrier tape as long as the cover tape is not connected to the carrier tape. The least component is followed by a carrier tape trailer with a least 75 empty compartments and sealed with cover tape.

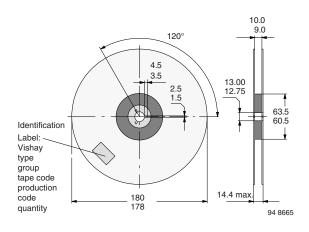


Fig. 11 - Dimensions of Reel-GS08

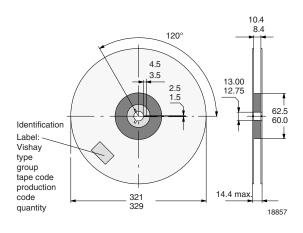


Fig. 12 - Dimensions of Reel-GS18

#### COVER TAPE REMOVAL FORCE

The removal force lies between 0.1 N and 1.0 N at a removal speed of 5 mm/s. In order to prevent components from popping out of the blisters, the cover tape must be pulled off at an angle of 180° with regard to the feed direction.

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