SPEC

Spec No.	TQ3C-8EAF0-E1DDM16-01
Date	August 7, 2008

TYPE: TCG062HVLBC-G20

< 6.2 inch HVGA transmissive color TFT with LED backlight, Constant current circuit for LED backlight and Touch panel>

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KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

This specification is subject to change without notice.

Consult Kyocera before ordering.

Original	Designed by: 1	Engineering de	Confirmed by: QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved
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Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.



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Revision record

	Revision record						
	Date	Design	ed by :	Engineering of	dept.	Confirmed by	: QA dept.
	Date	Prepa	ared	Checked	Approved	Checked	Approved
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Rev.No.	Date	Page	Descriptions				
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		8	8 8-1. Timing characteristics ~Delete () ~Change "H _{SYNC} -V _{SYNC} signal phase difference"				
		10	9. Backlight characteristics Delete ()				
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1. Application

This document defines the specification of TCG062HVLBC-G20. (RoHS Compliant)

2. Construction and outline

LCD : Transmissive color dot matrix type TFT

Backlight system : LED

Polarizer : Glare treatment

Additional circuit : Timing controller, Power supply (3.3V input)

(with Constant current circuit for LED backlight)

Touch panel : Analog type(Glass/Glass), Glare Anti-reflection treatment

3. Mechanical specifications

3-1. Mechanical specifications of LCD

Item	Specification	Unit
Outline dimensions	173 (W)× 70 (H) × 8.9Max (D)	mm
Active area	147.84 (W) × 55.44 (H) (15.8cm / 6.2 inch (Diagonal))	mm
Effective viewing area	149.8 (W) × 57.4 (H)	mm
Dot format	640×(B,G,R) (W) × 240 (H)	dot
Dot pitch	0.077 (W) × 0.231 (H)	mm
Base color *1	Normally White	-
Mass	145	g

^{*1} Due to the characteristics of the LCD material, the color varies with environmental temperature.

3-2. Mechanical specifications of touch panel

Item	Specification	
Input	Radius-0.8 stylus or Finger	
Actuation Force	0.1 ~ 2.0	N
Transmittance	Typ. 85	%
Surface hardness	Pencil hardness 2H or more according	-



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4. Absolute maximum ratings

4-1. Electrical absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Supply voltage for logic	$V_{ m DD}$	0	4.0	V
Input signal voltage *1	$V_{\rm IN}$	-0.3	6.0	V
Supply voltage for backlight	$V_{\rm IN}B$	0	6.0	V
Backlight ON-OFF	BLEN	0	$ m V_{IN}B$	V
Brightness adjust voltage	VBRT	0	$ m V_{IN}B$	V
Supply voltage for touch panel	V_{TP}	0	6.0	V
Input current of touch panel	I_{TP}	0	0.5	mA

^{*1} Input signal: CK, R0 ~ R5, G0 ~ G5, B0 ~ B5, H_{SYNC}, V_{SYNC}, ENAB, R/L, U/D

4-2. Environmental absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Operating temperature	*1	T_{OP}	-20	70	$^{\circ}\mathrm{C}$
Storage temperature	*2	Тѕто	-30	80	$^{\circ}\mathrm{C}$
Operating humidity	*3	Нор	10	*4	%RH
Storage humidity	*3	H_{STO}	10	*4	%RH
Vibration		-	*5	*5	-
Shock		-	*6	*6	-

^{*1} Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.

Store LCD panels at normal temperature/humidity. Keep them free from vibration and shock. An LCD panel that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard.

(Please refer to "Precautions for Use" for details.)

Temp. > 40°C, Absolute humidity shall be less than 85%RH at 40°C.

*5

Frequency	10 ~ 55 Hz	Acceleration value
Vibration width	0.15mm	$(0.3 \sim 9 \text{ m/s}^2)$
Interval	10-55-10	Hz 1 minutes

2 hours in each direction X, Y, Z (6 hours total)

EIAJ ED-2531

*6 Acceleration: 490 m/s2, Pulse width: 11 ms

3 times in each direction: $\pm X$, $\pm Y$, $\pm Z$

EIAJ ED-2531



^{*2} Temp. = -30°C < 48h, Temp. = 80°C < 168h

^{*3} Non-condensing

^{*4} Temp. 40°C, 85%RH Max.

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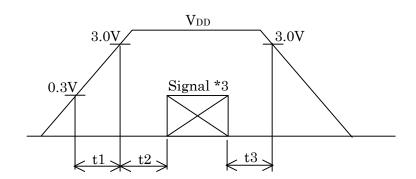
5. Electrical characteristics

5-1. LCD

Temp. = $-20 \sim 70$ °C

					10111p. =	
Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Supply voltage for logic *1	$V_{ m DD}$	-	3.0	3.3	3.6	V
Current consumption for logic	${ m I}_{ m DD}$	*2	-	250	330	mA
Permissive input ripple voltage	V_{RP}	-	-	-	100	mVp-p
T , 1 1 40	$ m V_{IL}$	"Low" level	0	-	$0.3V_{\mathrm{DD}}$	V
Input signal voltage *3	V _{IH}	"High" level	$0.7 V_{\mathrm{DD}}$	-	V_{DD}	V

*1 $V_{\rm DD}$ -turn-on conditions



0 < t1 20ms

0 < t2 50 ms

 $0 \le t3$ 1s

*2 Display pattern:

$$V_{DD}$$
 = 3.3V, Temp. = 25°C

123 456 789 • • • • • • • • • • • • • • • • • 1918 1919 1920 (dot)

 $\frac{1}{2}$

3

:

239

240

(dot)

*3 Input signal : CK, R0 ~ R5, G0 ~ G5, B0 ~ B5, Hsync, Vsync, ENAB, R/L, U/D

5-2. Touch panel

Item	Specification		
Supply voltage for touch panel	5.0V		
m · 1 · .	$xL \sim xR : 700\Omega \sim 2,100\Omega$		
Terminal resistance	$yU \sim yL : 100\Omega \sim 500\Omega$		
Linearity	less than $\pm 2.5\%$		
Insulation resistance	$(50\mathrm{M})\Omega$ or more at DC25V		



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6. Optical characteristics

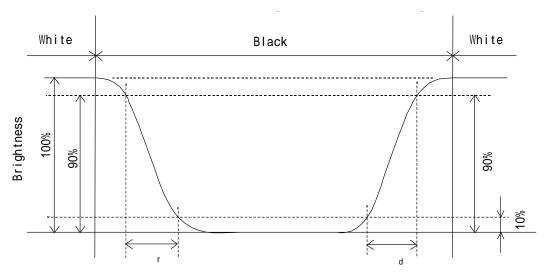
Measuring spot = 6.0mm, Temp. = 25°C

Item		Symbol	Condition	Min.	Тур.	Max.	Unit	
D	Rise	τr	= =0°	-	10	-	ms	
Response time	Down	τd	= =0°	-	15	-	ms	
T7: 1		UPPER		-	65	-	1	
Viewing angle View direction		LOWER	CR 5	-	80	-	deg.	
: 6 o'cloc		LEFT	CR 5	-	80	-	1	
(Gray in	version)	ϕ right		-	80	-	deg.	
Contrast ratio	Contrast ratio		= =0°	300	500	-	-	
Brightness		L	IF=15mA/Line	175	255	-	cd/m²	
	Red	X	= =0°	0.55	0.60	0.65		
		У		0.31	0.36	0.41		
	C	x		0.31	0.36	0.41		
Chromaticity	Green	= =0°	0.52	0.57	0.62	_		
coordinates	D1	x	= =0°	0.10	0.15	0.20	-	
	Blue	У	0	0.08	0.13	0.18		
	White	X	= =0°	0.28	0.33	0.38		
	wnite	у	− − 0	0.30	0.35	0.40		

6-1. Definition of contrast ratio

CR(Contrast ratio) = Brightness with all pixels "White"
Brightness with all pixels "Black"

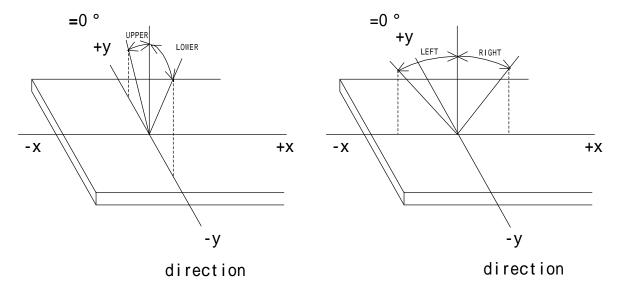
6-2. Definition of response time



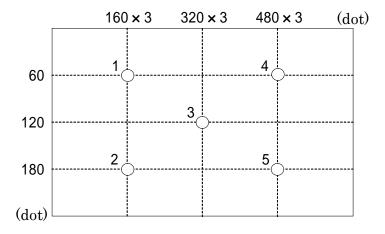


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6-3. Definition of viewing angle



6-4. Brightness measuring points



- 1) Rating is defined on the average in the viewing area.
- 2) Measured 30 minutes after the LED is powered on. (Ambient temp. = 25°C)



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7. Interface signals

7-1. Pin assignment of LCD panel and LED $\,$

No.	Symbol	Description	I/O	Note
1	GND	GND	-	
2	CK	Clock signal for sampling each data signal	I	
3	Hsync	Horizontal synchronous signal (negative)		
4	$V_{ m SYNC}$	Vertical synchronous signal (negative)		
5	GND	GND	-	
6	R0	RED data signal (LSB)	I	
7	R1	RED data signal	I	
8	R2	RED data signal	I	
9	R3	RED data signal	I	
10	R4	RED data signal	I	
11	R5	RED data signal (MSB)	I	
12	GND	GND	-	
13	G0	GREEN data signal (LSB)	I	
14	G1	GREEN data signal	I	
15	G2	GREEN data signal	I	
16	G3	GREEN data signal	I	
17	G4	GREEN data signal	I	
18	G5	GREEN data signal (MSB)	I	
19	GND	GND	-	
20	В0	BLUE data signal (LSB)	I	
21	B1	BLUE data signal	I	
22	B2	BLUE data signal	I	
23	В3	BLUE data signal	I	
24	B4	BLUE data signal	I	
25	B5	BLUE data signal (MSB)	I	
26	GND	GND	-	
27	ENAB	Signal to settle the horizontal display position (positive)	I	*1
28	$V_{ m DD}$	3.3V power supply	-	
29	$V_{ m DD}$	3.3V power supply	-	
30	R/L	Horizontal display mode select signal H: Normal , L: Left / Right reverse mode	I	*2
31	U/D	Vertical display mode select signal H : Normal , L : Up / Down reverse mode	I	
32	NC	No connect	-	
33	$V_{\rm IN} B$	Power supply for LED backlight	-	
34	$V_{\rm IN}B$	Power supply for LED backlight	-	
35	$V_{\rm IN} B$	Power supply for LED backlight	-	
36	BLEN	Backlight ON-OFF (H:ON, L:OFF)	-	
37	VBRT	Brightness adjust voltage	-	
38	GNDB	GND for LED backlight		
39	GNDB	GND for LED backlight	-	
40	GNDB	GND for LED backlight	-	

LCD connector : IMSA-9681S-40A-GF (IRISO)

Recommended matching FFC or FPC : 0.5mm pitch

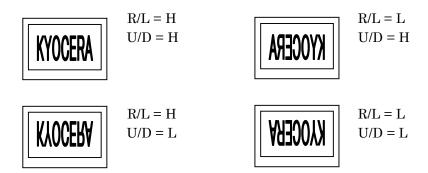


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^{*1} The horizontal display start timing is settled in accordance with a rising timing of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined.

Don't keep ENAB "High" during operation.

*2



7-2. Pin assignment of touch panel

No.	Symbol	Description
1	xR	x-Right terminal
2	уL	y-Lower terminal
3	хL	x-Left terminal
4	уU	y-Upper terminal

Touch panel side connector : 1.0mmpitch

Recommended matching connector : Series 9616 (IRISO)

: Series 9610 (IRISO): Series FMS (JST)



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8. Input timing characteristics

8-1. Timing characteristics

	Symbol	Min	Тур	Max	Unit	Note	
Clock	Frequency	1/Tc	1	25.18	28.33	MHz	
Clock	Duty ratio	Tch/Tc	40	50	60	%	
Data	Set up time	Tds	5	-	-	ns	
Data	Hold time	Tdh	10	-	-	ns	
	Crolo	TH	30.0	31.8	-	μs	
Horizontal sync. signal	Cycle	1П	770	800	900	clock	
organi.	Pulse width	ТНр	2	96	200	clock	
Vertical sync.	Cycle	TV	515	525	560	line	
signal	Pulse width	TVp	2	-	34	line	
Horizontal displa	ny period	THd		640		clock	
H _{SYNC} – Clock phase difference		ТНс	10	-	Tc-10	ns	
H _{SYNC} - V _{SYNC} signal phase difference		TVh	Тс	-	ТН-ТНр	ns	
Vertical sync. sig	TVs	34			line		
Vertical display	period	TVd	240		line		

^{*}In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

8-2. Horizontal display position

The horizontal display position is determined by ENAB signal.

Item		Symbol	Min	Typ	Max	Unit	Note
Enable simuel	Set up time	Tes	5	-	Tc-10	ns	
Enable signal	Pulse width	Tep	2	640	TH-10	clock	
H _{SYNC} – Enable signal phase difference		The	44	-	TH-664	clock	

^{*} When ENAB is fixed at "Low", the display starts from the data of C104(clock) as shown in 8-5.

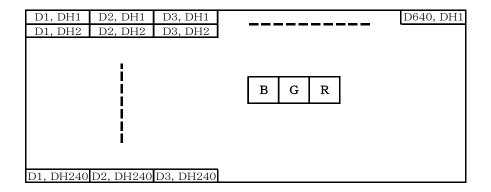


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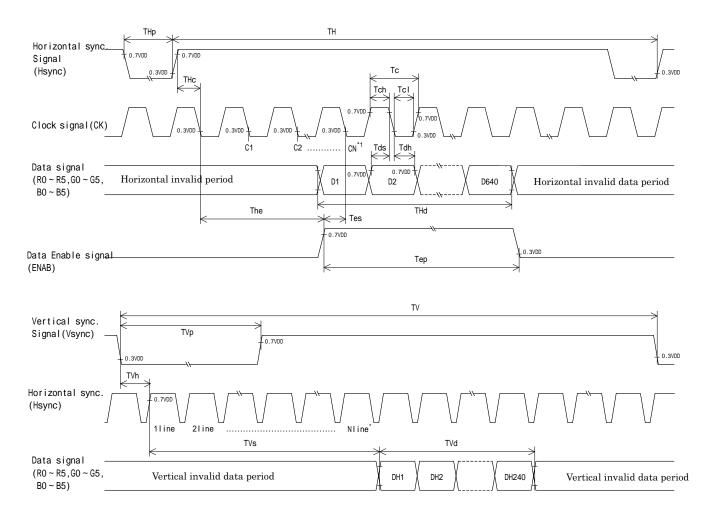
8-3. Vertical display position

The vertical display position (TVs) is fixed at 34th line. Note) ENAB signal is independent of vertical display position.

8-4. Input Data Signals and Display position on the screen



8-5. Input timing characteristics



- *1 When ENAB is fixed at "Low", the display starts from the data of C104 (clock).
- *2 The vertical display position(TVs) is fixed at 34 (line).



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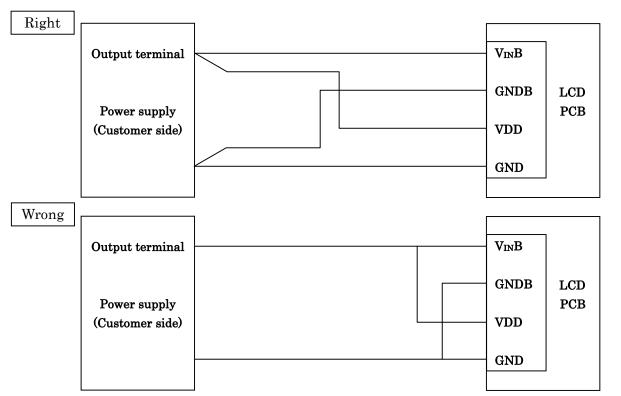
9. Backlight characteristics

Temp.=25

Item	Symbol	Min.	Тур.	Max.	Unit	Note
Supply voltage	$V_{\rm IN} B$	3.0	-	5.5	V	Ta=-20 ~ 70°C
ON-OFF (H)	BLEN	$0.8~\mathrm{V_{IN}B}$	-	$V_{\rm IN} B$	V	-
ON-OFF (L)	BLEN	0	-	$0.2 \mathrm{V_{IN}B}$	V	-
LED forward current *1, *2	IF	14	15	16	mA	VBRT=0 ~ 1.4V
LED forward current "1, "2		2.8	3.0	3.2		VBRT=2.8V
Complex company	ID	-	500	650	mA	V _{IN} B=3.3V,IF=15mA
Supply current	$I_{IN}B$	-	320	420	mA	V _{IN} B=5.0V,IF=15mA
Operating life time *3, *4	Т	-	40,000	-	h	IF=15mA, Ta=25

^{*1} For each "LED"

^{*} In case VDD and V_{IN}B are supplied by a single power source, VDD & V_{IN}B, and GND are connected directly and separately from the output on the power source. If the common wire are used for VDD & V_{IN}B, and for GND, and are split near the PCB, and connect to each LCD driving circuit and backlight driving circuit, a flicker might be occurred due to a ripple between the both circuit.





^{*2} An input current below 5.0mA may reduce the brightness uniformity of the LED backlight. This is because the amount of light from each LED chip is different. Therefore, please evaluate carefully before finalizing the input current.

^{*3} When brightness decrease 50% of initial brightness.

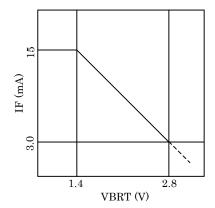
^{*4} Life time is estimated data. (Condition: IF=15mA, Ta=25 in chamber)

^{*} When you start-up, please charge in sequence of V_{IN}B->BLEN, or VBRT. When you shut-down, please stop in sequence of BLEN and/or VBRT->V_{IN}B.

^{*} Please do not connect the other than our backlight to this output connector on the PCB.

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^{*} VBRT-IF characteristics



10. Design guidance for analog touch panel (T/P)

10-1 Electrical (In customer's design, please remember the following considerations.)

- 1 Do not use the current regulated circuit.
- 2 Keep the current limit with top and bottom layer. (Please refer to "Electrical absolute maxim um ratings" for details.)
- 3 Analog T/P can not sense two points touching separately.
- 4 A contact resistance is appeared at the touch point between top and bottom layer. After this resistance has stable read of the T/P position data.
- 5 Because noise of inverter or peripheral circuits may interfere signal of touch panel itself it is necessary to design carefully in advance to avoid these noise problem.

10-2 Software

- 1 Do the "User Calibration".
- 2 "User Caribration" may be needed with long term using. Include "User Caribration" menu in your software.
- 3 When drawing a line with a stylus, there may be a slight discontinuity when the stylus passes over a spacer-dot. If necessary, please provide a compensation feature within your software.

10-3 Mounting on display and housing bezel

- 1 Do not use an adhesive tape to bond it on the front of T/P and hang it to the housing bezel.
- 2 This touch panel has an airtight but not watertight structure. Please not to use it for the applications requiring watertight or under the environments occurred condensation. If it is expected to be exposed to the environments that vapor, moisture or other liquids may seep inside a bezel, please be sure to take some measurements for drip-proof or waterproof by using sealing materials on the bezel.



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11. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.

No1. - No5. above indicate

- 1. Year code
- 2. Month code
- 3. Date
- 4. Version Number
- 5. Country of origin (Japan or China)

Year	2008	2009	2010	2011	2012	2013
Code	8	9	0	1	2	3

Mont	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	X	Y	Z

12. Warranty

12-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

12-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.



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13. Precautions for use

13-1. Installation of the LCD

- 1) The LCD shall be installed so that there is no pressure on the LSI chips.
- 2) The LCD shall be installed flat, without twisting or bending.
- 3) Please design the housing window so that its edges are between the active area and the effective area of the LCD screen.
 - Must maintain a gap between inside of bezel and touch panel to avoid malfunction or electrode damage of touch panel.
- 4) A transparent protection sheet is attached to the touch panel. Please remove the protection film slowly before use, paying attention to static electricity.

13-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.

13-3. LCD operation

1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.

13-4. Storage

- 1) The LCD shall be stored within the temperature and humidity limits specified. Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

13-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) Do not push or rub the touch panel's surface with hard to sharp objects such as knives, or the touch panel may be scratched.
- 3) When the touch panel is dirty, gently wipe the surface with a soft cloth, sometimes moistened by mild detergent or alcohol. If a hazardous chemical is dropped on the touch panel by mistake, wipe it off right away to prevent human contact.
- 4) Touch panel edges are sharp. Handle the touch panel with enough care to prevent cuts
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not disassemble LCD module because it will result in damage.
- 7) This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 8) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 9) Liquid crystal may leak when the module is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.



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14. Reliability test data

Test item	Test condition	Test time	Judger	nent
High temp. atmosphere	80°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Low temp. atmosphere	-30°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function Display quality Current consumption	: No defect: No defect: No defect
High temp. operation	70°C	500h	Display function Display quality Current consumption	: No defect : No defect : No defect
Point Activation life	Polyacetal stylus R4, Hardness 60° Hitting force 2.9N Hitting speed 5 time/s	one million times	Terminal resistance Insulation resistance Linearity Actuation Force	No defectNo defectNo defectNo defect

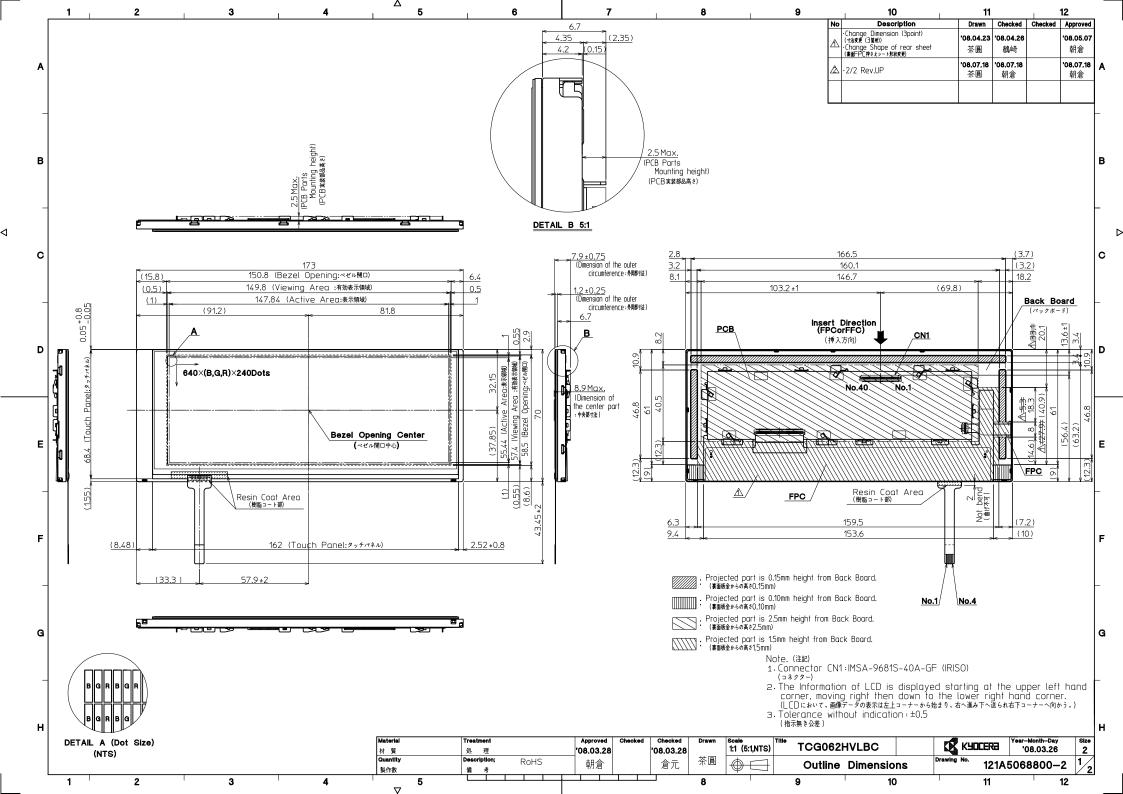
^{*} Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

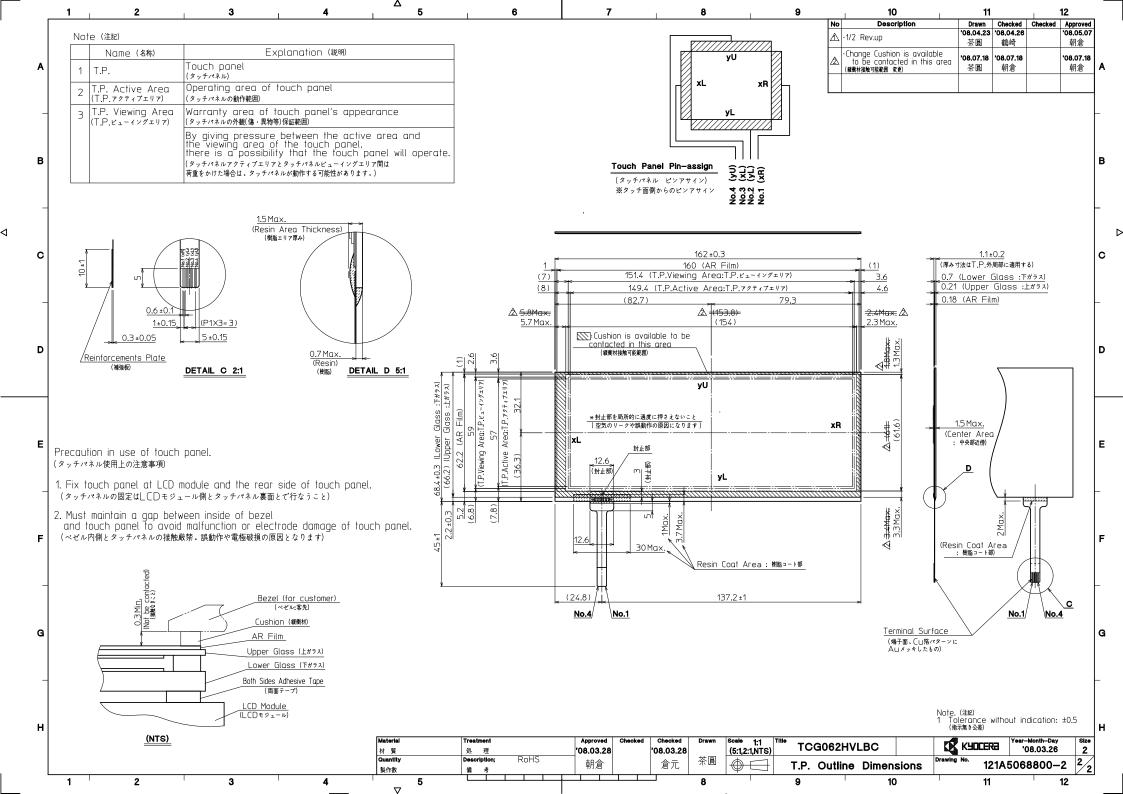


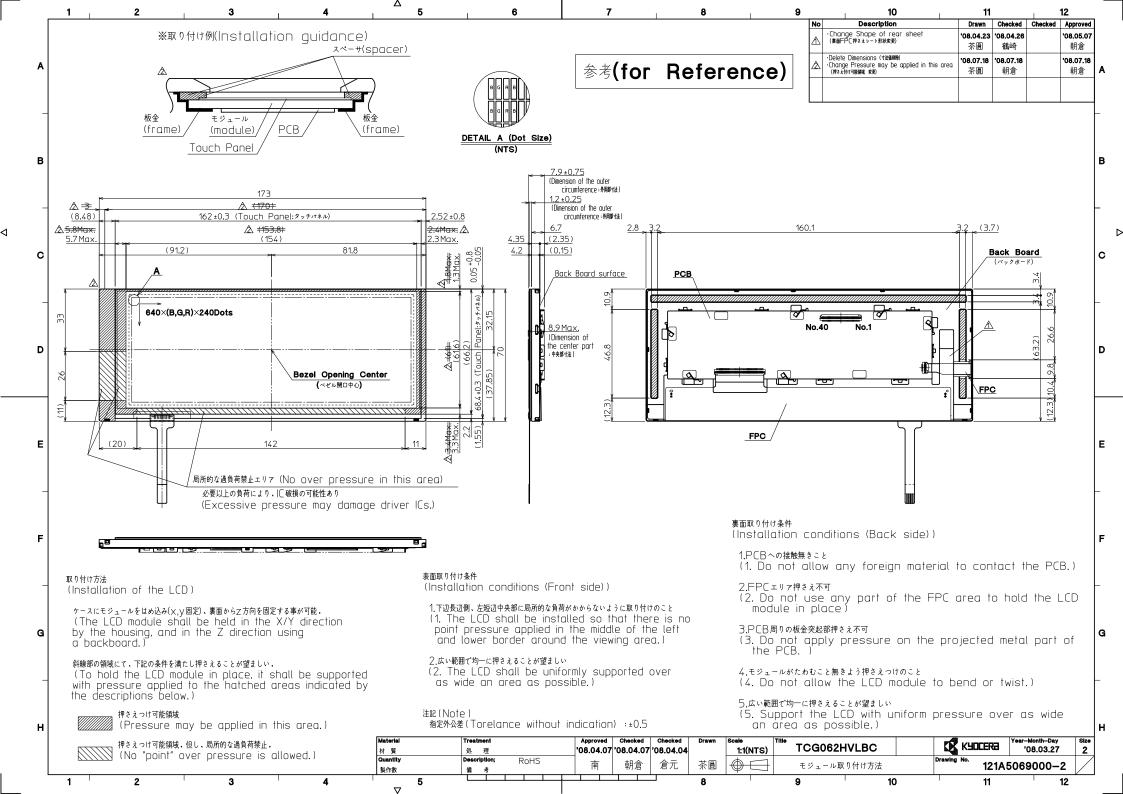
^{*} The LCD is tested in circumstances in which there is no condensation.

^{*} The reliability test is not an out-going inspection.

^{*} The result of the reliability test is for your reference purpose only. The reliability test is conducted only to examine the LCD's capability.







Spec No.	TQ3C-8EAF0-E2DDM16-01
Date	August 7, 2008

KYOCERA INSPECTION STANDARD

TYPE: TCG062HVLBC-G20

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

Original	Designed by:	Engineering de	Confirmed by : QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved
April 3, 2008	7d Topusor	W. Yamo	lf. Matriemoto	S. Hajoshi	Jo , Suf



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Revision record

	Revision record							
llate —				Engineering of	lept.	Confirmed by		
		Prepa	ared	Checked	Approved	Checked	Approved	
Λ,,	g. 7, 2008		. \			_ 1	2 1 1	
Au	g. 1, 2000	79. Ja	humori	W. Yano	1. Matsumoto	S. Hyposhi	To . Sur	
Rev.No.	Date	Page			Description	ons		
01	Aug.7,2008	3	Scratch, Froeign particle(Touch screen portion)					
				inge "Judgeme	nt standard"			
				on's ring				
			Cha	inge comment				
		1						



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Visuals specification

1) Note

1) Note			Note					
C 1	1 0	1	Note					
General			not defined within this inspection standard shall be					
	reviewed by Kyocera, and an additional standard shall be determined by mutual consent.							
		=						
	the active area and shall not be applicable to outside of the area.							
	3. Inspection conditions							
	Lumina	ance	: 500 Lux min.					
	Inspect	ion distance	: 300 mm.					
	Temper	rature	$:25 \pm 5$					
	Direction	on	: Directly above					
Definition of	Dot defect	Bright dot defect	The dot is constantly "on" when power applied to the					
inspection item			LCD, even when all "Black" data sent to the screen.					
			Inspection tool: 5% Transparency neutral density filter.					
			Count dot: If the dot is visible through the filter.					
			Don't count dot: If the dot is not visible through the					
			filter.					
			RGBRGB					
			R G B R G B R G B dot defect					
			R G B R G B B G B G G G G G G G G G G G					
		Black dot defect	The dot is constantly "off" when power applied to the					
			LCD, even when all "White" data sent to the screen.					
		Adjacent dot	Adjacent dot defect is defined as two or more bright dot					
			defects or black dot defects.					
			RGBRGBRGB					
			R G B R G B R G B dot defect					
			RGBRGB					
	External	Bubble, Scratch,	Visible operating (all pixels "Black" or "White") and non					
	inspection	Foreign particle	operating.					
		(Polarizer, Cell,						
		Backlight)						
		Appearance	Does not satisfy the value at the spec.					
		inspection						
	Others	LED wire	Damaged to the LED wire, connector, pin, functional					
			failure or appearance failure.					
	Definition	Definition of	circle size Definition of linear size					
	of size							
			\ _ 					
		/ ⁴↓						
		d =(a +	b)/2					



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2) Standard

2) Standa:		_				_		
	ication		tion item	Judgement standard				
Defect	Dot	Bright dot	defect	Acceptable number	: 4			
(in LCD	defect			Bright dot spacing			or more	
glass)		Black dot	defect	Acceptable number	: 5			
			1	Bright dot spacing	: 5 mm		or more	
		2 dot join	Bright dot	Acceptable number	: 2			
			defect	receptable frameer	· 2			
			Black dot defect	Acceptable number	: 3			
		3 or more	dots join	Acceptable number		: 0		
		Total dot d	lefects	Acceptable number		: 5 Max	X	
	Others	White dot,	Dark dot	•				
		(Circle)		Size (mm	1)	Ac	ceptable number	
				d	0.2		(Neglected)	
				0.2 < d	0.4		5	
				0.4 < d	0.5		3	
				0.5 < d			0	
External	inspection	Polarizer ((Saratah)					
(Defect on		1 Olarizer (Scrawii)	Width (mm)	Longth (mm)	Acceptable number	
Polarizer				Width (min) W 0.1	Length (mm)		(Neglected)	
between H				T.		5.0 (Neglected)		
and LCD				0.1 < W 0.3	5.0 < L		0	
and LCD	giass)			0.3 < W	-		0	
		Polarizer (Bubblo)			*		
		1 Olarizer (Dubble)	Size (mm	,)	Λο	ceptable number	
				d 0.2		(Neglected)		
				0.2 < d	0.3		5	
				0.3 < d	0.5		3	
				0.5 < d			0	
		Foreign pa	rticle					
				Size (mm	ceptable number			
		(Circular shape)		d 0.2		(Neglected)		
				0.2 < d	0.4		5	
				0.4 < d	0.5		3	
				0.5 < d			0	
		Foreign pa	untiala					
				W. 1/1 / \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \		()	<u> </u>	
		(Linear s	nape)	Width (mm) W 0.03	Length	(mm)	Acceptable number (Neglected)	
		Scratch		VV 0.03	L	2.0	(Neglected)	
				0.03 < W = 0.1	2.0 < L	4.0	(Negrected)	
					4.0 < L	1.0	0	
				0.1 < W	1.0 · H		(According to	
							circular shape)	



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Inspection item		Jı	udgemer	nt stand	ard			
Scratch,	(W = Width, L = Length, D = Diameter = $\frac{\text{major axis} + \text{minor axis}}{2}$)							
Foreign particle							ceptable number	
(Touch screen	100111	W	0.03	L	20	1100	Neglected	
portion)	-	0.03 < W	0.05	L	10	2рс	es within φ20mm	
	Scratch	0.05 < W	0.08	L	6		es within φ20mm	
		0.08 < W 0.1		L	4		es within φ30mm	
	Foreign	W	0.05	Negle	ected	1	Neglected	
	(line like)	0.05 < W	0.1	L	5	2pce	es within φ30mm	
	Foreign		D	0.2			Neglected	
	(circle like)	0.2	< D	0.3		2pce	es within φ30mm	
	Above are app	lied to the visib	le area.			•		
	Unless there	are foreign pa	article a	ınd dan	nage af	fecte	d seriously to the	
	electrical perfe	ormance out of	the activ	e area,	we appi	ove o	f this product.	
Glass crack	T,		g: (`			Acceptable	
(Touch screen	Item		Size (m	im)			number	
portion)				1	X	3		
	Conner	\	/ 1 ^z	·/	**		9	
	crack				Y	3	2 pcs /panel	
	Crack						/panei	
				Z	< t			
	Crack in				X	5		
	other area				V	1 5	2 pcs	
	than in				Y	1.5	/side	
	corner	2			\mathbf{z}	< t		
		/ †						
					/			
			_	/	//			
	Progressive	_		V,			0 pcs	
	crack		\searrow				(NG even 1pcs)	
			\sim $ $					
		•	•					
	Above are app	lied to the visib	le area.					
	Unless there	are foreign p	article a	and dar	nage a	ffecte	d seriously to the	
	electrical perfo	rmance out of t	he activ	e area, v	ve appr	ove of	f this product.	
Noveton's ring	All Noveton Di-	aga in the corte	n of the	201002	anat ha	noice	end	
Newton's ring		ngs in the cente			nust be	reject	ı∈u.	
	Border around the screen are permitted.							
			NG				OK	
			_					

